



17 June 2021

Product data sheet

1. General description

NPN/NPN general-purpose transistor pair in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package.

PNP/PNP complement: BC857BS-Q

2. Features and benefits

- Low collector capacitance
- Low collector-emitter saturation voltage
- Closely matched current gain
- Reduces number of components and board space
- No mutual interference between the transistors
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

· General-purpose switching and amplification

4. Quick reference data

Table 1. Quid	ck reference data						
Symbol	Parameter	Conditions	Mi	in	Тур	Max	Unit
Per transist	or						
V _{CEO}	collector-emitter voltage	open base	-		-	45	V
I _C	collector current		-		-	100	mA
h _{FE}	DC current gain	V_{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	20	0	-	450	

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5. Pinning information

Table 2	. Pinning info	ormation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1		C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2		
4	E2	emitter TR2		
5	B2	base TR2		E1 B1 C2
6	C1	collector TR1	TSSOP6 (SOT363)	sym020

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BC847BS-Q		plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363		

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BC847BS-Q	1F%

[1] % = placeholder for manufacturing site code

8. Limiting values

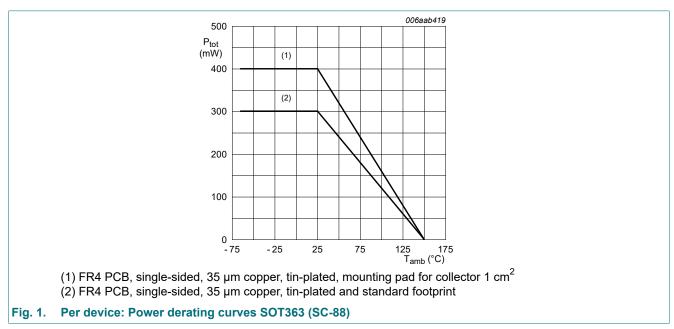
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transiste	or		I			
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	45	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	220	mW
			[2]	-	250	mW
Per device						
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
			[2]	-	400	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

 $\label{eq:constraint} [2] \quad \text{Device mounted on an FR4 PCB, single-sided, 35 } \mu\text{m copper, tin-plated, mounting pad for collector 1 } \text{cm}^2.$

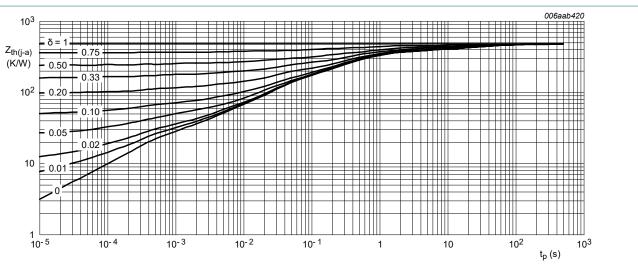


9. Thermal characteristics

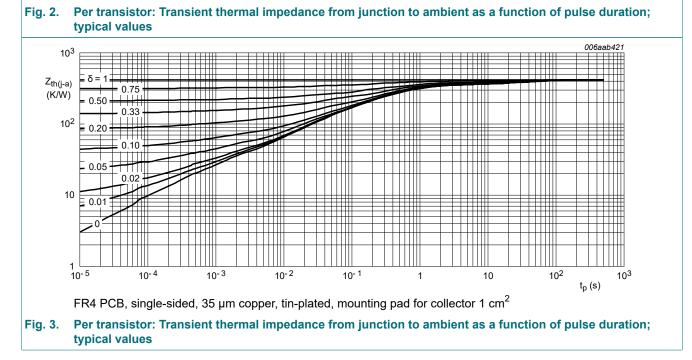
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Per transist	tor						
·······	thermal resistance from	in free air	[1]	-	-	568	K/W
	junction to ambient		[2]	-	-	500	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	230	K/W
Per device							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W
			[2]	-	-	313	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 1 cm².



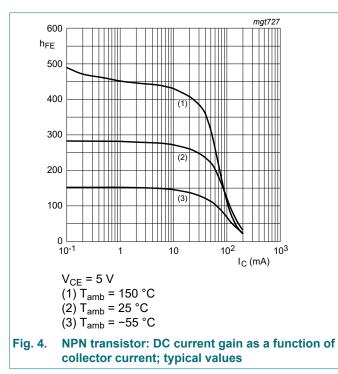
FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint

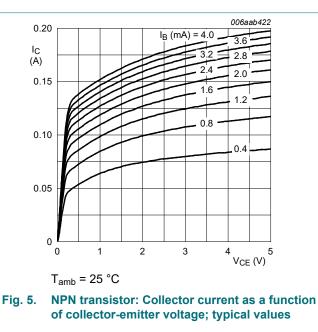


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10. Characteristics

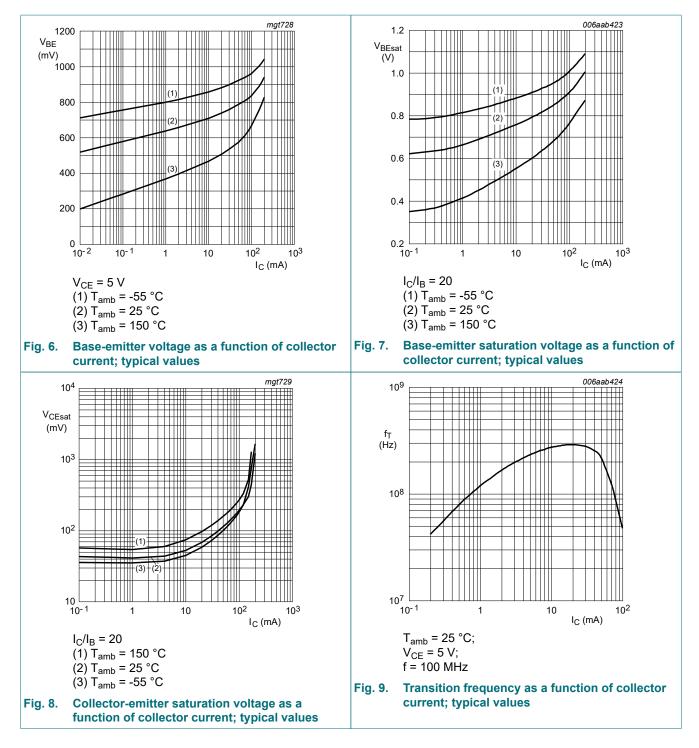
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per transist	or	· · · · · ·				
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A; T _{amb} = 25 °C	50	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _B = 0 A; T _{amb} = 25 °C	45	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _C = 0 A; I _E = 100 μA; T _{amb} = 25 °C	5	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	15	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	200	-	450	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	-	100	mV
	saturation voltage	I_{C} = 100 mA; I_{B} = 5 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	300	mV
V _{BEsat}	base-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	755	-	mV
V _{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	580	655	700	mV
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	1.5	pF
C _e	emitter capacitance	V _{EB} = 0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	11	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C	100	-	-	MHz





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45 V, 100 mA NPN/NPN general-purpose transistor

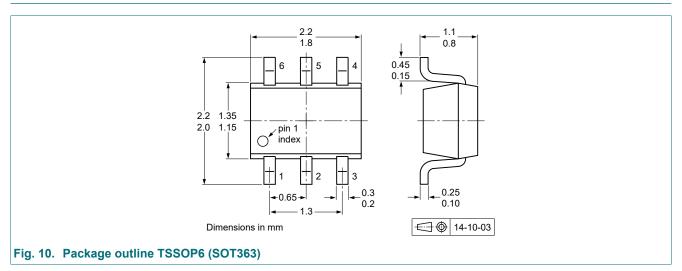


11. Test information

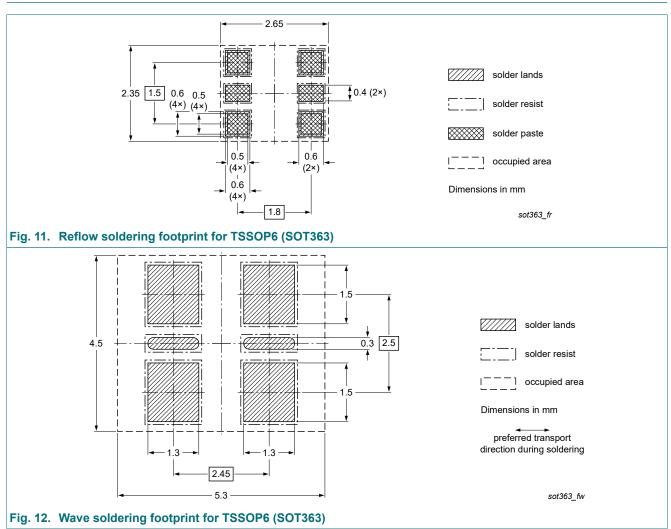
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



Product data sheet

14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
BC847BS-Q v.1	20210617	Product data sheet	-	-		

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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