

2PD601ARL; 2PD601ASL

50 V, 100 mA NPN general-purpose transistors
Rev. 01 — 6 November 2008

Product data sheet

Product profile

1.1 General description

NPN general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

Table 1. **Product overview**

Type number[1]	Package	Package		
	Nexperia	JEDEC		
2PD601ARL	SOT23	TO-236AB	2PB709ARL	
2PD601ASL			2PB709ASL	
2PD601ARL/DG	SOT23	TO-236AB	2PB709ARL/DG	
2PD601ASL/DG			2PB709ASL/DG	

^{[1] /}DG: halogen-free.

1.2 Features

- General-purpose transistors
- Two current gain selections
- AEC-Q101 qualified
- Small SMD plastic package

1.3 Applications

■ General-purpose switching and amplification

1.4 Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	50	V
I _C	collector current		-	-	100	mA
h _{FE}	DC current gain	$V_{CE} = 10 \text{ V};$ $I_C = 2 \text{ mA}$				
	h _{FE} group R		210	-	340	
	h _{FE} group S		290	-	460	



Pinning information 2.

Table 3 Pinning

Table 5.	i iiiiiiig		
Pin	Description	Simplified outline	Graphic symbol
1	base		_
2	emitter	<u> 3</u>	3
3	collector	1 2	1 — 2
			sym021

Ordering information 3.

Table 4. Ordering information

Type number[1]	Package	Package		
	Name	Description	Version	
2PD601ARL	-	plastic surface-mounted package; 3 leads	SOT23	
2PD601ASL				
2PD601ARL/DG				
2PD601ASL/DG				

^{[1] /}DG: halogen-free.

Marking

Product data sheet

Table 5. **Marking codes**

Type number	Marking code ^[1]
2PD601ARL	SM*
2PD601ASL	SK*
2PD601ARL/DG	SR*
2PD601ASL/DG	SY*

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[1] * = -: made in Hong Kong

* = p: made in Hong Kong

* = t: made in Malaysia

* = W: made in China

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5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	60	V
V_{CEO}	collector-emitter voltage	open base	-	50	V
V_{EBO}	emitter-base voltage	open collector	-	6	V
I _C	collector current		-	100	mA
I _{CM}	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	200	mA
I _{BM}	peak base current	single pulse; $t_p \le 1 \text{ ms}$	-	100	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 ^{\circ}C$	<u>[1]</u> _	250	mW
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		- 55	+150	°C
T _{stg}	storage temperature		-65	+150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

6. Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] -	-	500	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

7. Characteristics

Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I_{CBO}	collector-base cut-off current	$V_{CB} = 60 \text{ V}; I_{E} = 0 \text{ A}$		-	-	10	nA
		$V_{CB} = 60 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 ^{\circ}\text{C}$		-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_C = 0 \text{ A}$		-	-	10	nA
h _{FE}	DC current gain	$V_{CE} = 2 \text{ V};$ $I_{C} = 100 \text{ mA}$	<u>[1]</u>	90	-	-	
	h _{FE} group R	$V_{CE} = 10 \text{ V};$ $I_C = 2 \text{ mA}$		210	-	340	
	h _{FE} group S	$V_{CE} = 10 \text{ V};$ $I_C = 2 \text{ mA}$		290	-	460	

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Characteristics ...continued Table 8. T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEsat}	collector-emitter saturation voltage	$I_C = 100 \text{ mA};$ $I_B = 10 \text{ mA}$	<u>[1]</u> -	-	250	mV
f _T	transition frequency	$V_{CE} = 10 \text{ V};$ $I_{C} = 2 \text{ mA};$ $f = 100 \text{ MHz}$	100	-	-	MHz
C _c	collector capacitance	$V_{CB} = 10 \text{ V};$ $I_{E} = i_{e} = 0 \text{ A};$ $f = 1 \text{ MHz}$	-	-	3	pF

^[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.

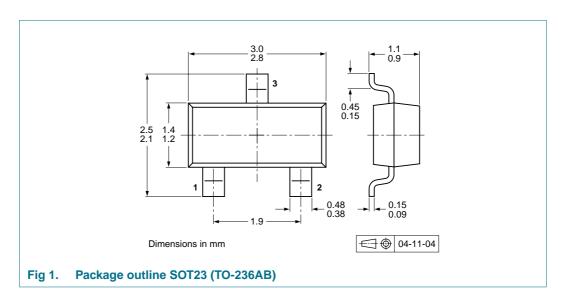
Test information 8.

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

Package outline

Product data sheet



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10. Packing information

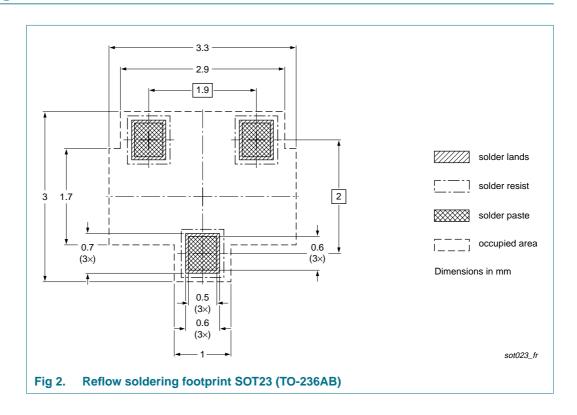
Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

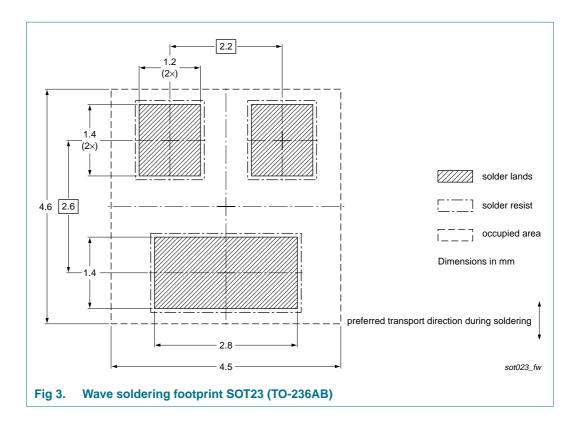
Type number[2]	Package	Description	Packing	quantity
			3000	10000
2PD601ARL	SOT23	4 mm pitch, 8 mm tape and reel	-215	-235
2PD601ASL				
2PD601ARL/DG				
2PD601ASL/DG				

- [1] For further information and the availability of packing methods, see Section 14.
- [2] /DG: halogen-free.

11. Soldering



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12. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
2PD601AXL_1	20081106	Product data sheet	-	-

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2PD601ARL; 2PD601ASL

50 V, 100 mA NPN general-purpose transistors

13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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