

# NHDTC123JU/143ZU/114YU

# series

80 V, 100 mA NPN resistor-equipped transistors

Rev. 1 — 17 July 2020

**Product data sheet** 

### 1. General description

NPN Resistor-Equipped Transistor (RET) family in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

**Table 1. Product overview** 

Type number	R1	R2		Package	PNP complement:
	kΩ	kΩ	Nexperia	JEITA	
NHDTC123JU	2.2	47	SOT323	SC-70	NHDTA123JU
NHDTC143ZU	4.7	47			NHDTA143ZU
NHDTC114YU	10	47			NHDTA114YU

#### 2. Features and benefits

- · 100 mA output current capability
- · High breakdown voltage
- · Built-in resistors
- · Simplifies circuit design
- · Reduces component count
- Reduces pick and place costs
- AEC-Q101 qualified

### 3. Applications

- Digital applications
- · Cost saving alternative for BC846 series in digital applications
- Controlling IC inputs
- Switching loads

#### 4. Quick reference data

#### Table 2. Quick reference data

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	80	V
I <sub>O</sub>	output current		-	-	100	mA



### 5. Pinning information

#### **Table 3. Pinning**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	<u></u> 3	
2	GND	GND (emitter)		R1
3	0	output (collector)		
				GND
				aaa-019964

### 6. Ordering information

#### **Table 4. Ordering information**

Type number	Package					
	Name	Description	Version			
NHDTC123JU	SC-70	plastic surface-mounted package; 3 leads	SOT323			
NHDTC143ZU						
NHDTC114YU	-					

### 7. Marking

#### Table 5. Marking

Type number	Marking code [1]
NHDTC123JU	5P%
NHDTC143ZU	5R%
NHDTC114YU	5N%

[1] % = placeholder for manufacturing site code

### 8. Limiting values

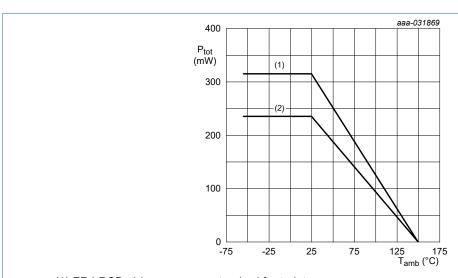
#### **Table 6. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

T<sub>amb</sub> = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	80	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	7	V
V <sub>I</sub>	input voltage	,		_		
	NHDTC123JU			-7	+20	V
	NHDTC143ZU			-7	+30	V
	NHDTC114YU			-7	+40	V
lo	output current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	235	mW
			[2]	-	315	mW
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit-Board (PCB);4-layer copper; tin-plated and standard footprint.



- (1) FR4 PCB, 4-layer copper, standard footprint
- (2) FR4 PCB, single-sided copper, standard footprint

Fig. 1. Power derating curves for SOT23 (TO-236AB)

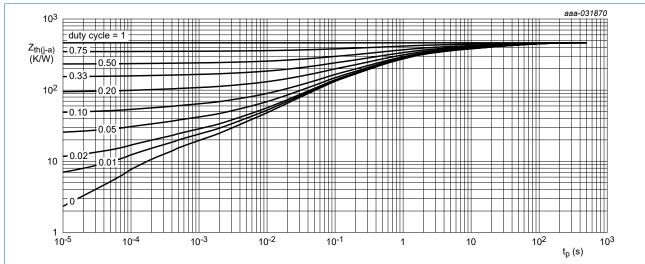
#### 9. Thermal characteristics

#### **Table 7. Thermal characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

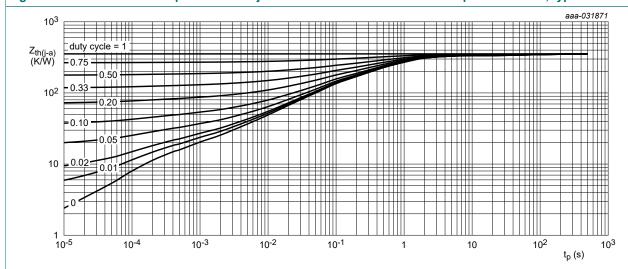
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	532	K/W
			[2]	-	-	397	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	150	K/W

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated and standard footprint.



FR4 PCB, single-sided copper, tin-plated and standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, 4-layer copper, tin-plated and standard footprint

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

### 10. Characteristics

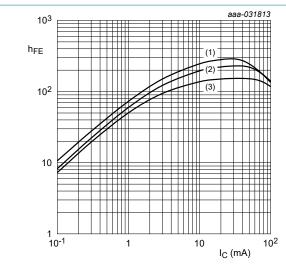
#### **Table 8. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A	80	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}$		-	-	V
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = 80 \text{ V}; I_E = 0 \text{ A}$		-	100	nA
I <sub>CEO</sub>	collector-emitter cut-off	V <sub>CE</sub> = 60 V; I <sub>B</sub> = 0 A	-	-	100	nA
	current	V <sub>CE</sub> = 60 V; I <sub>B</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	5	μA
I <sub>EBO</sub>	emitter-base cut-off curr	ent				
	NHDTC123JU	V <sub>EB</sub> = 7 V; I <sub>C</sub> = 0 A	-	-	270	μA
	NHDTC143ZU		-	-	260	μA
	NHDTC114YU		-	-	230	μA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> =10 mA	100	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA	-	-	100	mV
V <sub>I(off)</sub>	off-state input voltage					
	NHDTC123JU	V <sub>CE</sub> = 5 V ; I <sub>C</sub> = 100 μA	-	595	500	mV
	NHDTC143ZU		-	625	500	mV
	NHDTC114YU		-	690	500	mV
V <sub>I(on)</sub>	on-state input voltage		'	'	'	'
	NHDTC123JU	V <sub>CE</sub> = 0.3 V ; I <sub>C</sub> = 10 mA	1.2	0.81	-	V
	NHDTC143ZU		1.4	0.95	-	V
	NHDTC114YU		1.6	1.22	-	V
R1	bias resistor 1 (input)		,			
	NHDTC123JU		1] 1.54	2.2	2.86	kΩ
	NHDTC143ZU		3.3	4.7	6.1	kΩ
	NHDTC114YU		7	10	13	kΩ
R2/R1	bias resistor ratio		, , , , , , , , , , , , , , , , , , ,			
	NHDTC123JU		1] 17	21	26	
	NHDTC143ZU		8	10	12	
	NHDTC114YU		3.7	4.7	5.7	
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz	2] -	170	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz	-	-	2.5	pF

<sup>[1]</sup> See section "Test information" for resistor calculation and test conditions

<sup>[2]</sup> Characteristics of built-in transistor



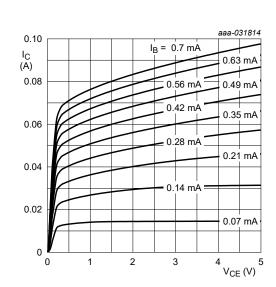
$$V_{CE} = 5 V$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

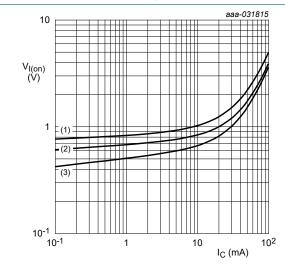
(3) 
$$T_{amb} = -40 \, ^{\circ}C$$

Fig. 4. NHDTC123JU: DC current gain as a function of collector current; typical values



 $T_{amb}$  = 25 °C

Fig. 5. NHDTC123JU: Collector current as a function of collector-emitter voltage; typical values

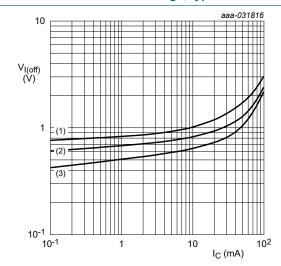


 $V_{CE} = 0.3 V$ 

(1) 
$$T_{amb} = -40 \, ^{\circ}C$$

(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 6. NHDTC123JU: On-state input voltage as a function of collector current; typical values



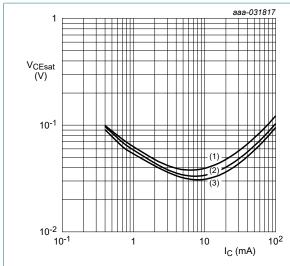
$$V_{CE} = 5 V$$

(1) 
$$T_{amb} = -40$$
 °C

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 7. NHDTC123JU: Off-state input voltage as a function of collector current; typical values



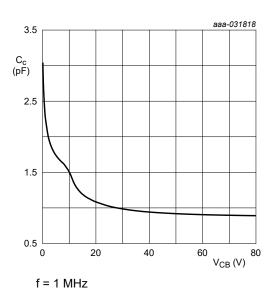
 $I_{\rm C}/I_{\rm B} = 20$ 

(1)  $T_{amb}$  = 100 °C

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

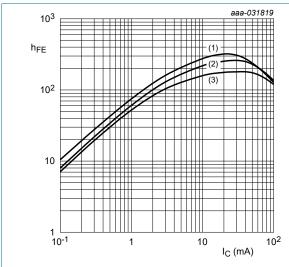
(3)  $T_{amb} = -40 \, ^{\circ}C$ 

Fig. 8. NHDTC123JU: Collector-emitter saturation voltage as a function of collector current; typical values



f = 1 MHz $T_{amb} = 25 ^{\circ}\text{C}$ 

Fig. 9. NHDTC123JU: Collector capacitance as a function of collector-base voltage; typical values



 $V_{CE} = 5 V$ 

(1)  $T_{amb} = 100 \, ^{\circ}C$ 

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb} = -40 \, ^{\circ}C$ 

Fig. 10. NHDTC143ZU: DC current gain as a function of collector current; typical values

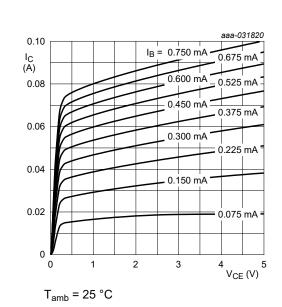
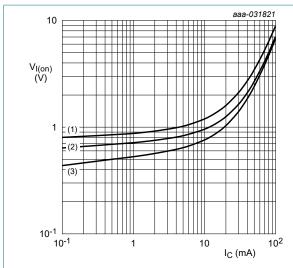


Fig. 11. NHDTC143ZU: Collector current as a function of collector-emitter voltage; typical values



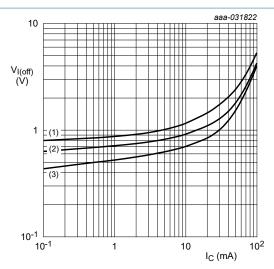
$$V_{CE} = 0.3 V$$

(1) 
$$T_{amb} = -40 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb}$$
 = 100 °C

Fig. 12. NHDTC143ZU: On-state input voltage as a function of collector current; typical values



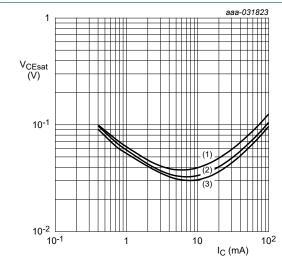
$$V_{CE} = 5 V$$

(1) 
$$T_{amb} = -40 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 13. NHDTC143ZU: Off-state input voltage as a function of collector current; typical values



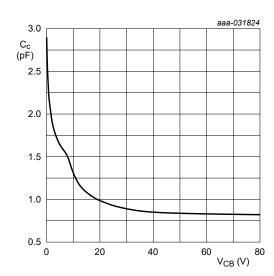
$$I_{\rm C}/I_{\rm B} = 20$$

$$(1) T_{amb} = 100 °C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = -40 \, ^{\circ}C$$

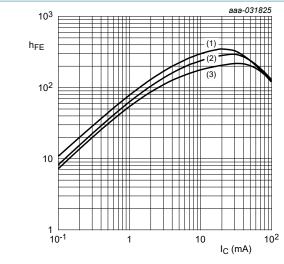
Fig. 14. NHDTC143ZU: Collector-emitter saturation voltage as a function of collector current; typical values



f = 1 MHz

Fig. 15. NHDTC143ZU: Collector capacitance as a function of collector-base voltage; typical values

8 / 15



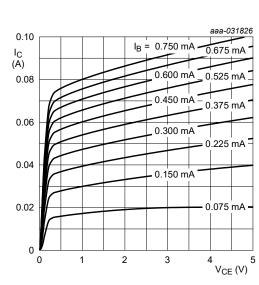
$$V_{CE} = 5 V$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

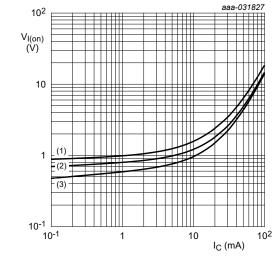
(3) 
$$T_{amb} = -40 \, ^{\circ}C$$

Fig. 16. NHDTC114YU: DC current gain as a function of collector current; typical values



 $T_{amb}$  = 25 °C

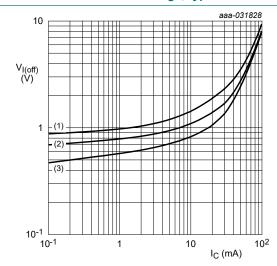
Fig. 17. NHDTC114YU: Collector current as a function of collector-emitter voltage; typical values



 $V_{CE} = 0.3 V$ 

(1) 
$$T_{amb} = -40 \, ^{\circ}C$$

Fig. 18. NHDTC114YU: On-state input voltage as a function of collector current; typical values



 $V_{CE} = 5 V$ 

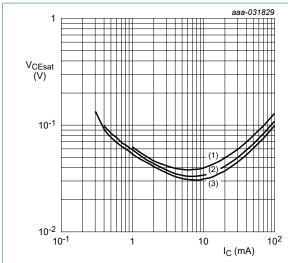
(1) 
$$T_{amb} = -40 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 19. NHDTC114YU: Off-state input voltage as a function of collector current; typical values

9 / 15

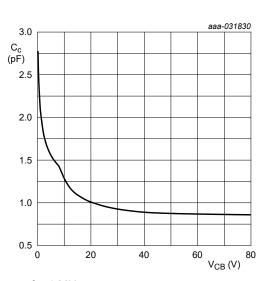


$$I_{\rm C}/I_{\rm B} = 20$$

(2) 
$$T_{amb}$$
 = 25 °C

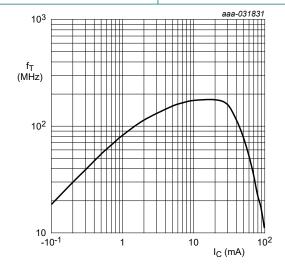
(3) 
$$T_{amb} = -40 \, ^{\circ}C$$

Fig. 20. NHDTC114YU: Collector-emitter saturation voltage as a function of collector current; typical values



$$f = 1 MHz$$
  
 $T_{amb} = 25 °C$ 

Fig. 21. NHDTC114YU: Collector capacitance as a function of collector-base voltage; typical values



f = 100 MHz

$$V_{CE} = 5 V$$

 $T_{amb} = 25 \, ^{\circ}C$ 

Fig. 22. Transition frequency as a function of collector current; typical values of built-in transistor

#### 11. Test information

#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

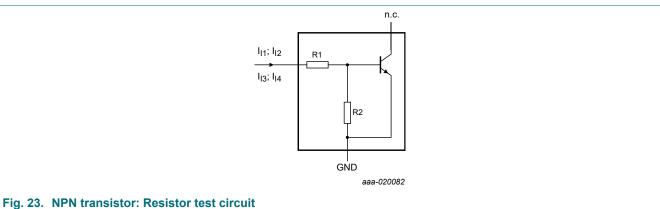
#### **Resistor calculation**

Calculation of bias resistor 1 (R1)

$$R1 = \frac{V(I12) - V(I11)}{I12 - I11}$$

Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I14) - V(I13)}{R1 \cdot (I14 - I13)} - 1$$

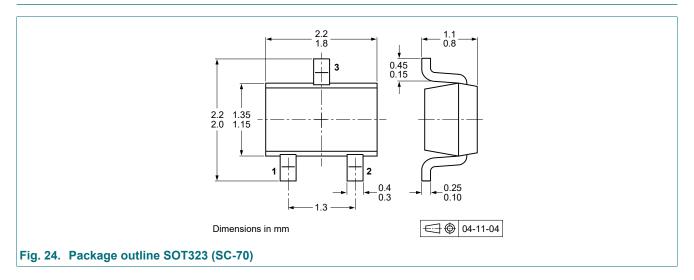


#### **Resistor test conditions**

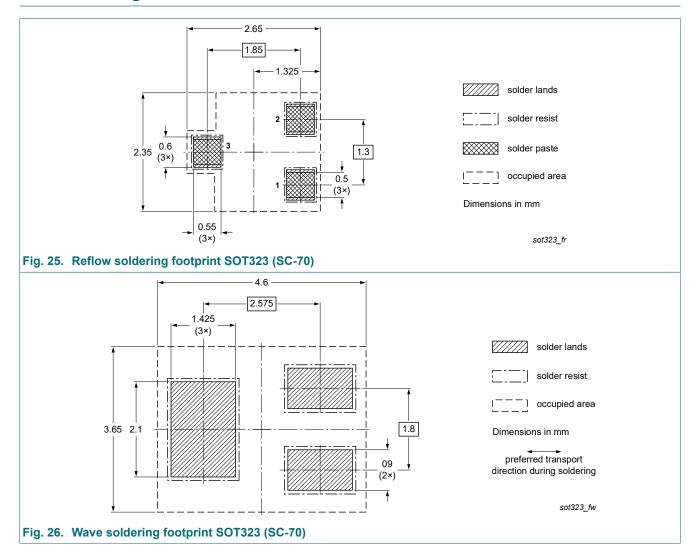
Table 9. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I <sub>I1</sub>	I <sub>I2</sub>	I <sub>13</sub>	I <sub>14</sub>
NHDTC123JU	2.2	47	1.6 mA	2.4 mA	-55 μΑ	-105 μA
NHDTC143ZU	4.7	47	1.2 mA	1.8 mA	-55 μΑ	-105 μA
NHDTC114YU	10	47	0.8 mA	1.1 mA	-55 μΑ	-105 μA

### 12. Package outline



### 13. Soldering



## 14. Revision history

#### Table 10. Revision history

Data sheet ID	Release date		Change notice	Supersedes
NHDTC123JU_143ZU_114YU_SER v.1	20200717	Product data sheet	-	-

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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NHDTC123JU\_143ZU\_114YU\_SER

Product data sheet

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### **Contents**

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	3
9.	Thermal characteristics	4
10.	. Characteristics	5
11.	Test information	. 11
12.	. Package outline	. 12
13.	. Soldering	. 12
14.	. Revision history	.13
15.	. Legal information	14

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