



NHDTC123JT/143ZT/114YT series

80 V, 100 mA NPN resistor-equipped transistors

Rev. 1 — 7 July 2020

Product data sheet

1. General description

NPN Resistor-Equipped Transistor (RET) family in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	R1	R2	Package		PNP complement:
	k Ω	k Ω	Nexperia	JEDEC	
NHDTC123JT	2.2	47	SOT23	TO-236AB	NHDTA123JT
NHDTC143ZT	4.7	47			NHDTA143ZT
NHDTC114YT	10	47			NHDTA114YT

2. Features and benefits

- 100 mA output current capability
- High breakdown voltage
- Built-in resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- AEC-Q101 qualified

3. Applications

- Digital applications
- Cost saving alternative for BC846 series in digital applications
- Controlling IC inputs
- Switching loads

4. Quick reference data

Table 2. Quick reference data

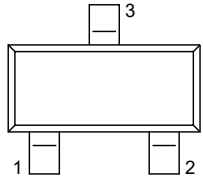
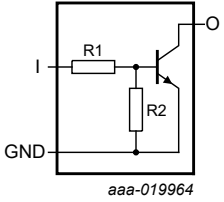
$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	80	V
I_O	output current		-	-	100	mA

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5. Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)		 aaa-019964
2	GND	GND (emitter)		
3	O	output (collector)		

6. Ordering information

Table 4. Ordering information

Type number	Package		
	Name	Description	Version
NHDTC123JT	TO-236AB	plastic surface-mounted package; 3 leads	SOT23
NHDTC143ZT			
NHDTC114YT			

7. Marking

Table 5. Marking

Type number	Marking code [1]
NHDTC123JT	QJ%
NHDTC143ZT	QL%
NHDTC114YT	QH%

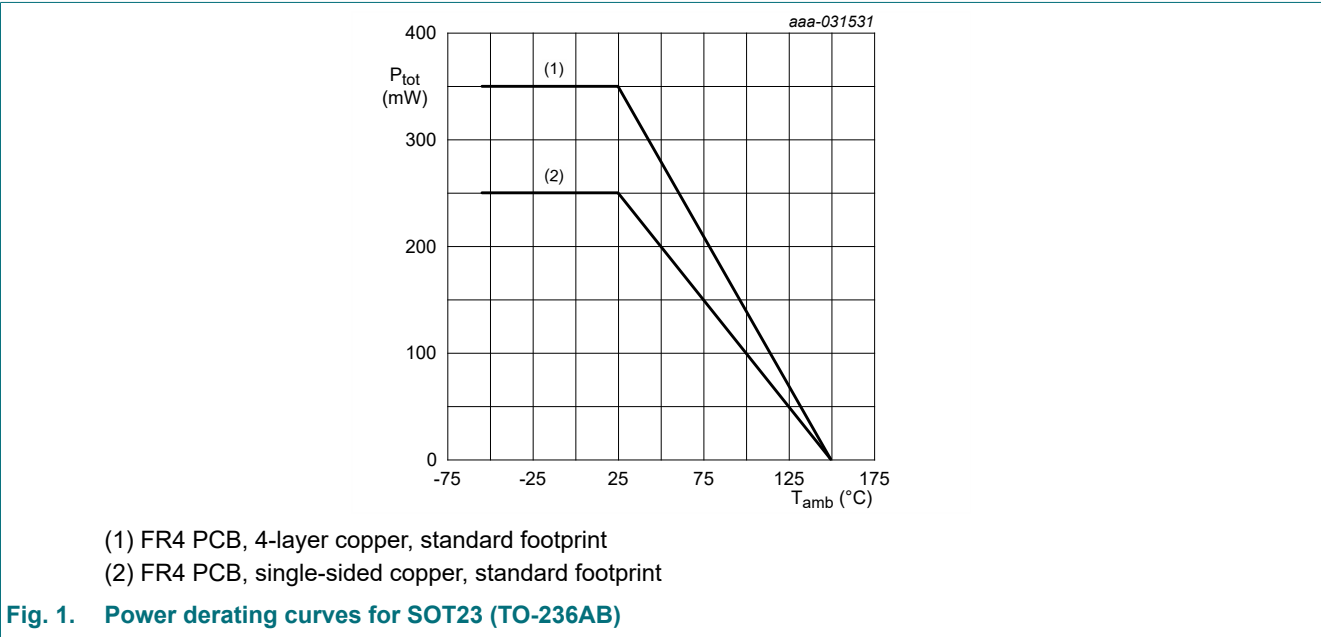
[1] % = placeholder for manufacturing site code

8. Limiting values

Table 6. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).
T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	-	80	V
V _{CEO}	collector-emitter voltage	open base	-	80	V
V _{EBO}	emitter-base voltage	open collector	-	7	V
V _I	input voltage				
	NHDTC123JT		-7	+20	V
	NHDTC143ZT		-7	+30	V
	NHDTC114YT		-7	+40	V
I _O	output current		-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	250	mW
			[2]	350	mW
T _j	junction temperature		-	150	°C
T _{amb}	ambient temperature		-55	150	°C
T _{stg}	storage temperature		-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit-Board (PCB);4-layer copper; tin-plated and standard footprint.



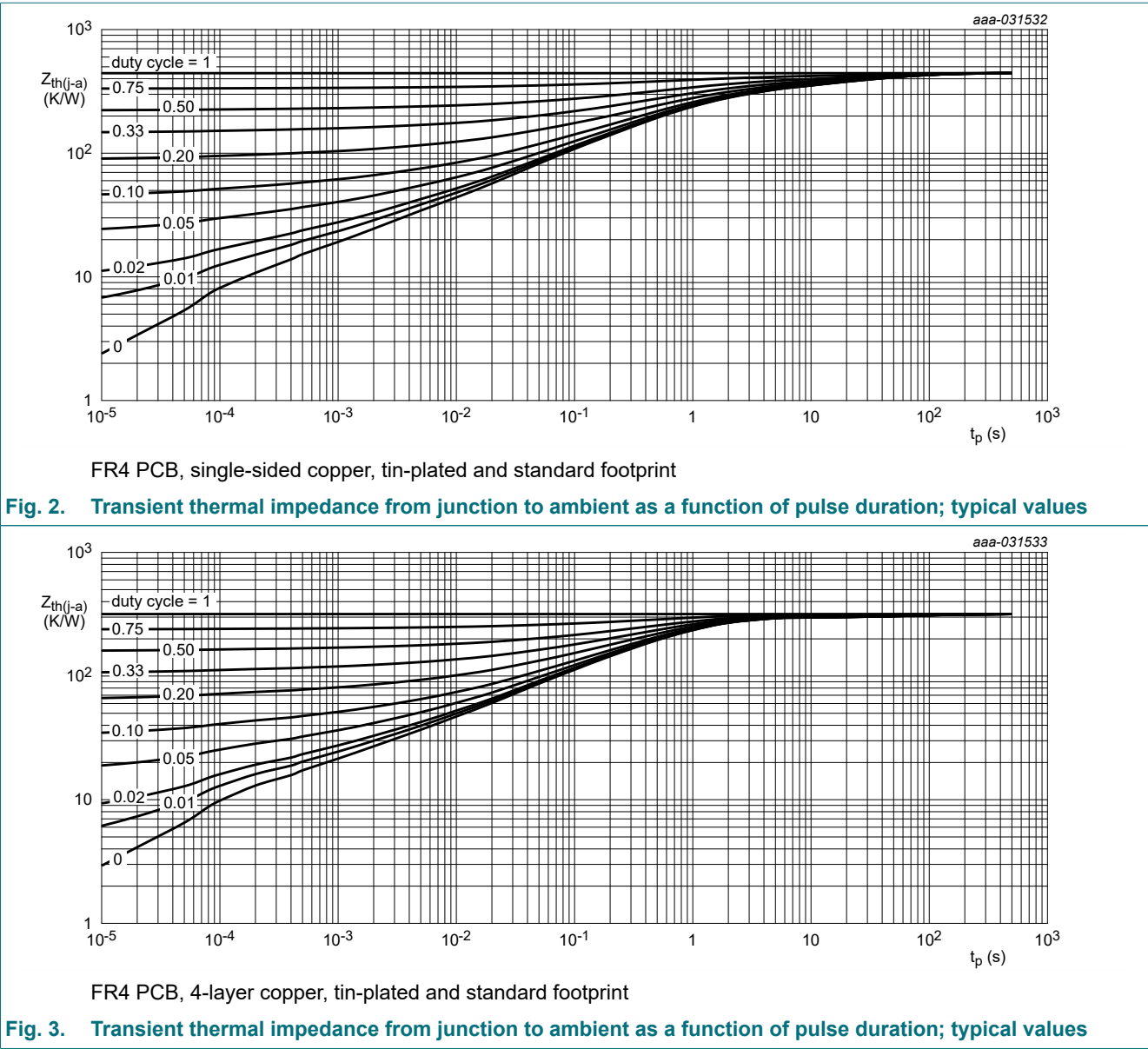
9. Thermal characteristics

Table 7. Thermal characteristics

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W
			[2]	-	-	358	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	130	K/W

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated and standard footprint.



10. Characteristics

Table 8. Characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A	80	-	-	V	
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _B = 0 A	80	-	-	V	
I _{CBO}	collector-base cut-off current	V _{CB} = 80 V; I _E = 0 A	-	-	100	nA	
I _{CEO}	collector-emitter cut-off current	V _{CE} = 60 V; I _B = 0 A	-	-	100	nA	
		V _{CE} = 60 V; I _B = 0 A; T _j = 150 °C	-	-	5	μA	
I _{EBO}	emitter-base cut-off current						
	NHDTTC123JT	V _{EB} = 7 V; I _C = 0 A	-	-	270	μA	
	NHDTTC143ZT		-	-	260	μA	
	NHDTTC114YT		-	-	230	μA	
h _{FE}	DC current gain	V _{CE} = 5 V; I _C =10 mA	100	-	-		
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA	-	-	100	mV	
V _{I(off)}	off-state input voltage						
	NHDTTC123JT	V _{CE} = 5 V ; I _C = 100 μA	-	595	500	mV	
	NHDTTC143ZT		-	625	500	mV	
	NHDTTC114YT		-	690	500	mV	
V _{I(on)}	on-state input voltage						
	NHDTTC123JT	V _{CE} = 0.3 V ; I _C = 10 mA	1.2	0.81	-	V	
	NHDTTC143ZT		1.4	0.95	-	V	
	NHDTTC114YT		1.6	1.22	-	V	
R1	bias resistor 1 (input)						
	NHDTTC123JT	[1]	1.54	2.2	2.86	kΩ	
	NHDTTC143ZT		3.3	4.7	6.1	kΩ	
	NHDTTC114YT		7	10	13	kΩ	
R2/R1	bias resistor ratio						
	NHDTTC123JT	[1]	17	21	26		
	NHDTTC143ZT		8	10	12		
	NHDTTC114YT		3.7	4.7	5.7		
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz	[2]	-	170	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz	-	-	2.5	pF	

[1] See section "Test information" for resistor calculation and test conditions

[2] Characteristics of built-in transistor

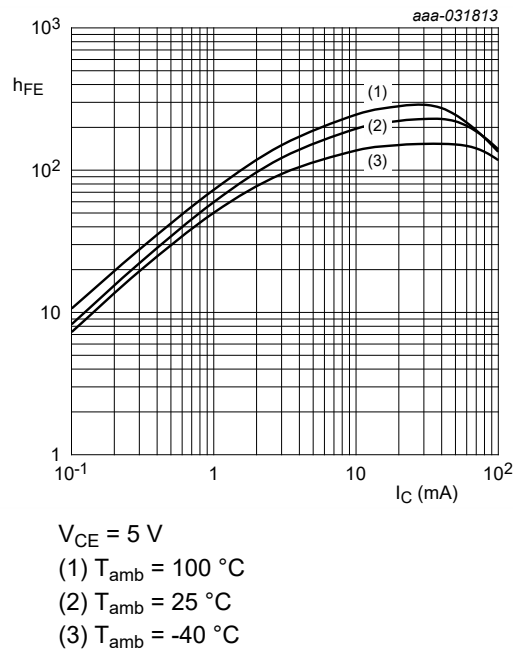


Fig. 4. NHDTC123JT: DC current gain as a function of collector current; typical values

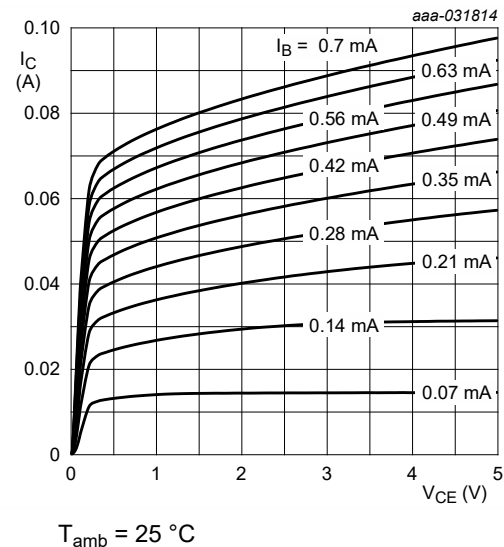


Fig. 5. NHDTC123JT: Collector current as a function of collector-emitter voltage; typical values

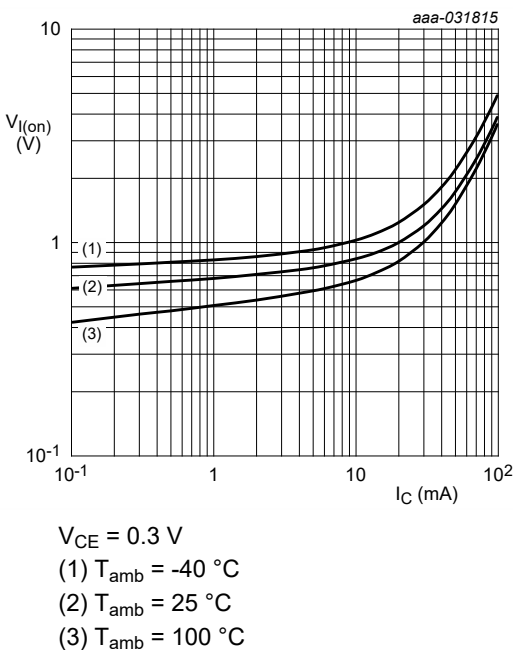


Fig. 6. NHDTC123JT: On-state input voltage as a function of collector current; typical values

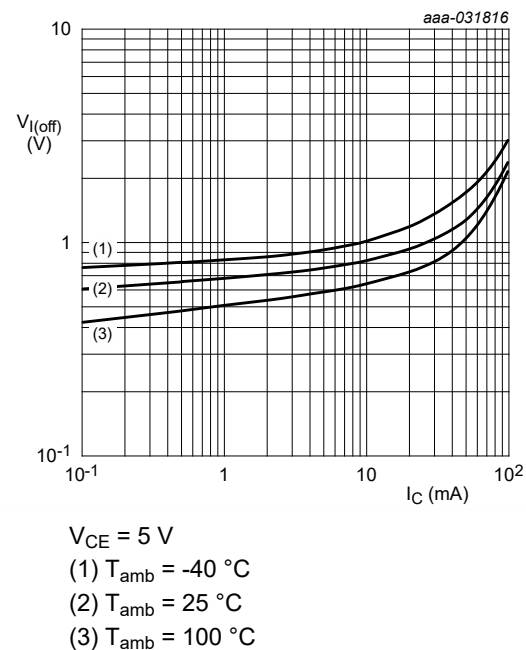
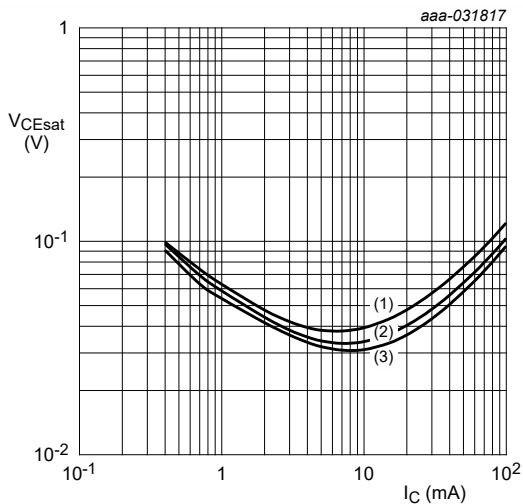
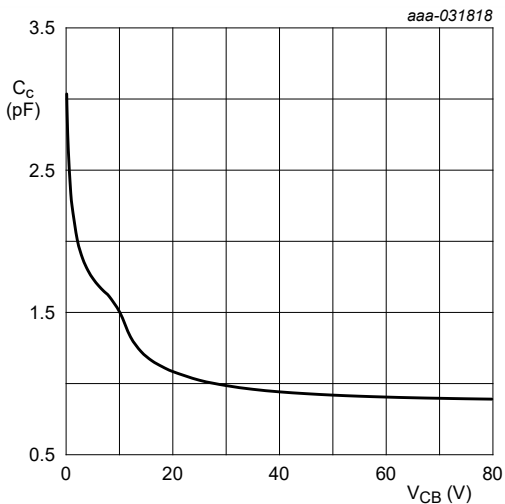


Fig. 7. NHDTC123JT: Off-state input voltage as a function of collector current; typical values



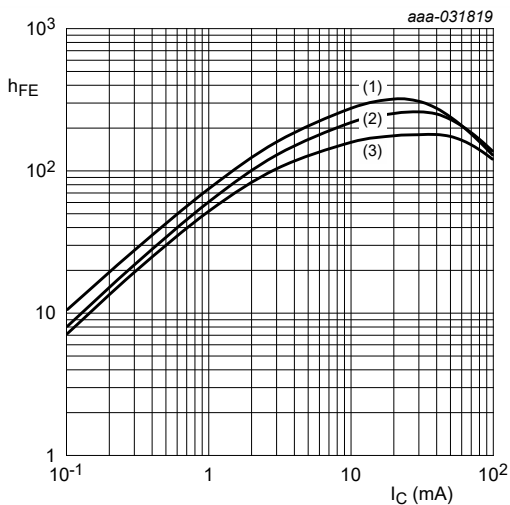
$I_C/I_B = 20$
(1) $T_{amb} = 100\text{ }^{\circ}\text{C}$
(2) $T_{amb} = 25\text{ }^{\circ}\text{C}$
(3) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Fig. 8. NHDTC123JT: Collector-emitter saturation voltage as a function of collector current; typical values



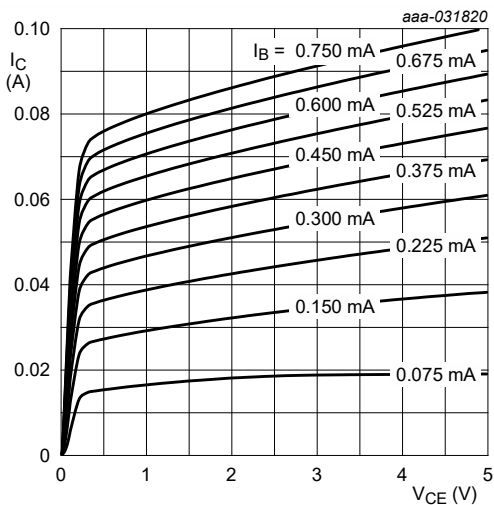
$f = 1\text{ MHz}$
 $T_{amb} = 25\text{ }^{\circ}\text{C}$

Fig. 9. NHDTC123JT: Collector capacitance as a function of collector-base voltage; typical values



$V_{CE} = 5\text{ V}$
(1) $T_{amb} = 100\text{ }^{\circ}\text{C}$
(2) $T_{amb} = 25\text{ }^{\circ}\text{C}$
(3) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Fig. 10. NHDTC143ZT: DC current gain as a function of collector current; typical values



$T_{amb} = 25\text{ }^{\circ}\text{C}$

Fig. 11. NHDTC143ZT: Collector current as a function of collector-emitter voltage; typical values

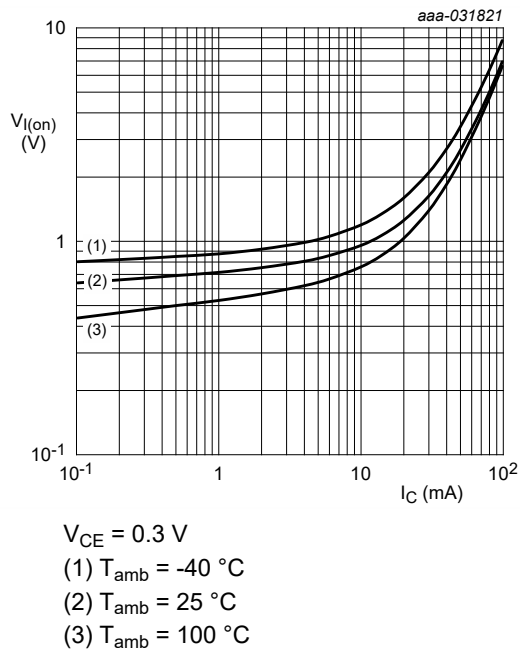


Fig. 12. NHDTC143ZT: On-state input voltage as a function of collector current; typical values

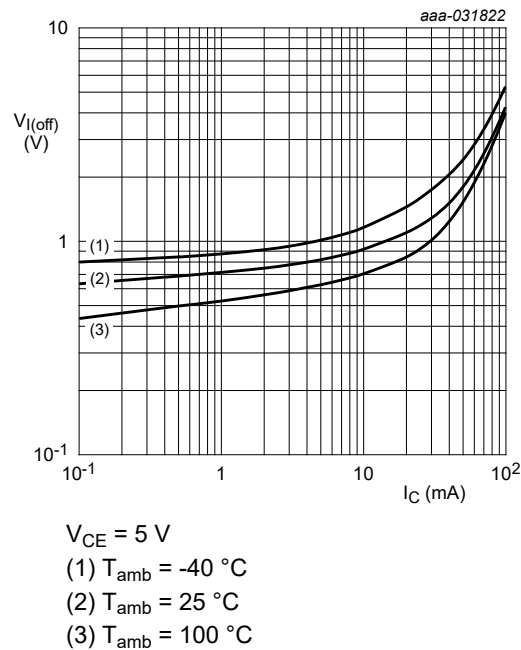


Fig. 13. NHDTC143ZT: Off-state input voltage as a function of collector current; typical values

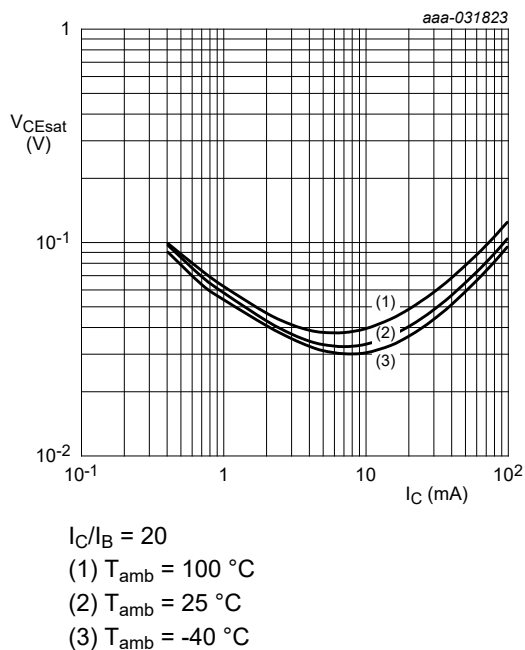


Fig. 14. NHDTC143ZT: Collector-emitter saturation voltage as a function of collector current; typical values

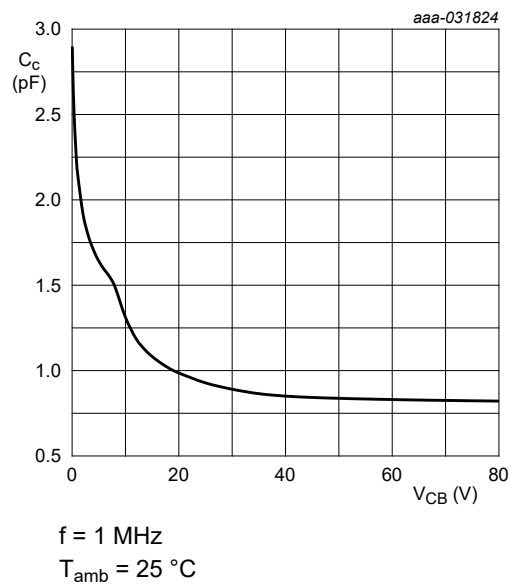


Fig. 15. NHDTC143ZT: Collector capacitance as a function of collector-base voltage; typical values

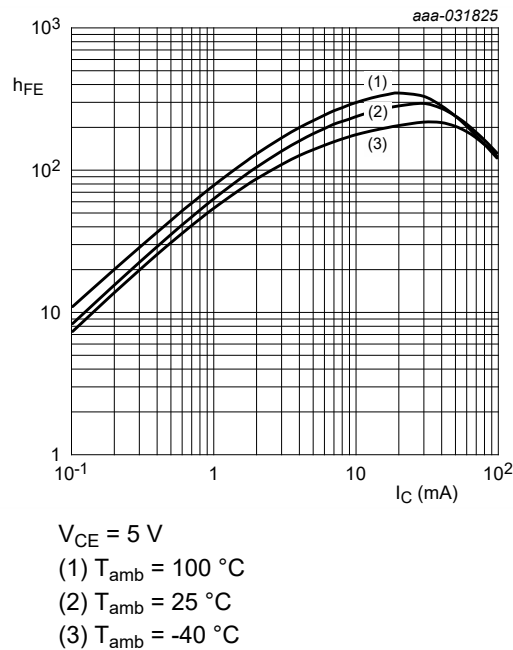


Fig. 16. NHDTC114YT: DC current gain as a function of collector current; typical values

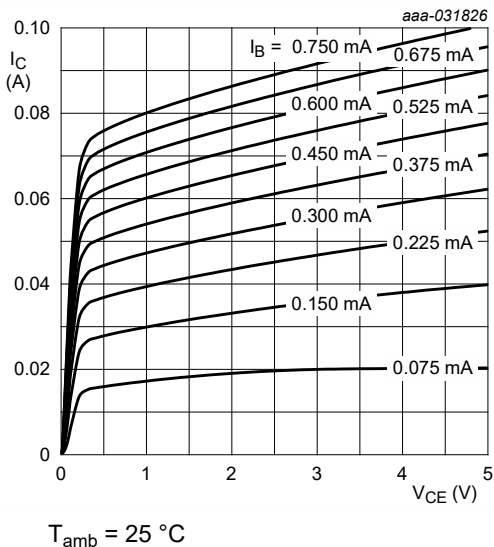


Fig. 17. NHDTC114YT: Collector current as a function of collector-emitter voltage; typical values

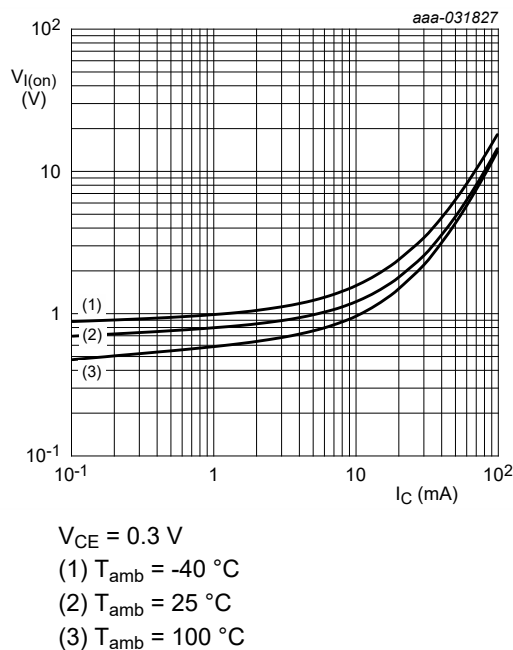


Fig. 18. NHDTC114YT: On-state input voltage as a function of collector current; typical values

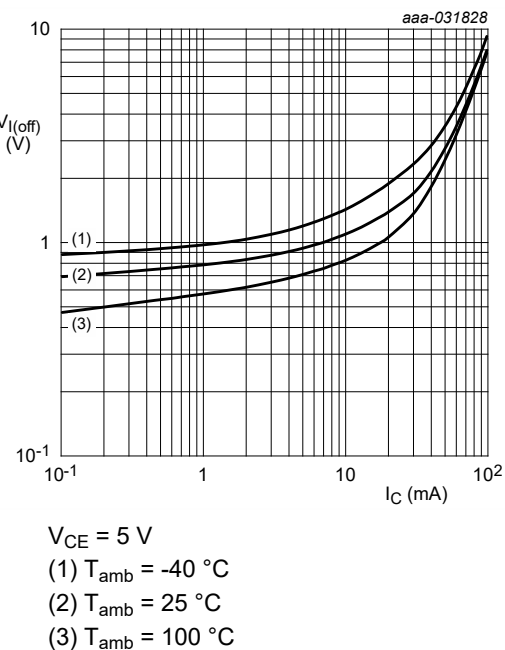
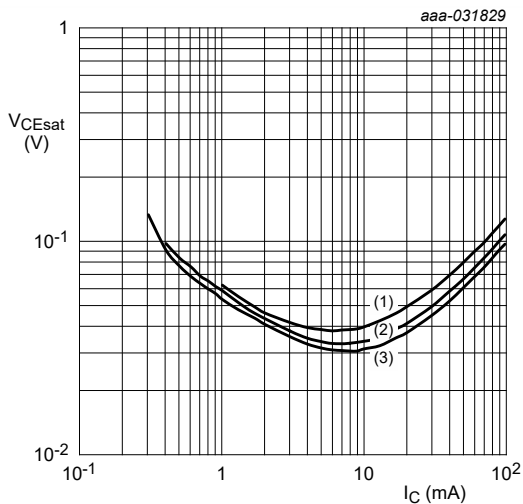
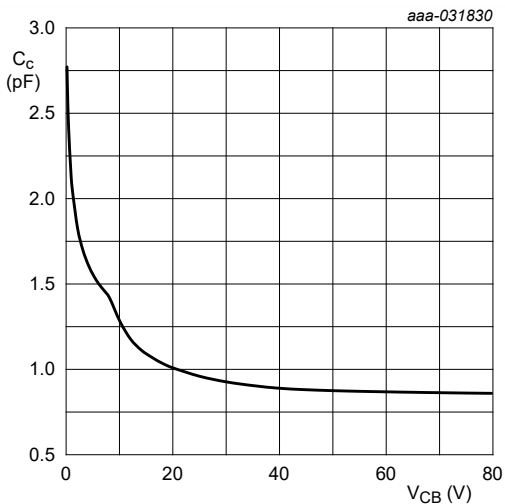


Fig. 19. NHDTC114YT: Off-state input voltage as a function of collector current; typical values



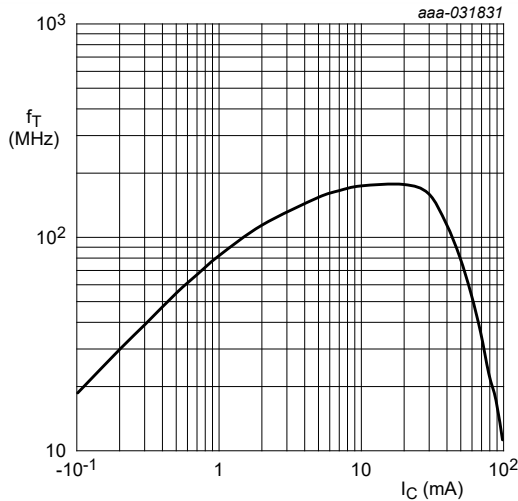
$I_C/I_B = 20$
(1) $T_{amb} = 100\text{ }^{\circ}\text{C}$
(2) $T_{amb} = 25\text{ }^{\circ}\text{C}$
(3) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Fig. 20. NHDTC114YT: Collector-emitter saturation voltage as a function of collector current; typical values



$f = 1\text{ MHz}$
 $T_{amb} = 25\text{ }^{\circ}\text{C}$

Fig. 21. NHDTC114YT: Collector capacitance as a function of collector-base voltage; typical values



$f = 100\text{ MHz}$
 $V_{CE} = 5\text{ V}$
 $T_{amb} = 25\text{ }^{\circ}\text{C}$

Fig. 22. Transition frequency as a function of collector current; typical values of built-in transistor

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

Resistor calculation

- Calculation of bias resistor 1 (R1)

$$R1 = \frac{V(I_{I2}) - V(I_{I1})}{I_{I2} - I_{I1}}$$
- Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I_{I4}) - V(I_{I3})}{R1 \cdot (I_{I4} - I_{I3})} - 1$$

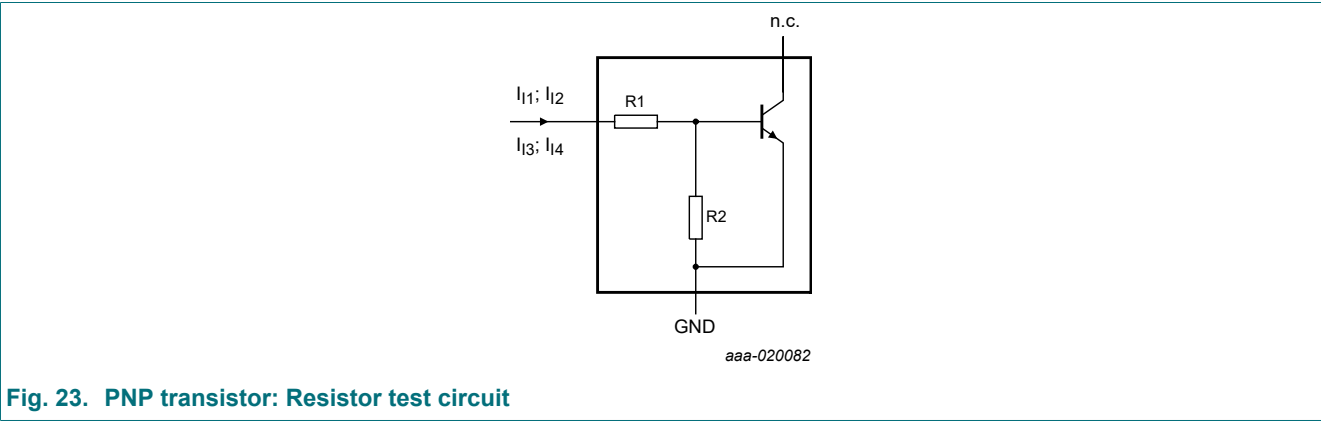


Fig. 23. PNP transistor: Resistor test circuit

Resistor test conditions

Table 9. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I _{I1}	I _{I2}	I _{I3}	I _{I4}
NHDTC123JT	2.2	47	1.6 mA	2.4 mA	-55 μA	-105 μA
NHDTC143ZT	4.7	47	1.2 mA	1.8 mA	-55 μA	-105 μA
NHDTC114YT	10	47	800 μA	1.1 mA	-55 μA	-105 μA

12. Package outline

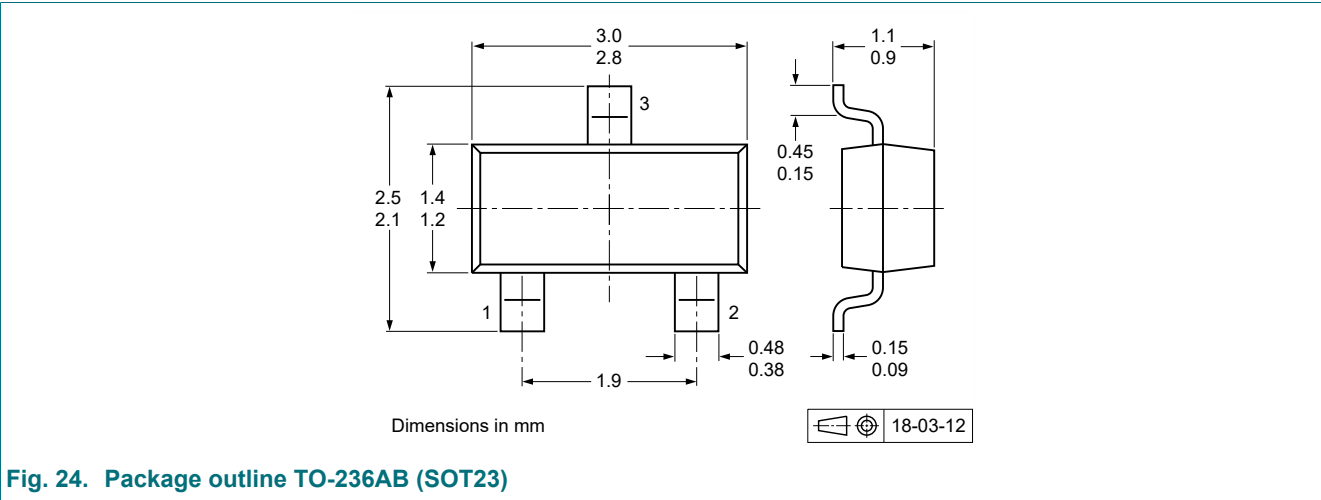


Fig. 24. Package outline TO-236AB (SOT23)

13. Soldering

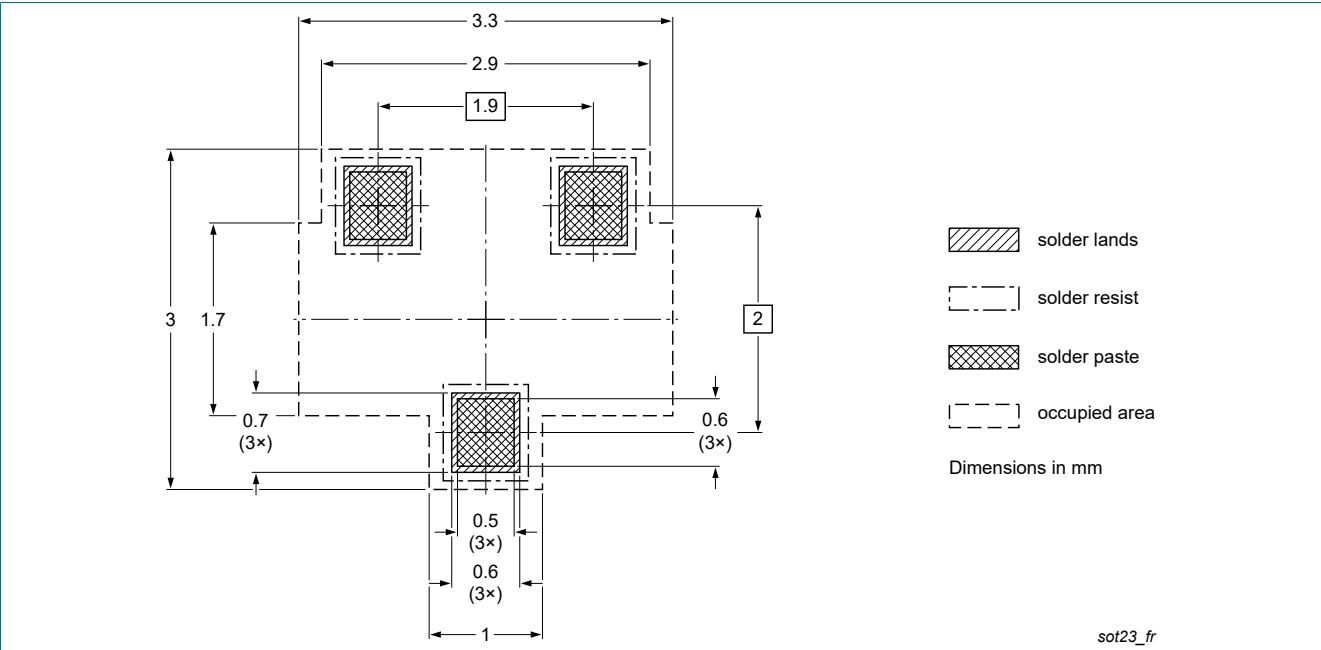


Fig. 25. Reflow soldering footprint for TO-236AB (SOT23)

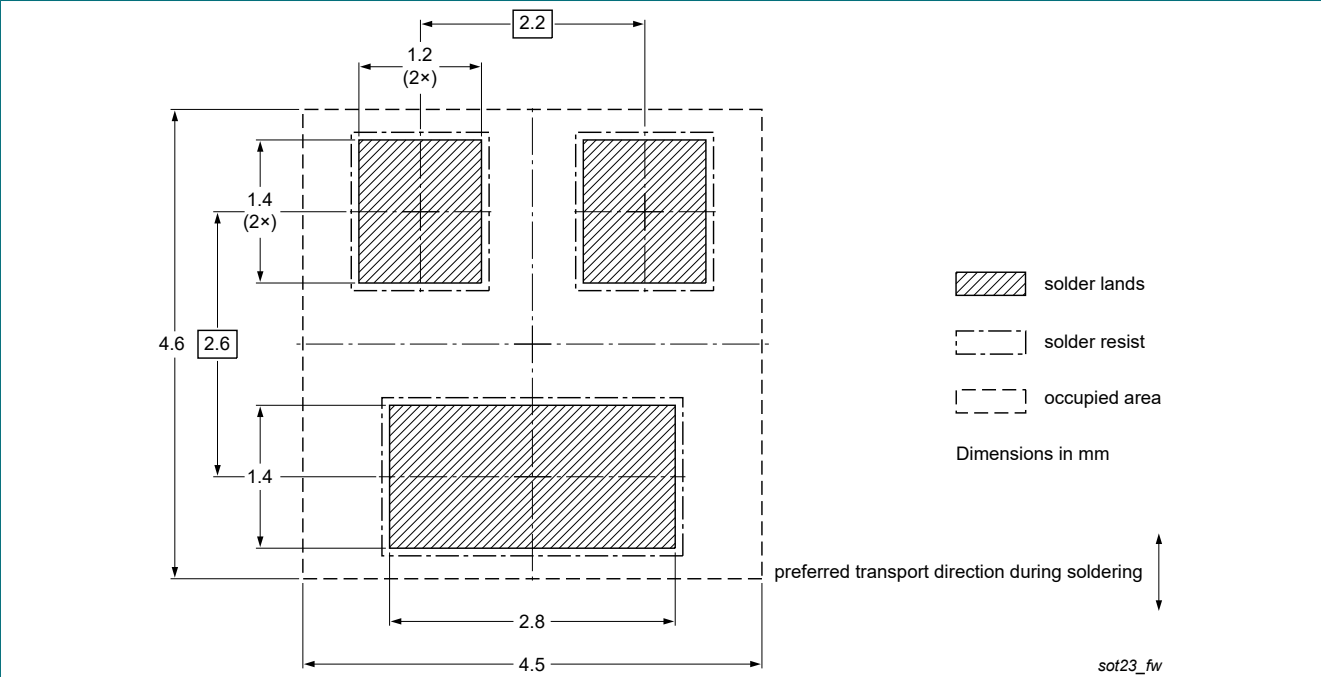


Fig. 26. Wave soldering footprint for TO-236AB (SOT23)

14. Revision history

Table 10. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NHDTC123JT_143ZT_114YT_SER v.1	20200707	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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