

40 V, 200 mA NPN/PNP switching transistor

28 December 2022

Product data sheet

1. General description

NPN/PNP double switching transistor in a SOT666 ultra small and flat lead Surface-Mounted Device (SMD) plastic package.

NPN/NPN complement: PMBT3904VS

PNP/PNP complement: PMBT3906VS

2. Features and benefits

- Double general-purpose switching transistor
- Board-space reduction
- Ultra small and flat lead SMD plastic package

3. Applications

· General-purpose switching and amplification

4. Quick reference data

Table 1. Quick	reference data					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per transistor	; for the PNP transistor	with negative polarity				
V _{CEO}	collector-emitter voltage	open base	-	-	40	V
I _C	collector current		-	-	200	mA
TR1 (NPN)		·				
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 10 mA; T _{amb} = 25 °C	100	180	300	
TR2 (PNP)		·				
h _{FE}	DC current gain	V _{CE} = -1 V; I _C = -10 mA; T _{amb} = 25 °C	100	180	300	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	6 5 4	C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2		$\left(\begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$
4	E2	emitter TR2		
5	B2	base TR2		 E1 B1 C2
6	C1	collector TR1	SOT666	sym019



6. Ordering information

Table 3. Ordering information					
Type number					
	Name	Description	Version		
PMBT3946VPN	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	<u>SOT666</u>		

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMBT3946VPN	ZE

8. Limiting values

Table 5. Limiting values

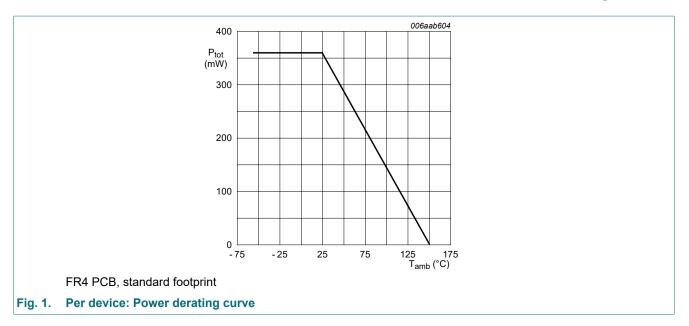
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
TR1 (NPN)		1				
V _{CBO}	collector-base voltage	open emitter		-	60	V
TR2 (PNP)						
V _{CBO}	collector-base voltage	open emitter		-	-40	V
Per transisto	or; for the PNP transistor wit	h negative polarity				
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
Ic	collector current			-	200	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	240	mW
Per device			·			
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	360	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

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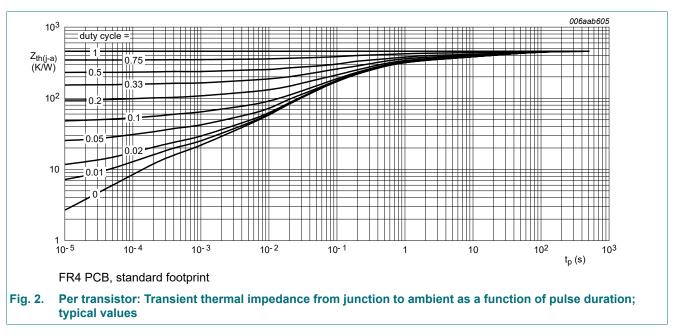


9. Thermal characteristics

Table 6. Thern	nal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor	r -						
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	521	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	100	K/W
Per device							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	347	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.



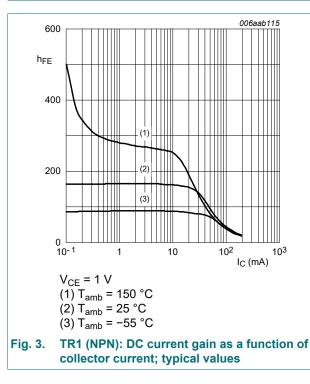
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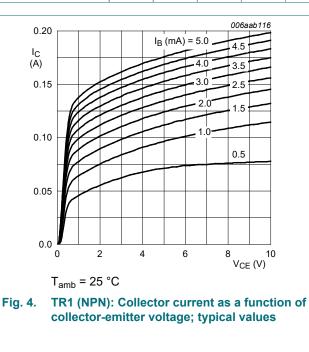
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
TR1 (NPN)						
I _{CBO}	collector-base cut-off current	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	50	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 6 V; I _C = 0 A; T _{amb} = 25 °C	-	-	50	nA
h _{FE}	DC current gain	V_{CE} = 1 V; I _C = 0.1 mA; T _{amb} = 25 °C	60	180	-	
		V _{CE} = 1 V; I _C = 1 mA; T _{amb} = 25 °C	80	180	-	
		V _{CE} = 1 V; I _C = 10 mA; T _{amb} = 25 °C	100	180	300	
		V _{CE} = 1 V; I _C = 50 mA; T _{amb} = 25 °C	60	105	-	
		V_{CE} = 1 V; I _C = 100 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	30	50	-	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 1 mA; T _{amb} = 25 °C	-	75	200	mV
	saturation voltage	I _C = 50 mA; I _B = 5 mA; T _{amb} = 25 °C	-	120	300	mV
V _{BEsat}	base-emitter saturation	I _C = 10 mA; I _B = 1 mA; T _{amb} = 25 °C	650	750	850	mV
	voltage	I _C = 50 mA; I _B = 5 mA; T _{amb} = 25 °C	-	850	950	mV
t _d	delay time	I _C = 10 mA; I _{Bon} = 1 mA; I _{Boff} = -1 mA;	-	-	35	ns
t _r	rise time	V _{CC} = 3 V; T _{amb} = 25 °C	-	-	35	ns
t _{on}	turn-on time	-	-	-	70	ns
t _s	storage time		-	-	200	ns
t _f	fall time		-	-	50	ns
t _{off}	turn-off time		-	-	250	ns
C _c	collector capacitance	$V_{CB} = 5 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ i}_{e} = 0 \text{ A}; \text{ f} = 1 \text{ MHz};$ $T_{amb} = 25 ^{\circ}\text{C}$	-	-	4	pF
C _e	emitter capacitance	$V_{EB} = 500 \text{ mV}; I_C = 0 \text{ A}; i_c = 0 \text{ A};$ f = 1 MHz; T _{amb} = 25 °C	-	-	8	pF
f _T	transition frequency	V _{CE} = 20 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C	300	-	-	MHz
NF	noise figure	V _{CE} = 5 V; I _C = 100 μA; R _S = 1 kΩ; f = 10 Hz to 15.7 kHz; T _{amb} = 25 °C	-	-	5	dB
TR2 (PNP)		· · ·	L.			
I _{CBO}	collector-base cut-off current	V _{CB} = -30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-50	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = -6 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-50	nA
h _{FE}	DC current gain	V _{CE} = -1 V; I _C = -0.1 mA; T _{amb} = 25 °C	60	180	-	
		V _{CE} = -1 V; I _C = -1 mA; T _{amb} = 25 °C	80	180	-	
		V _{CE} = -1 V; I _C = -10 mA; T _{amb} = 25 °C	100	180	300	
		V _{CE} = -1 V; I _C = -50 mA; T _{amb} = 25 °C	60	130	-	
		V_{CE} = -1 V; I _C = -100 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	30	50	-	
V _{CEsat}	collector-emitter	I _C = -10 mA; I _B = -1 mA; T _{amb} = 25 °C	-	-100	-250	V
	saturation voltage	I _C = -50 mA; I _B = -5 mA; T _{amb} = 25 °C	-	-165	-400	V

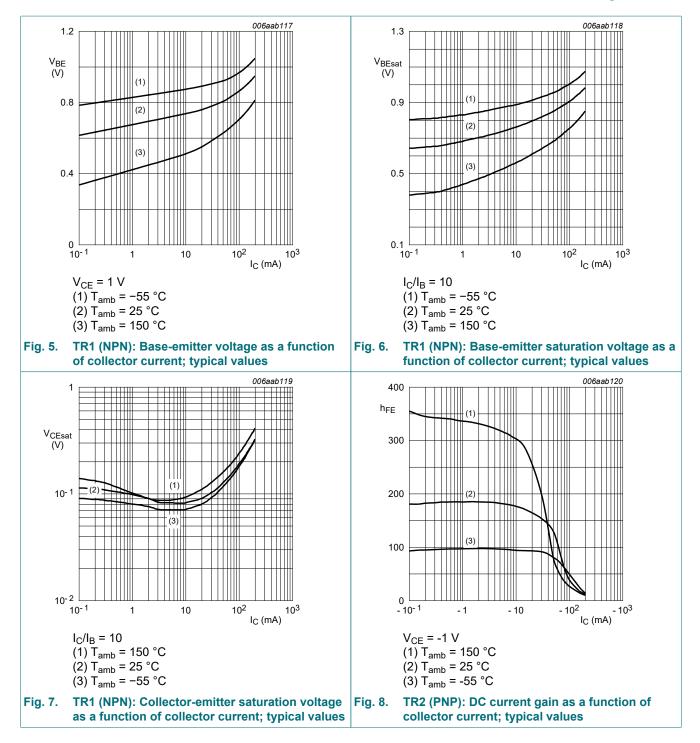
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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{BEsat}	base-emitter saturation	I _C = -10 mA; I _B = -1 mA; T _{amb} = 25 °C		-	-750	-850	V
	voltage	I _C = -50 mA; I _B = -5 mA; T _{amb} = 25 °C		-	-850	-950	V
t _d	delay time	I _C = -10 mA; I _{Bon} = -1 mA; I _{Boff} = 1 mA;		-	-	35	ns
t _r	rise time	V _{CC} = -3 V; T _{amb} = 25 °C		-	-	35	ns
t _{on}	turn-on time			-	-	70	ns
t _s	storage time			-	-	225	ns
t _f	fall time			-	-	75	ns
t _{off}	turn-off time			-	-	300	ns
C _c	collector capacitance	V_{CB} = -5 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	-	4.5	pF
C _e	emitter capacitance	V _{EB} = -500 mV; I _C = 0 mA; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	-	10	pF
f _T	transition frequency	V_{CE} = -20 V; I _C = -10 mA; f = 100 MHz; T _{amb} = 25 °C		250	-	-	MHz
NF	noise figure	V_{CE} = -5 V; I _C = -100 μA; R _S = 1 kΩ; f = 10 Hz to 15.7 kHz; T _{amb} = 25 °C		-	-	4	dB

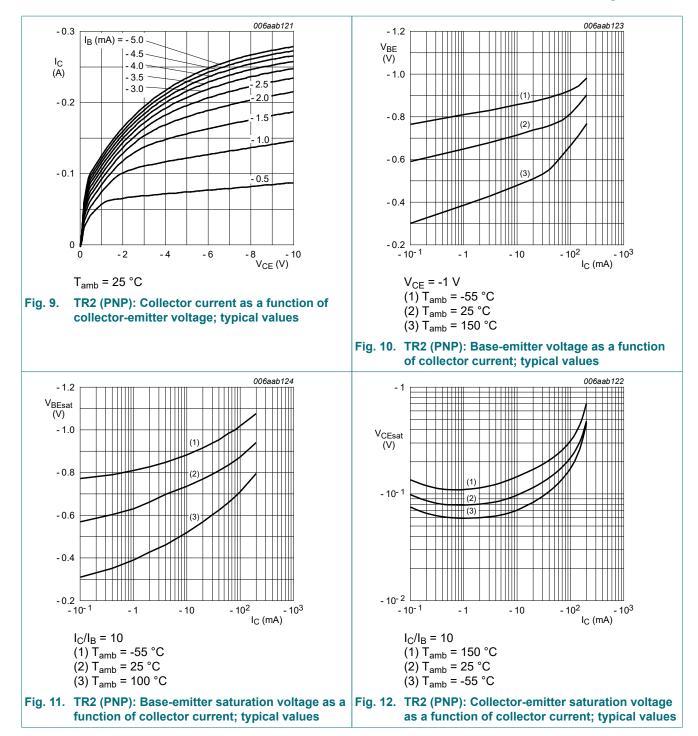




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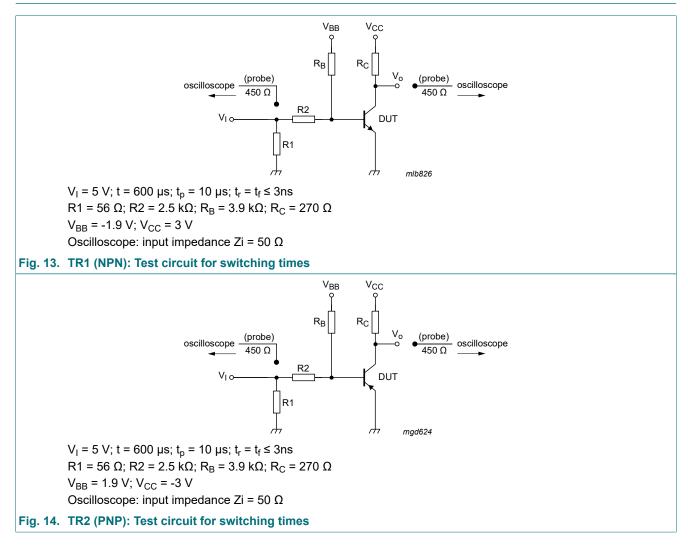


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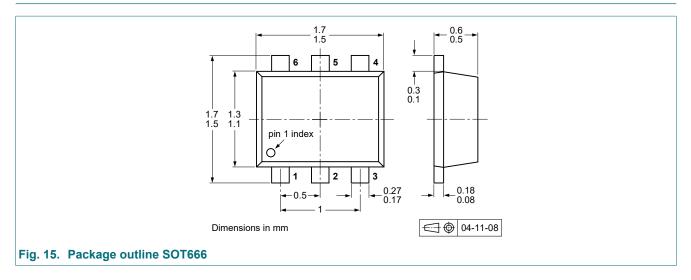


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11. Test information

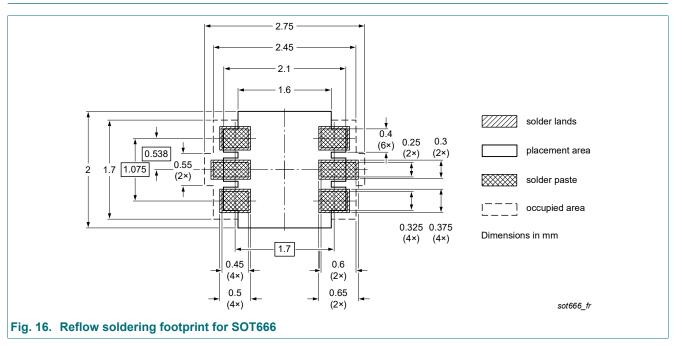


12. Package outline



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13. Soldering



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14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBT3946VPN v.2	20221228	Product data sheet	-	PMBT3946VPN v.1
Modifications:	Nexperia.Legal texts have bePacking information	ata sheet has been redes en adapted to the new co removed I to non-automotive qualifi	mpany name where ap	
PMBT3946VPN v.1	20090831	Product data sheet	-	-

PMBT3946VPN

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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