### 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

#### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 3 A
- Reverse voltage: V<sub>R</sub> ≤ 60 V
- Low forward voltage
- High power capability due to clip-bonding technology
- · Small and flat lead SMD plastic package
- High temperature T<sub>i</sub> ≤ 175 °C
- · Suitable for both reflow and wave soldering
- Qualified according to AEC-Q101 and recommended for use in automotive applications

# 3. Applications

- · Low voltage rectification
- · High efficiency DC-to-DC conversion
- · Switch mode power supply
- · Reverse polarity protection

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
IF	forward current	T <sub>sp</sub> = 160 °C		-	-	4.2	Α
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; square wave; T <sub>amb</sub> ≤ 80 °C	[1]	-	-	3	А
		$\delta$ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> $\leq$ 165 °C		-	-	3	А
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	60	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 3 A; T <sub>j</sub> = 25 °C		-	460	530	mV
I <sub>R</sub>	reverse current	$V_R = 60 \text{ V}; t_p \le 300 \text{ µs}; \delta \le 0.02;$ $T_j = 25 \text{ °C}; \text{ pulsed}$		-	80	200	μΑ
t <sub>rr</sub>	reverse recovery time	$I_F = 0.5 \text{ A}$ ; $I_R = 0.5 \text{ A}$ ; $I_{R(meas)} = 0.1 \text{ A}$ ; $I_{j} = 25 \text{ °C}$		-	12	-	ns

[1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		к- <del>][</del> - А
2	А	anode	1 2 2 CFP5 (SOD128)	sym001

<sup>[1]</sup> The marking bar indicates the cathode.

## 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
PMEG6030ETP-Q		plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	SOD128		

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG6030ETP-Q	DA

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## 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	60	V
I <sub>F</sub>	forward current	T <sub>sp</sub> = 160 °C		-	4.2	А
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; square wave; $T_{amb} \le$ 80 °C	[1]	-	3	А
		$\delta$ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> ≤ 165 °C		-	3	А
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; square wave; $T_{j(init)}$ = 25 °C		-	50	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2]	-	750	mW
			[3]	-	1.25	W
			[1]	-	2.5	W
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

- [1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ιι ( <u>)</u> -α <i>)</i>	thermal resistance from		[1] [2]	-	-	200	K/W
	junction to ambient		[1] [3]	-	-	120	K/W
			[1] [4]	-	-	60	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[5]	-	-	12	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [4] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [5] Soldering point of cathode tab.

**Product data sheet** 

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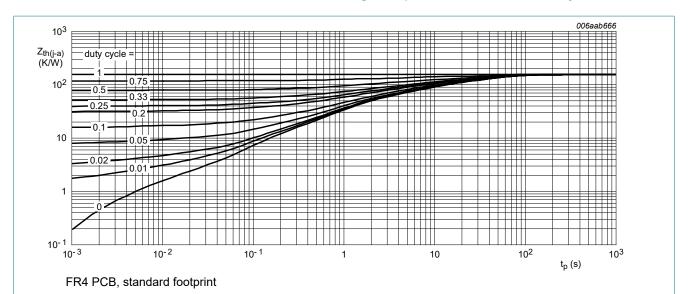
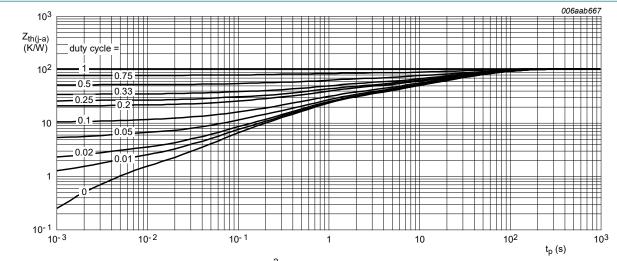
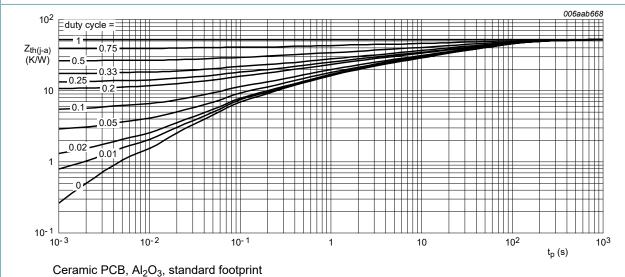


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



PMEG6030ETP-Q

Fig. 3.

Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

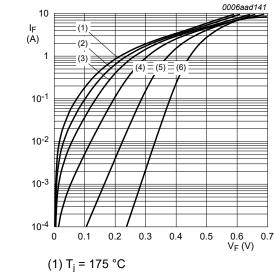
## 10. Characteristics

### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
√ <sub>F</sub>	forward voltage	I <sub>F</sub> = 0.1 A; T <sub>j</sub> = 25 °C	-	290	330	mV
		I <sub>F</sub> = 0.5 A; T <sub>j</sub> = 25 °C	-	340	400	mV
		I <sub>F</sub> = 1 A; T <sub>j</sub> = 25 °C	-	380	440	mV
		I <sub>F</sub> = 1.5 A; T <sub>j</sub> = 25 °C	-	400	470	mV
		I <sub>F</sub> = 2 A; T <sub>j</sub> = 25 °C	-	430	500	mV
		I <sub>F</sub> = 3 A; T <sub>j</sub> = 25 °C	-	460	530	mV
		I <sub>F</sub> = 3 A; T <sub>j</sub> = -40 °C	-	510	590	mV
		I <sub>F</sub> = 3 A; T <sub>j</sub> = 125 °C	-	405	480	mV
		I <sub>F</sub> = 3 A; T <sub>j</sub> = 150 °C	-	390	460	mV
		I <sub>F</sub> = 3 A; T <sub>j</sub> = 175 °C	-	370	450	mV
I <sub>R</sub>	reverse current	$V_R = 5 \text{ V}; t_p \le 300 \text{ µs}; \delta \le 0.02;$ $T_j = 25 \text{ °C}; \text{ pulsed}$	-	4	-	μΑ
		$V_R = 10 \text{ V; } t_p \le 300  \mu\text{s; } \delta \le 0.02;$ $T_j = 25 ^\circ\text{C; } \text{ pulsed}$	-	5	-	μA
		$V_R = 60 \text{ V}; t_p \le 300  \mu\text{s}; \delta \le 0.02;$ $T_j = 25 ^{\circ}\text{C}; \text{ pulsed}$	-	80	200	μΑ
		$V_R$ = 60 V; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = -40 °C; pulsed	-	0.5	10	μA
		$V_R = 60 \text{ V; } t_p \le 300  \mu\text{s; } \delta \le 0.02;$ $T_j = 125 \text{ °C; pulsed}$	-	45	150	mA
$C_d$	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	360	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	120	-	pF
rr	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$	-	12	-	ns
V <sub>FRM</sub>	peak forward recovery voltage	I <sub>F</sub> = 1 A; dI <sub>F</sub> /dt = 40 A/μs; T <sub>j</sub> = 25 °C	-	425	-	mV

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#### High-temperature 60 V, 3 A Schottky barrier rectifier



(2)  $T_i = 150 \,^{\circ}\text{C}$ 

 $(3) T_i = 125 °C$ 

 $(4) T_i = 85 ^{\circ}C$ 

(5)  $T_i = 25 °C$ (6)  $T_i = -40 \, ^{\circ}\text{C}$ 

Fig. 4. Forward current as a function of forward voltage; typical values

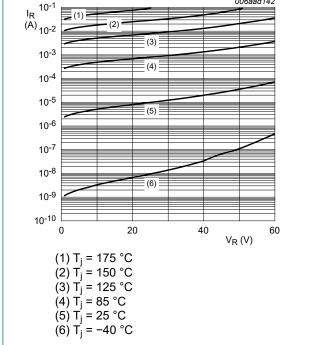
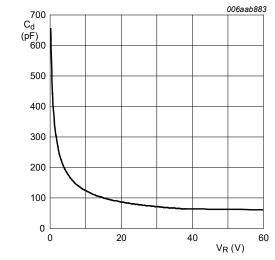
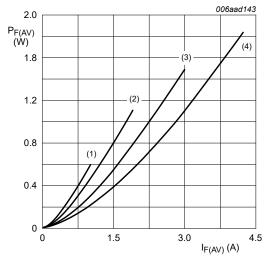


Fig. 5. Reverse current as a function of reverse voltage; typical values



 $f = 1 MHz; T_{amb} = 25 °C$ 

Diode capacitance as a function of reverse Fig. 6. voltage; typical values



T<sub>i</sub> = 175 °C

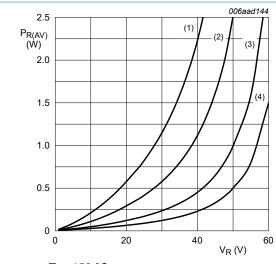
 $(1) \delta = 0.1$ 

 $(2) \delta = 0.2$ 

 $(3) \delta = 0.5$ 

 $(4) \delta = 1$ 

Fig. 7. Average forward power dissipation as a function of average forward current; typical values



 $T_j = 150 \,^{\circ}C$ 

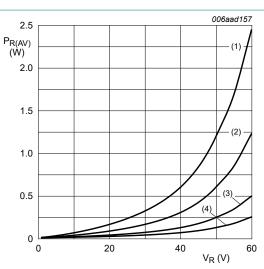
 $(1) \delta = 1$ 

 $(2) \delta = 0.5$ 

 $(3) \delta = 0.2$ 

 $(4) \delta = 0.1$ 

Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values



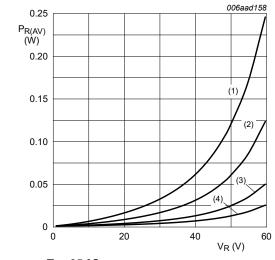
T<sub>j</sub> = 125 °C

 $(1) \delta = 1$ 

 $(2) \delta = 0.5$ 

 $(3) \delta = 0.2$  $(4) \delta = 0.1$ 

Fig. 9. Average reverse power dissipation as a function of reverse voltage; typical values



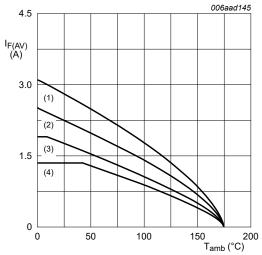
T<sub>i</sub> = 85 °C

 $(1) \delta = 1$ 

 $(2) \delta = 0.5$ 

 $(3) \delta = 0.2$  $(4) \delta = 0.1$ 

Fig. 10. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

T<sub>i</sub> = 175 °C

 $(1) \delta = 1 (DC)$ 

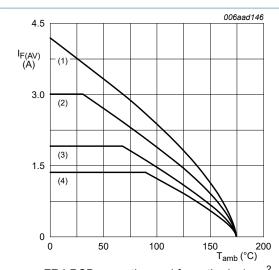
(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values

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FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

 $T_i = 175 \,{}^{\circ}\text{C}$ 

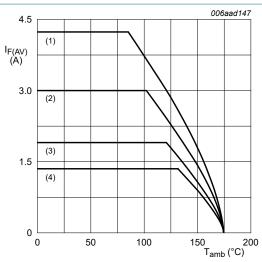
 $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 12. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

T<sub>i</sub> = 175 °C

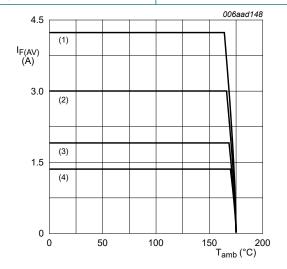
 $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 13. Average forward current as a function of ambient temperature; typical values



T<sub>i</sub> = 175 °C

 $(1) \delta = 1 (DC)$ 

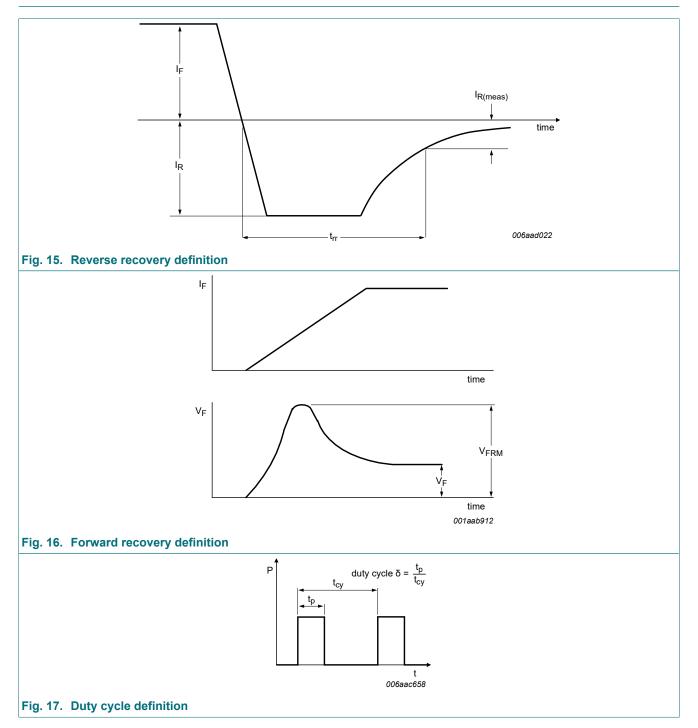
(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta$  = 0.1; f = 20 kHz

Fig. 14. Average forward current as a function of solder point temperature; typical values

### 11. Test information



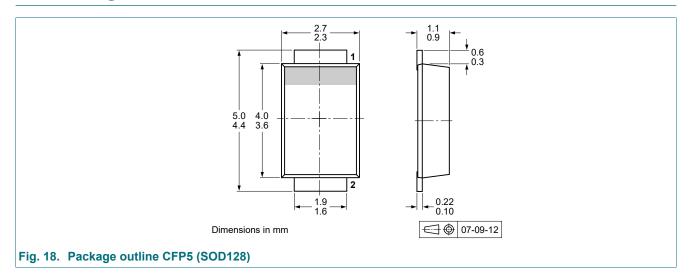
The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

#### **Quality information**

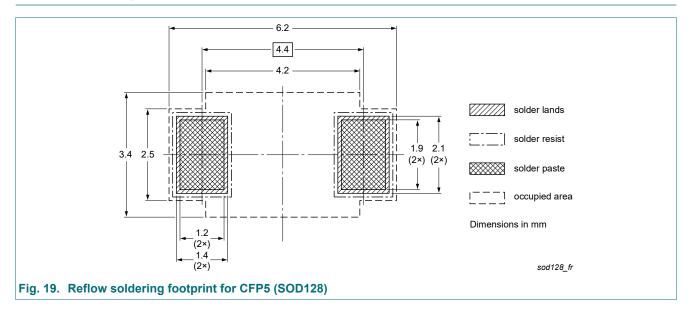
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

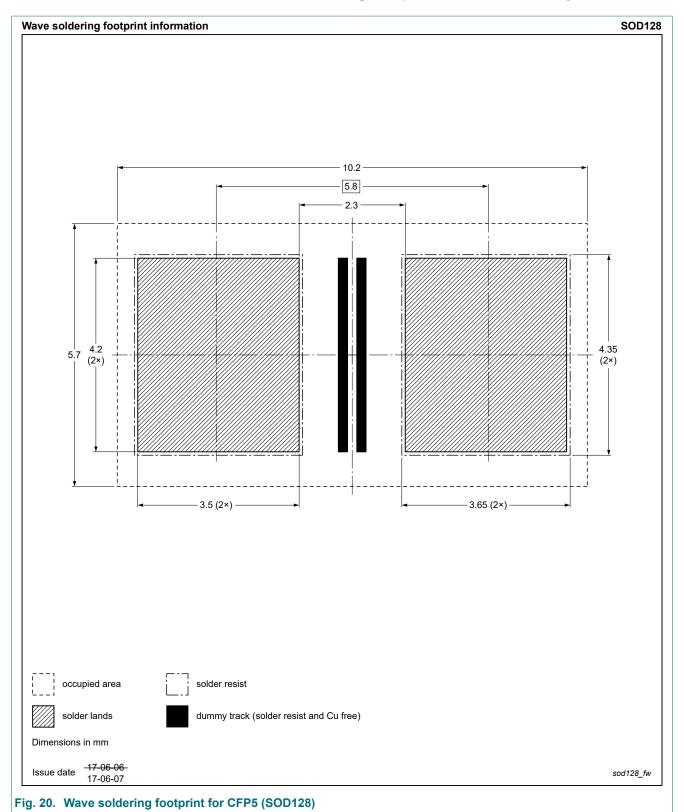
Downloaded From Oneyac.com

# 12. Package outline



## 13. Soldering





# 14. Revision history

#### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG6030ETP-Q v.1	20210719	Product data sheet	-	-

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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