1-bit bidirectional multi-voltage level translator; open-drain; push-pull

Rev. 3 — 8 February 2022

Product data sheet

1. General description

The LSF0101 is an 1 channel bidirectional multi-voltage level translator for open-drain and push-pull applications. It supports up to 100 MHz up translation and \geq 100 MHz down translation at \leq 30 pF capacitive load. There is no need for a direction pin which minimizes system effort. The LSF0101 supports 5 V tolerant I/O pins for compatibility with TTL levels in a variety of applications. The ability to set up different voltage translation levels on each channel makes the device very flexible and suitable for a lot of different applications.

2. Features and benefits

- Bidirectional voltage translation with no direction pin
- Up translation
 - ≤ 100 MHz; C_L = 30 pF
 - ≤ 40 MHz; C_L = 50 pF
- Down translation
 - ≥ 100 MHz; C_L = 30 pF
 - ≤ 40 MHz; C_L = 50 pF
- Hot insertion
- Bidirectional voltage level translation between:
 - 0.95 V and 1.8 V, 2.5 V, 3.3 V and 5.0 V
 - 1.2 V and 1.8 V, 2.5 V, 3.3 V and 5.0 V
 - 1.8 V and 2.5 V, 3.3 V and 5.0 V
 - 2.5 V and 3.3 V and 5.0 V
 - 3.3 V and 5.0 V
- Low standby current
- 5 V tolerant I/O pins to support TTL
- Low R_{ON} provides less signal distortion
- High-impedance I/O pins for EN = Low.
- · Flow-through pinout for easy PCB trace routing.
- Latch-up performance exceeds 100 mA per JESD78 class II level A
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 exceeds 2000 V
 - CDM ANSI/ESDA/JEDEC JS-002 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +125 °C

3. Applications

- GPIO, MDIO, PMBus, SMBus, SDIO, UART, I²C, and other interfaces in Telecom infrastructure
- Industrial
- Personal computing



4. Ordering information

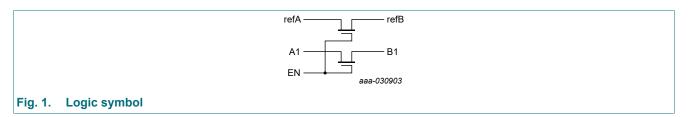
Type number	Package						
	Temperature range	Name	Description	Version			
LSF0101GW	-40 °C to +125 °C	TSSOP6	plastic thin shrink small outline package; 6 leads; body width 1.25 mm	SOT363-2			
LSF0101GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886			
LSF0101GX	-40 °C to +125 °C	X2SON6	plastic thermal enhanced extremely thin small outline package; no leads; 6 terminals; body 1.0 × 0.8 × 0.32 mm	SOT1255-2			

5. Marking

Table 2. Marking				
Type number	Marking code[1]			
LSF0101GW	h1			
LSF0101GM	h1			
LSF0101GX	h1			

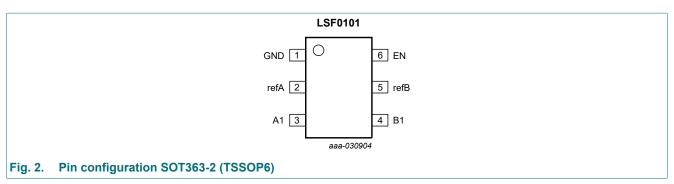
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

6. Functional diagram

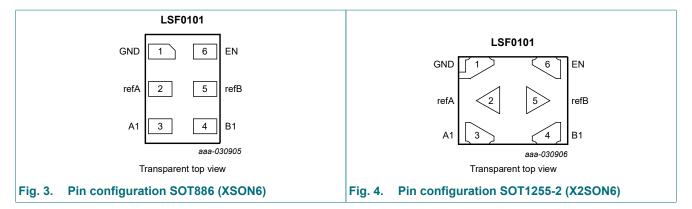


7. Pinning information

7.1. Pinning



1-bit bidirectional multi-voltage level translator; open-drain; push-pull



7.2. Pin description

Table 3. Pin description	1	1
Symbol	Pin	Description
GND	1	ground (0 V)
refA	2	reference voltage A
A1	3	data input/output A
B1	4	data input/output B
refB	5	reference voltage B
EN	6	enable input (active HIGH)

8. Functional description

Table 4. Function table

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H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF-state.

Input	input/output
EN	A1, B1 channel
Н	A1 = B1
L	Z

9. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
VI	input voltage	pins refA, refB, A1, B1 and EN	[1]	-0.5	+7.0	V
I _{I/O}	input/ouput current	pins refA, refB, A1 and B1; continuous channel current		-	+128	mA
I _{IK}	input clamping current	V ₁ < 0 V		-50	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T_{amb} = -40 °C to +125 °C	[2]	-	250	mW

[1] The minimum input voltage rating may be exceeded if the input current rating is observed.

[2] For SOT363-2 (TSSOP6) package: P_{tot} derates linearly with 3.7 mW/K above 83 °C.

For SOT886 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

For SOT1255-2 (X2SON6) package: P_{tot} derates linearly with 3.3 mW/K above 75 °C.

LSF0101

10. Recommended operating conditions

Table 6. Recommended operating conditions							
Symbol	Parameter	Conditions	Min	Max	Unit		
VI	input voltage	pins refA, refB, A1, B1 and EN	0.0	5.0	V		
I _{I/O}	input/ouput current	pins refA, refB, A1 and B1; continuous channel current	-	+64	mA		
T _{amb}	ambient temperature		-40	+125	°C		

11. Static characteristics

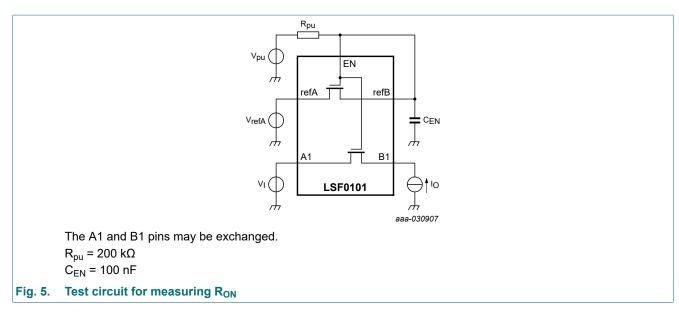
Table 7. Static characteristics

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	T _{amb} =	-40 °C to	+125 °C	Unit
				Typ[1]	Мах	
V _{IK}	input clamping voltage	V _{EN} = 0 V; I _I = -18 mA	-1.2	-	-	V
l _l	leakage current	pins A1, B1, refA, refB and EN; V _I = GND to 5.0 V	-	1	5	μA
Cı	input capacitance	pins refA, refB and EN; V _I = 0 V or 3 V	-	6	-	pF
C _{io(off)}	OFF-state input/output capacitance	pins A1, B1; V_0 = 0 V or 3 V; V_{EN} = 0.0 V	-	3.7	6.0	pF
C _{io(on)}	ON-state input/output capacitance	pins A1, B1; V_0 = 0 V or 3 V; V_{EN} = 3.0 V	-	6.0	12.5	pF
R _{ON}	ON resistance	see <u>Fig. 5</u> [2]				
		V _I = 0 V; V _{pu} = 5.0 V; I _O = 64 mA				
		V _{refA} = 3.3 V	-	3	-	Ω
		V _{refA} = 1.8 V	-	4	-	Ω
		V _{refA} = 1.0 V	-	7	-	Ω
		V _I = 0 V; V _{pu} = 5.0 V; I _O = 32mA				
		V _{refA} = 1.8 V	-	4	-	Ω
		V _{refA} = 2.5 V	-	3	-	Ω
		V _I = 1.8 V; V _{pu} = 5.0 V; I _O = 15 mA				
		V _{refA} = 3.3 V	-	4	-	Ω
		V _I = 1.0 V; V _{pu} = 3.3 V; I _O = 10 mA				
		V _{refA} = 1.8 V	-	7	-	Ω
		V _I = 0 V; V _{pu} = 3.3 V; I _O = 10 mA				
		V _{refA} = 1.0 V	-	5	-	Ω
		V _I = 0 V; V _{pu} = 1.8 V; I _O = 10 mA				
		V _{refA} = 1.0 V	-	6	-	Ω

[1] All typical values are measured at T_{amb} = 25 °C.

[2] Measured by the voltage drop between the An and Bn pins at the indicated current through the switch. ON resistance is determined by the lowest voltage of the two (An or Bn) pins.



12. Dynamic characteristics

Table 8. Switching characteristics

GND = 0 V; for waveform see Fig. 6; for test circuit see Fig. 7

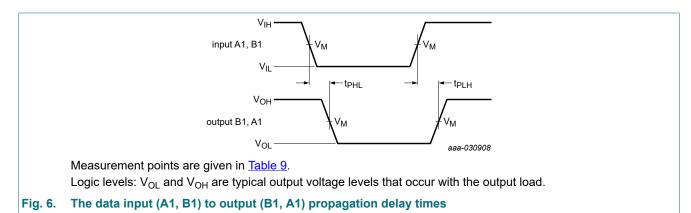
Symbol	Parameter	Conditions	T _{amb}	= -40 °C to +1	25 °C	Unit
			Min	Typ[1]	Мах	
Translati	ing down		I	<u> </u>		
t _{PLH}	LOW to HIGH	A1 to B1 or B1 to A1;				
	propagation delay	V _{IH} = V _{pu} = V _{refA} + 1 V				
		V _{refA} = 1.5 V; C _L = 15 pF	-	0.35	-	ns
		V _{refA} = 1.5 V; C _L = 30 pF	-	0.8	-	ns
		V _{refA} = 1.5 V; C _L = 50 pF	-	1.2	-	ns
		V _{refA} = 2.3 V; C _L = 15 pF	-	0.3	-	ns
		V _{refA} = 2.3 V; C _L = 30 pF	-	0.7	-	ns
		V _{refA} = 2.3 V; C _L = 50 pF	-	1.1	-	ns
t _{PHL}	HIGH to LOW propagation delay	A1 to B1 or B1 to A1;				
		V _{IH} = V _{pu} = V _{refA} + 1 V				
		V _{refA} = 1.5 V; C _L = 15 pF	-	0.5	-	ns
		V _{refA} = 1.5 V; C _L = 30 pF	-	1.0	-	ns
		V _{refA} = 1.5 V; C _L = 50 pF	-	1.3	-	ns
		V _{refA} = 2.3 V; C _L = 15 pF	-	0.4	-	ns
		V _{refA} = 2.3 V; C _L = 30 pF	-	0.8	-	ns
		V _{refA} = 2.3 V; C _L = 50 pF	-	1.2	-	ns

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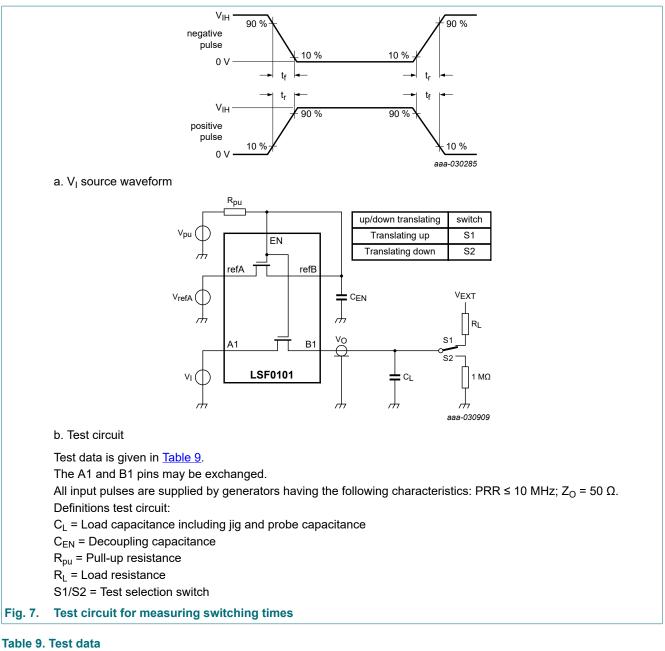
Symbol	Parameter	Conditions	T _{amb}	= -40 °C to +1	25 °C	Unit
			Min	Typ[1]	Max	
Translati	ing up					
t _{PLH}	LOW to HIGH	A1 to B1 or B1 to A1;				
	propagation delay	$V_{IH} = V_{refA}; V_{EXT} = V_{pu} = V_{refA} + 1 V$				
		V _{refA} = 1.5 V; C _L = 15 pF	-	0.5	-	ns
		V _{refA} = 1.5 V; C _L = 30 pF	-	0.9	-	ns
		V _{refA} = 1.5 V; C _L = 50 pF	-	1.1	-	ns
		V _{refA} = 2.3 V; C _L = 15 pF	-	0.4	-	ns
		V _{refA} = 2.3 V; C _L = 30 pF	-	0.8	-	ns
		V _{refA} = 2.3 V; C _L = 50 pF	-	1.0	-	ns
t _{PHL}	HIGH to LOW	A1 to B1 or B1 to A1;				
	propagation delay	$V_{IH} = V_{refA}; V_{EXT} = V_{pu} = V_{refA} + 1 V$				
		V _{refA} = 1.5 V; C _L = 15 pF	-	0.6	-	ns
		V _{refA} = 1.5 V; C _L = 30 pF	-	1.1	-	ns
		V _{refA} = 1.5 V; C _L = 50 pF	-	1.3	-	ns
		V _{refA} = 2.3 V; C _L = 15 pF	-	0.4	-	ns
		V _{refA} = 2.3 V; C _L = 30 pF	-	0.9	-	ns
		V _{refA} = 2.3 V; C _L = 50 pF	-	1.0	-	ns

[1] All typical values are measured at T_{amb} = 25 °C.

12.1. Waveforms and test circuit



1-bit bidirectional multi-voltage level translator; open-drain; push-pull



Load Output Input Vм Vм CL C_{EN} [1] R_L [1] t_r, t_f R_{pu} 0.5V_{refA} 0.5V_{refA} 300 Ω 200 kΩ ≤ 2 ns 15 pF, 30 pF, 50 pF 100 nF

[1] All typical values are measured at T_{amb} = 25 °C.

13. Package outline

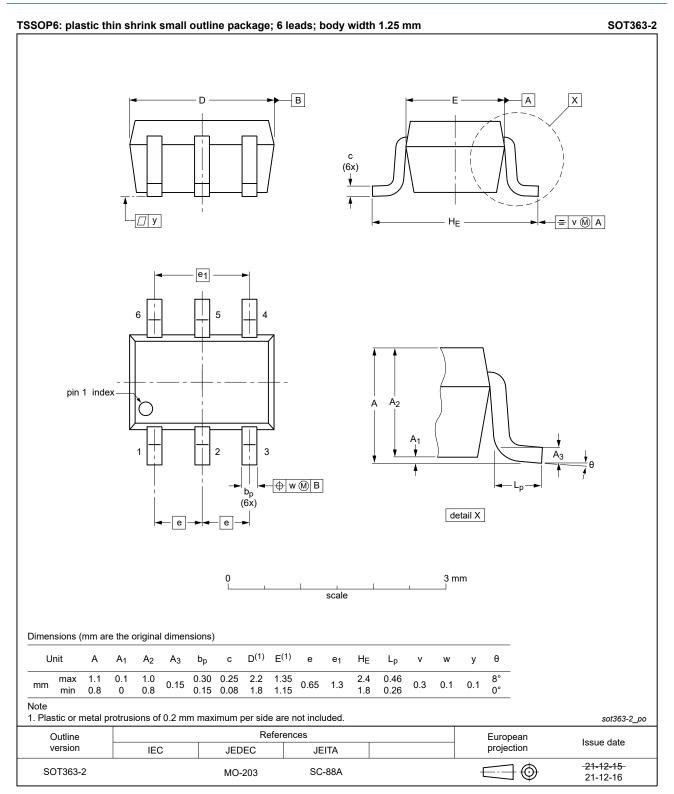


Fig. 8. Package outline SOT363-2 (TSSOP6)

1-bit bidirectional multi-voltage level translator; open-drain; push-pull

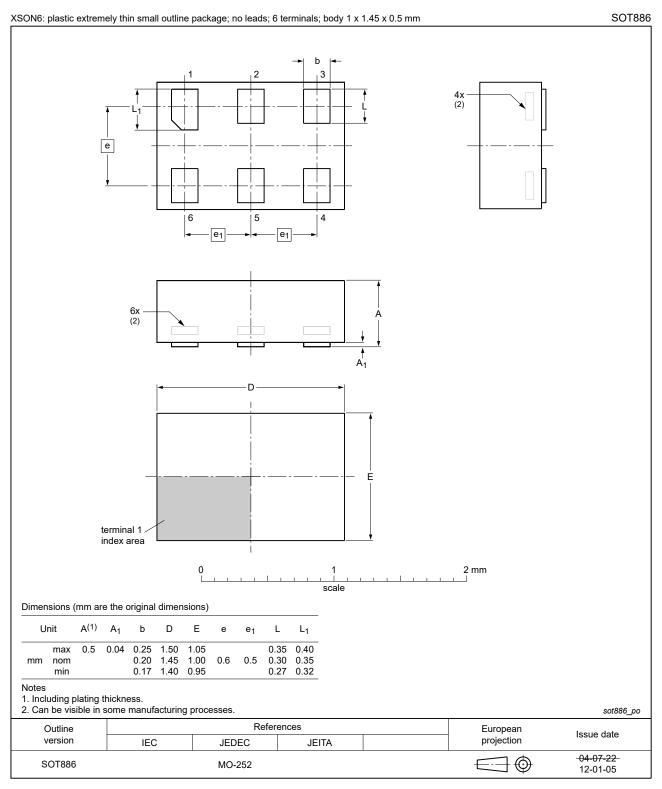
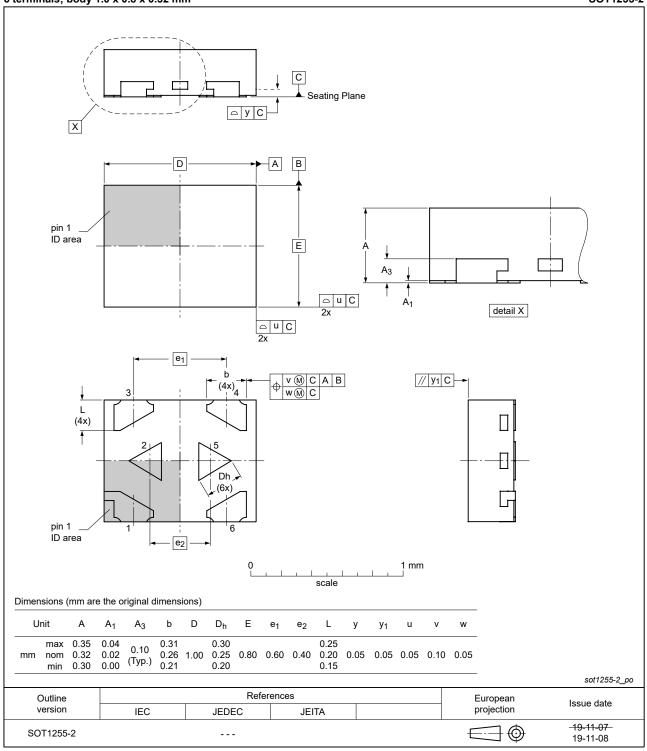


Fig. 9. Package outline SOT886 (XSON6)

1-bit bidirectional multi-voltage level translator; open-drain; push-pull

X2SON6: plastic thermal enhanced extremely thin small outline package; no leads; 6 terminals; body $1.0 \times 0.8 \times 0.32$ mm

SOT1255-2





14. Abbreviations

Acronym	Description
CMOS	Complementary Metal Oxide Semiconductor
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
PRR	Pulse Rate Repetition
TTL	Transistor-Transistor Logic

15. Revision history

Table 11. Revision history **Document ID Release date** Data sheet status Change notice Supersedes LSF0101 v.3 20220208 Product data sheet LSF0101 v.2 _ Modifications: Package SOT363 (SC-88) changed to SOT363-2 (TSSOP6). LSF0101 v.2 20200923 Product data sheet LSF0101 v.1.1 Modifications: Type number LSF0101GW (SOT363/SC-88) added. • LSF0101 v.1.1 20200626 Product data sheet LSF0101 v.1 Modifications: Type number LSF0101GW (SOT363/SC-88) is in development and removed from this product data sheet. Preliminary data sheet is available upon request. LSF0101 v.1 20200414 Product data sheet

16. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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