PMCA14UN

12 V, N-channel Trench MOSFET

6 August 2020 | Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DSN1010-3 (SOT8007) Surface-Mounted Device (SMD) package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Very fast switching
- Ultra small package: 0.96 × 0.96 × 0.24 mm
- Trench MOSFET technology

3. Applications

- Relay driver
- Battery management
- Low-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{DS}	drain-source voltage	T_j = 25 °C		-	-	12	V
V_{GS}	gate-source voltage			-8	-	8	V
I_D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-	14	A
Static characteristics							
R_{DSon}	drain-source on-state resistance	V_{GS} = 4.5 V; I_D = 5 A; T_j = 25 °C		-	13.2	16	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), 4 layer copper, tin-plated and mounting pad for drain 6 cm^2.

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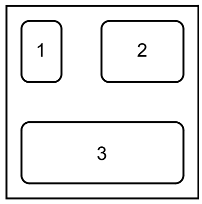
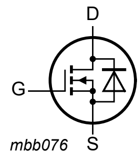
5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 Transparent top view **DSN1010-3 (SOT8007)**	
2	D	drain		
3	S	source		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMCA14UN	DSN1010-3	chip-scale package; 3 terminals; body 0.96 x 0.96 x 0.24 mm	SOT8007

7. Marking

Table 4. Marking codes

Type number	Marking code
PMCA14UN	L

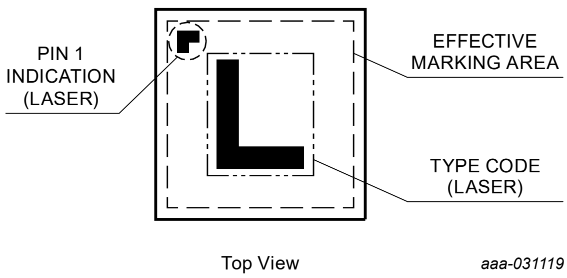


Fig. 1. DSN1010-3 (SOT8007) marking code description

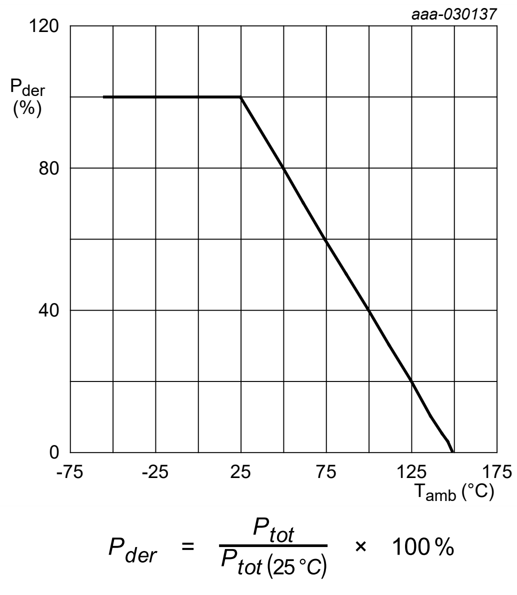
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

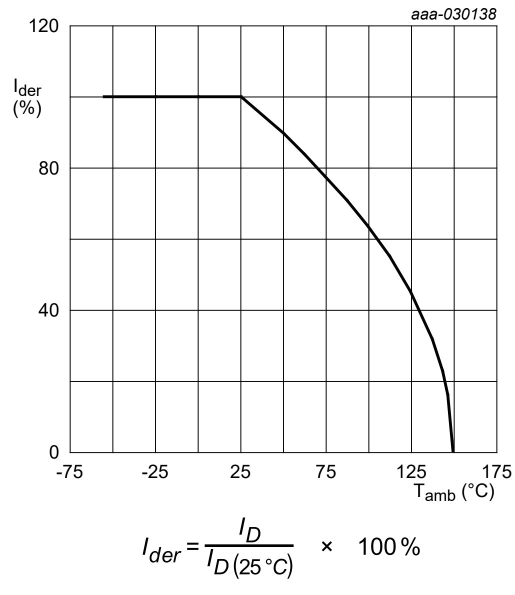
Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	T_j = 25 °C		-	12	V
V_{GS}	gate-source voltage			-8	8	V
I_D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	14	A
		V_{GS} = 4.5 V; T_{amb} = 25 °C	[1]	-	11	A
		V_{GS} = 4.5 V; T_{amb} = 100 °C	[1]	-	7	A
I_{DM}	peak drain current	T_{amb} = 25 °C; single pulse; t_p ≤ 10 µs		-	44	A
P_{tot}	total power dissipation	T_{amb} = 25 °C	[2]	-	1.2	W
			[1]	-	2.5	W
		T_{amb} = 25 °C; t ≤ 5 s	[1]	-	3.9	W
		T_{sp} = 25 °C		-	31	W
T_j	junction temperature			-55	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C
Source-drain diode						
I_S	source current	T_{amb} = 25 °C	[1]	-	1.2	A

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), 4 layer copper, tin-plated and mounting pad for drain 6 cm^2.
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), 4 layer copper, tin-plated and standard footprint.



$$P_{der} = \frac{P_{tot}}{P_{tot(25\,°C)}} \times 100\,\%$$

Fig. 2. Normalized total power dissipation as a function of ambient temperature



$$I_{der} = \frac{I_D}{I_{D(25\,°C)}} \times 100\,\%$$

Fig. 3. Normalized continous drain current as a function of ambient temperature

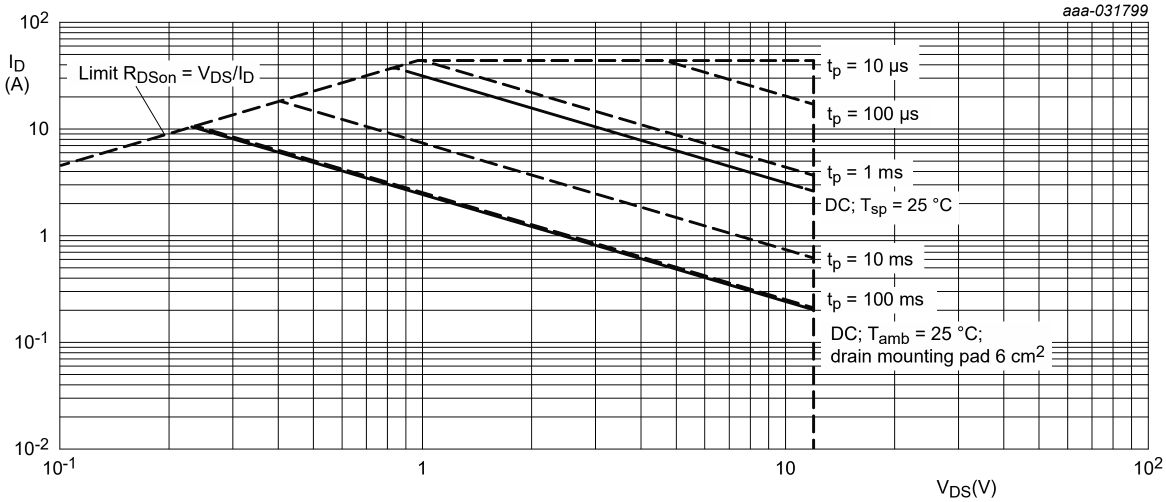


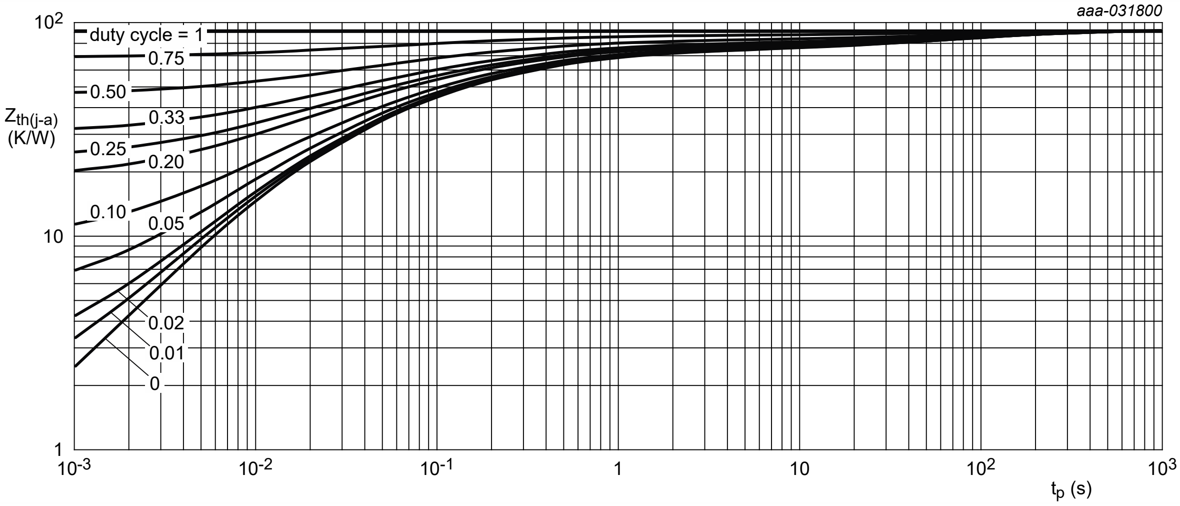
Fig. 4. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

9. Thermal characteristics

Table 6. Thermal characteristics

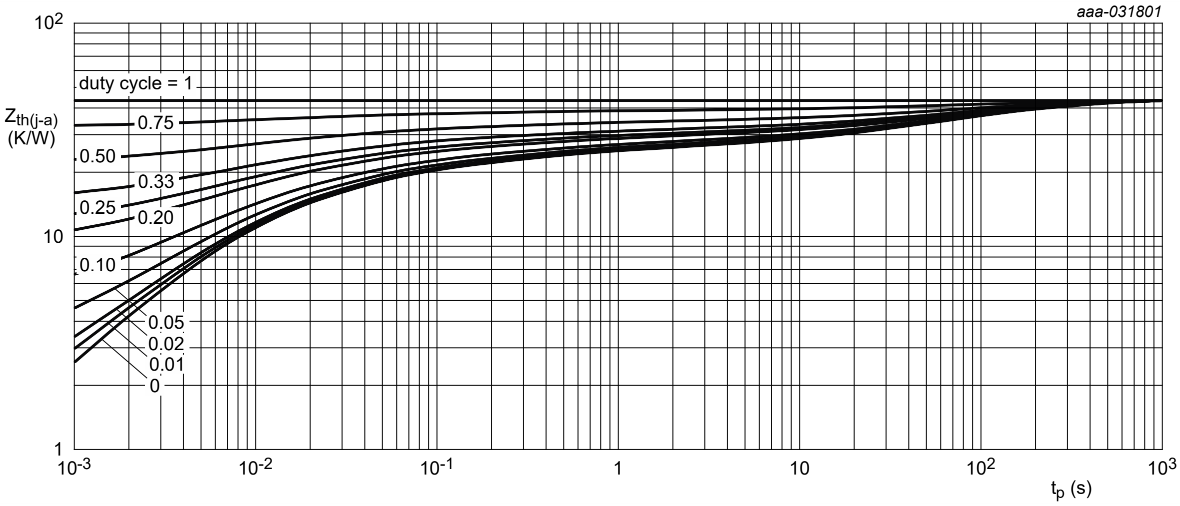
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	92	106	K/W
			[2]	-	43	50	K/W
		in free air; t ≤ 5 s	[2]	-	28	32	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	2	4	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), 4 layer copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), 4 layer copper, tin-plated and mounting pad for drain 6 cm^2.



FR4 PCB, 4 layer copper, standard footprint

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



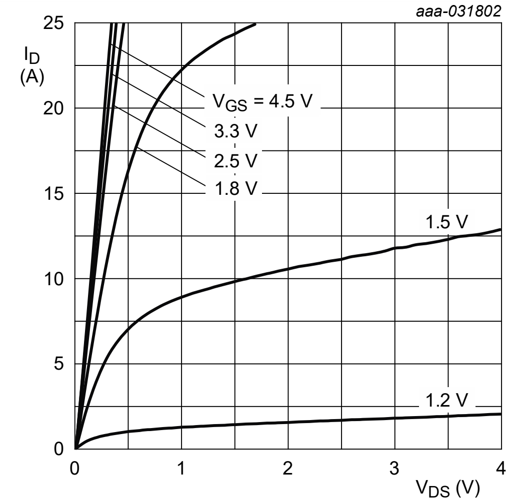
FR4 PCB, 4 layer copper, mounting pad for drain 6 cm^2

Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

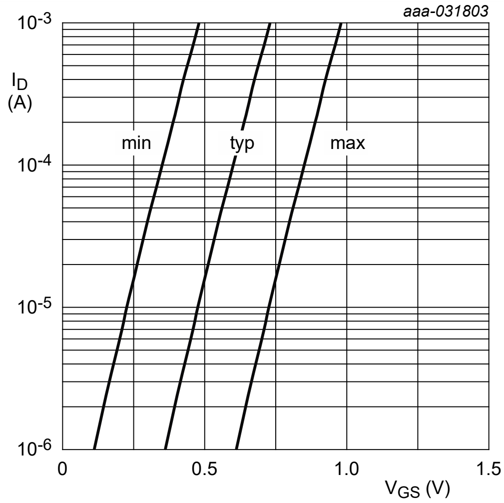
Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C		12	-	-	V
V_{GSth}	gate-source threshold voltage	I_D = 250 µA; V_{DS}=V_{GS}; T_j = 25 °C		0.4	0.6	0.9	V
I_{DSS}	drain leakage current	V_{DS} = 9.6 V; V_{GS} = 0 V; T_j = 25 °C		-	-	1	µA
I_{GSS}	gate leakage current	V_{GS} = 8 V; V_{DS} = 0 V; T_j = 25 °C		-	-	100	nA
		V_{GS} = -8 V; V_{DS} = 0 V; T_j = 25 °C		-	-	-100	nA
R_{DSon}	drain-source on-state resistance	V_{GS} = 4.5 V; I_D = 5 A; T_j = 25 °C		-	13.2	16	mΩ
		V_{GS} = 4.5 V; I_D = 5 A; T_j = 150 °C		-	17	21	mΩ
		V_{GS} = 3.3 V; I_D = 5 A; T_j = 25 °C		-	14.2	17	mΩ
		V_{GS} = 2.5 V; I_D = 5 A; T_j = 25 °C		-	16	21	mΩ
		V_{GS} = 1.8 V; I_D = 1 A; T_j = 25 °C		-	22	35	mΩ
g_{fs}	forward transconductance	V_{DS} = 6 V; I_D = 1 A; T_j = 25 °C		-	5.6	-	S
R_G	gate resistance	f = 1 MHz		-	1.5	-	Ω
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	V_{DS} = 6 V; I_D = 5 A; V_{GS} = 3.3 V; T_j = 25 °C		-	8	12	nC
Q_{GS}	gate-source charge			-	1.3	-	nC
Q_{GD}	gate-drain charge			-	3.2	-	nC
C_{iss}	input capacitance	V_{DS} = 6 V; f = 1 MHz; V_{GS} = 0 V; T_j = 25 °C		-	855	-	pF
C_{oss}	output capacitance			-	257	-	pF
C_{rss}	reverse transfer capacitance			-	237	-	pF
$t_{d(on)}$	turn-on delay time	V_{DS} = 6 V; I_D = 5 A; V_{GS} = 3.3 V; $R_{G(ext)}$ = 6 Ω; T_j = 25 °C		-	3	-	ns
t_r	rise time			-	6	-	ns
$t_{d(off)}$	turn-off delay time			-	16	-	ns
t_f	fall time			-	11	-	ns
Source-drain diode							
V_{SD}	source-drain voltage	I_S = 1.2 A; V_{GS} = 0 V; T_j = 25 °C		-	0.7	1.2	V



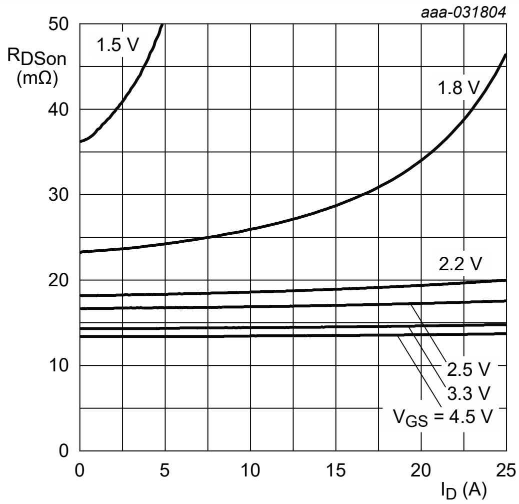
T_j = 25 °C

Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values



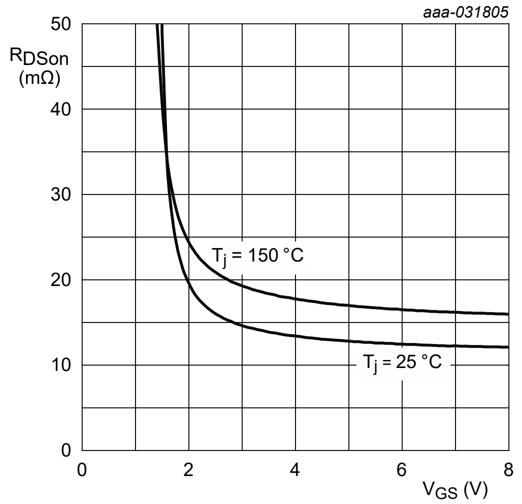
T_j = 25 °C; V_{DS} = 5 V

Fig. 8. Subthreshold drain current as a function of gate-source voltage



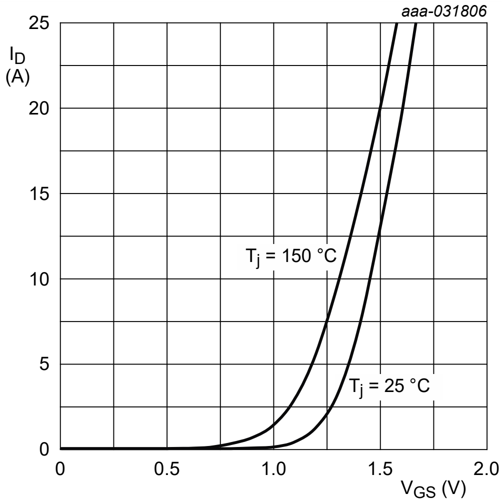
T_j = 25 °C

Fig. 9. Drain-source on-state resistance as a function of drain current; typical values



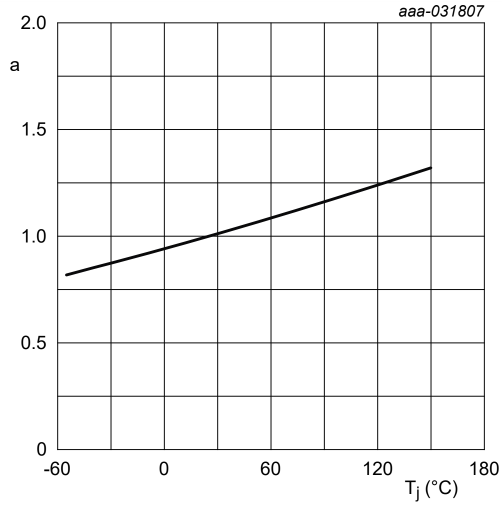
I_D = 5 A

Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values



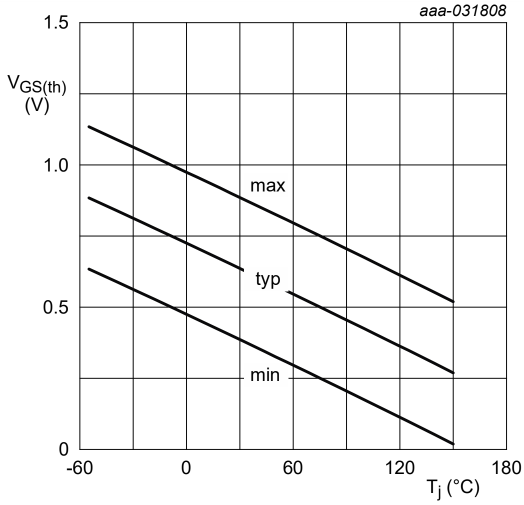
V_{DS} = 5 V

Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values



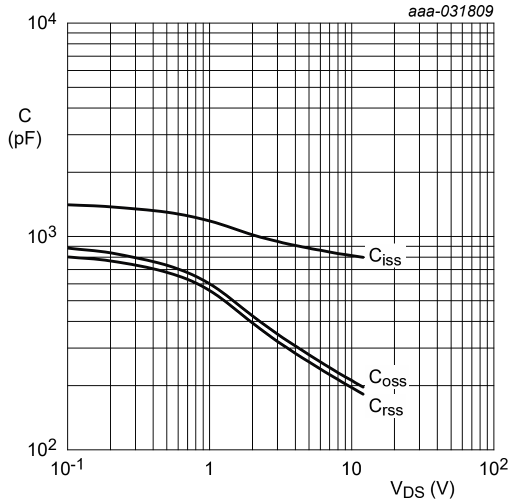
$$a = \frac{R_{DSon}}{R_{DSon(25°C)}}$$

Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values



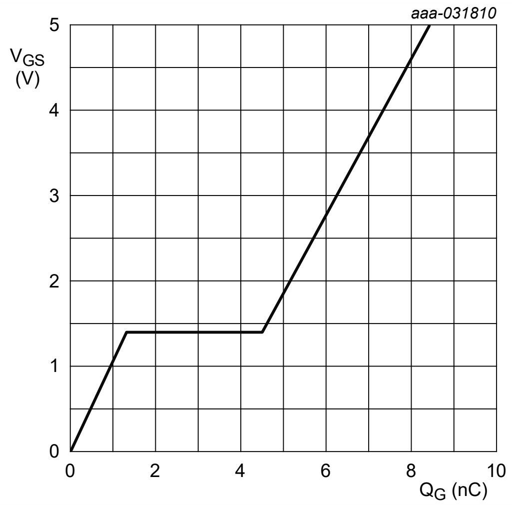
I_D = 250 µA; V_{DS} = V_{GS}

Fig. 13. Gate-source threshold voltage as a function of junction temperature



f = 1 MHz; V_{GS} = 0 V

Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



I_D = 5 A; V_{DS} =6 V; T_j = 25 °C

Fig. 15. Gate-source voltage as a function of gate charge; typical values

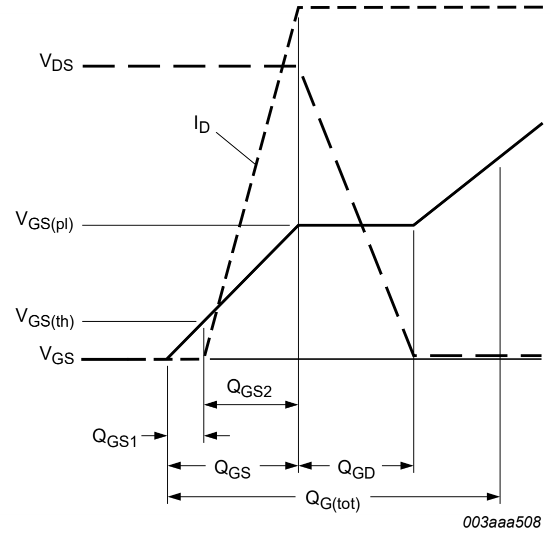
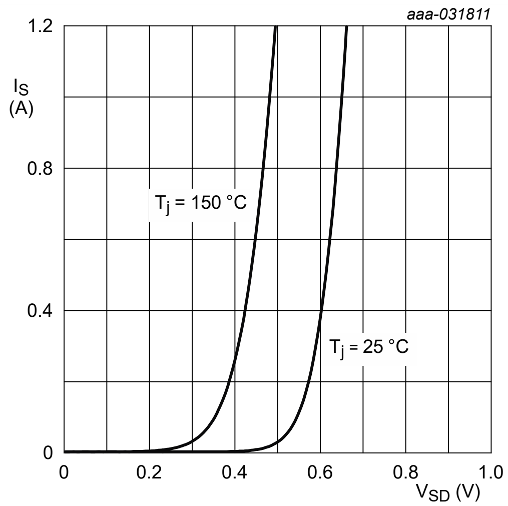


Fig. 16. Gate charge waveform definitions



V_{GS} = 0 V

Fig. 17. Source current as a function of source-drain voltage; typical values

11. Test information

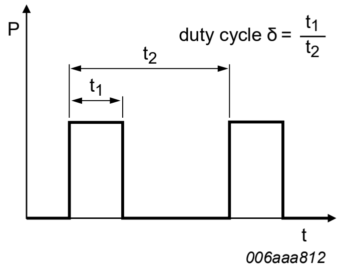
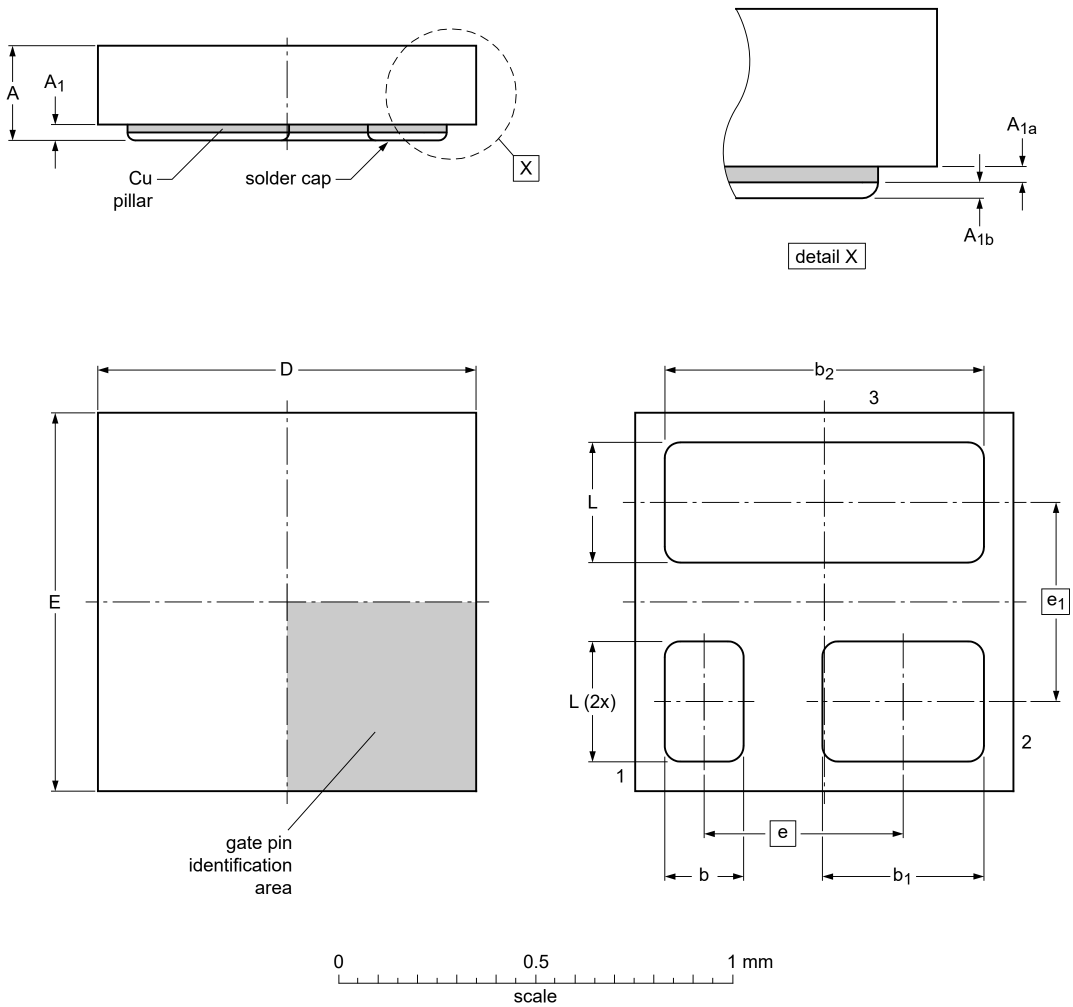


Fig. 18. Duty cycle definition

12. Package outline

DSN1010-3, chip-scale package; 3 terminals; body 0.96 x 0.96 x 0.24 mm **SOT8007**



Dimensions (mm are the original dimensions)

Unit[(1)]		A	A_1	A_{1a}	A_{1b}	b	b_1	b_2	D	E	e	e_1	L
mm	max	0.26	0.046	0.025	0.023	0.22	0.43	0.83	1.00	1.00			0.325
	nom	0.24	0.040	0.020	0.020	0.20	0.41	0.81	0.96	0.96	0.505	0.505	0.305
	min	0.22	0.034	0.015	0.017	0.18	0.39	0.79	0.92	0.92			0.285

Note
Dimension A is including coating foil thickness.

sot8007_po

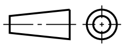
Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT8007		- - -				~~20-01-08~~ 20-03-02

Fig. 19. Package outline DSN1010-3 (SOT8007)

13. Soldering

Footprint information for reflow soldering of DSN1010-3 package **SOT8007**

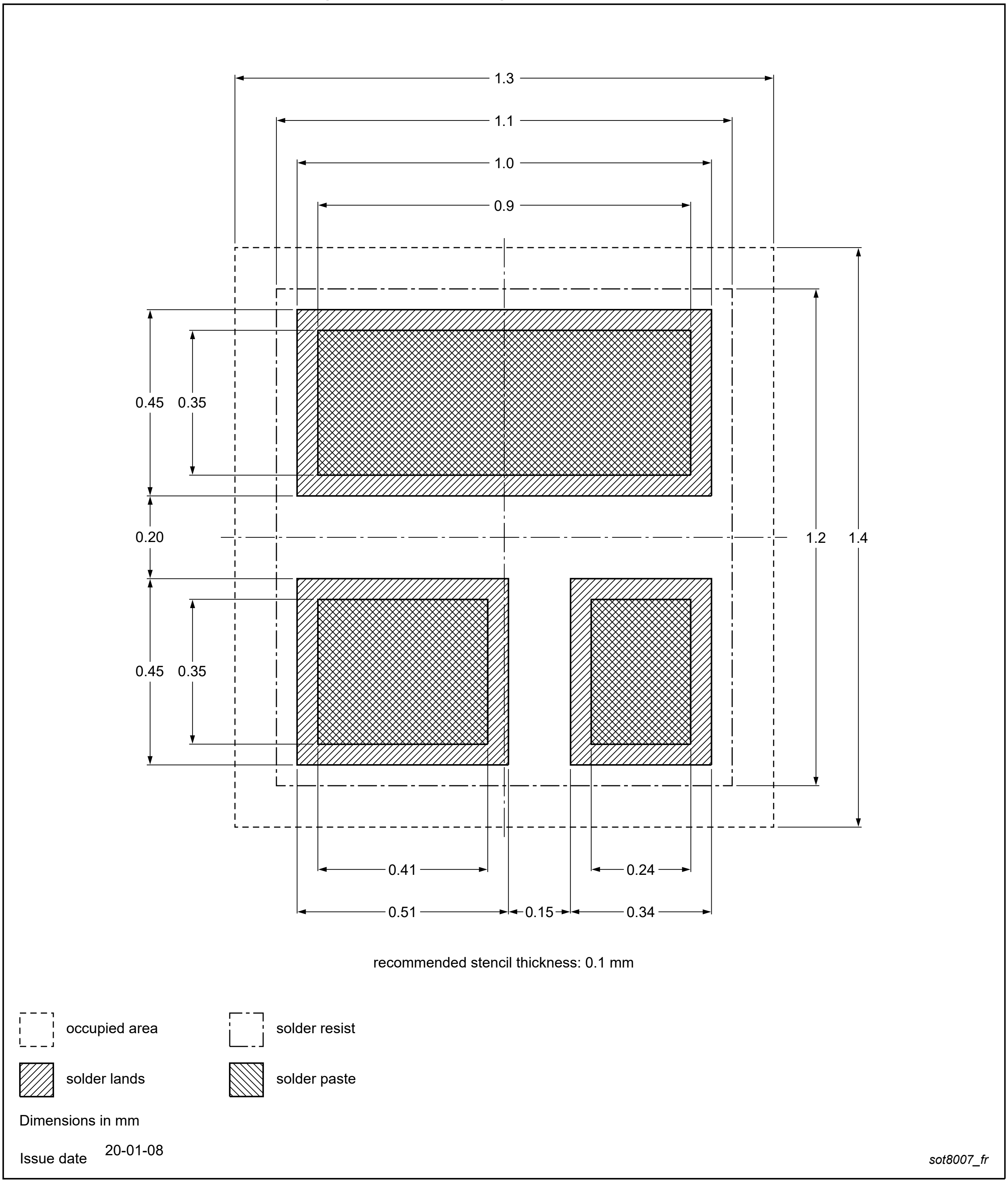


Fig. 20. Reflow soldering footprint for DSN1010-3 (SOT8007)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMCA14UN v.1	20200806	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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