

65 V, 100 mA PNP general-purpose transistor Rev. 1 — 22 September 2021

Product data sheet

1. General description

PNP general-purpose transistor in an ultra small DFN1110D-3 (SOT8015) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

Table 1. Product overview

Type number	Package		Package		NPN complement:
	Nexperia	JEDEC			
BC856AQB	SOT8015	MO-340BA	BC846AQB		
BC856BQB			BC846BQB		

2. Features and benefits

- High power dissipation capability
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- Smaller footprint compared to conventional leaded SMD packages
- Low package height of 0.5 mm

3. Applications

- General-purpose switching and amplification
- Space restricted applications

4. Quick reference data

Table 2. Quick reference data

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-65	V
I _C	collector current		-	-	-100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	-200	mA
h _{FE}	DC current gain		·			
	BC856AQB	V _{CE} = -5 V; I _C = -2 mA	125	-	250	
	BC856BQB		220	-	475	

nexperia

5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		ç
2	E	emitter	3	в
3	С	collector		
				E sym132
			Transparent top view	

6. Ordering information

Table 4. Ordering information						
Type number Package						
	Name	Description	Version			
BC856AQB	DFN1110D-3	plastic leadless extremely thin small outline package with	SOT8015			
BC856BQB		side-wettable flanks (SWF); 3 terminals; 0.65 mm pitch; body: 1.1 mm x 1.0 mm x 0.48 mm				

7. Marking

Table 5. Marking	
Type number	Marking code
BC856AQB	F4
BC856BQB	F5

8. Limiting values

Table 6. Limiting values

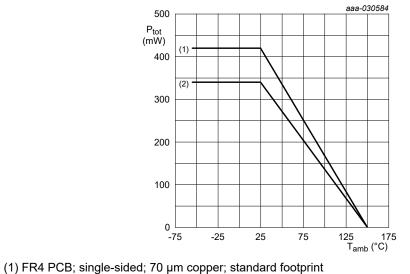
In accordance with the Absolute Maximum Rating System (IEC 60134).

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-80	V
V _{CEO}	collector-emitter voltage	open base		-	-65	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-200	mA
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	340	mW
			[2]	-	420	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided; 35 µm copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB; single-sided; 70 µm copper; tin-plated and standard footprint.



(1) FR4 FCB, single-sided, 70 μm copper, standard loopint
(2) FR4 PCB; single-sided; 35 μm copper; standard footprint

Fig. 1. Power derating curves DFN1110D-3 (SOT8015)

9. Thermal characteristics

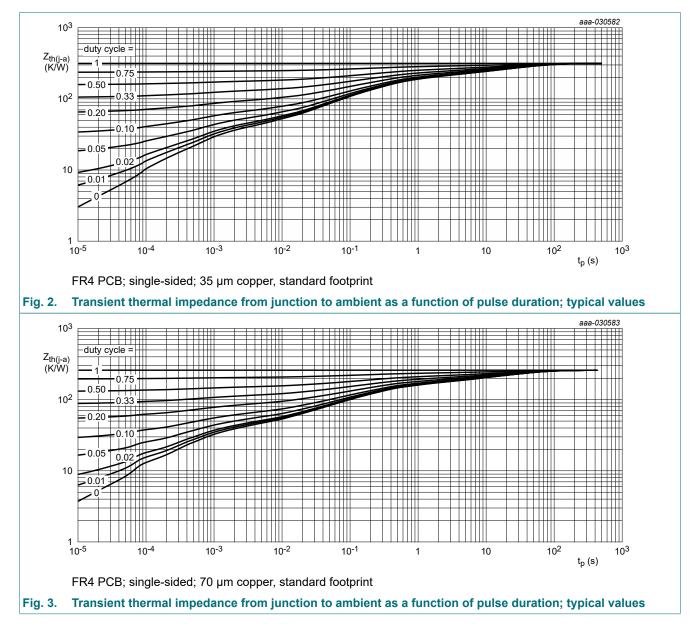
Table 7. Thermal characteristics

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	368	K/W
			[2]	-	-	298	K/W

[1] Device mounted on an FR4 PCB; single-sided; 35 µm copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB; single-sided; 70 µm copper; tin-plated and standard footprint.



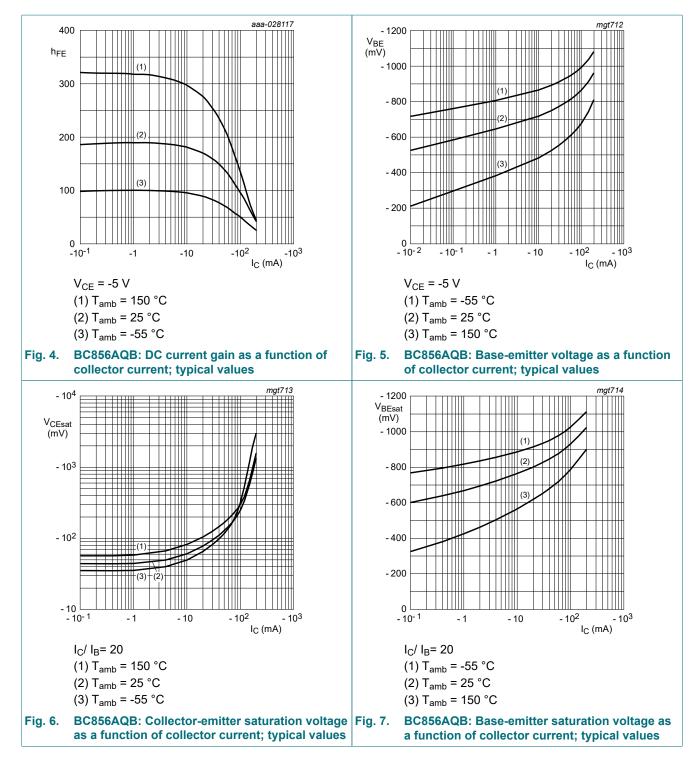
10. Characteristics

Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

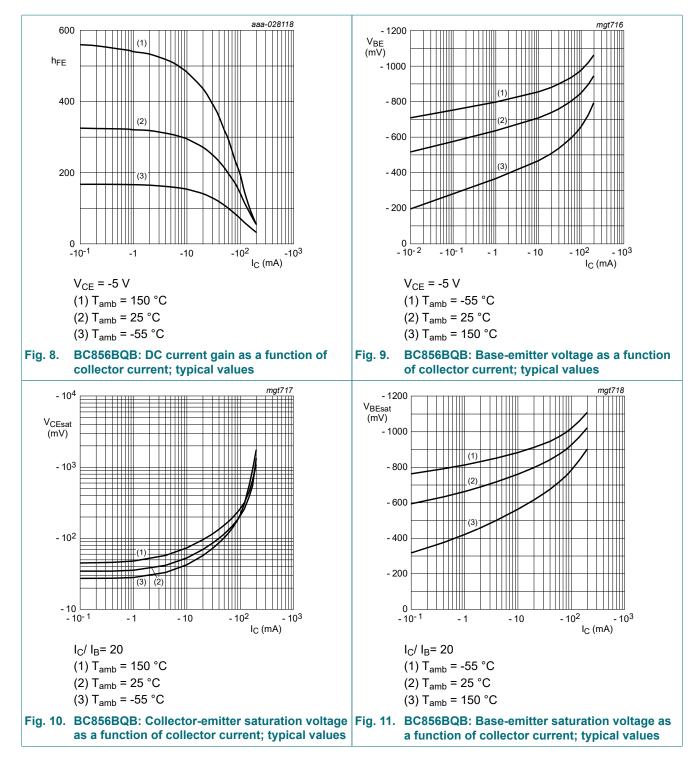
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A		-80	-	-	V
V _{(BR)CES}	collector-emitter peak voltage	I _C = -2 mA; I _E = 0 A		-65	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = -100 μA; I _C = 0 A		-6	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = -30 V; I _E = 0 A		-	-	-15	nA
	current	V _{CB} = -30 V; I _E = 0 A; T _j = 150 °C		-	-	-5	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$ -		-	-	-100	nA
h _{FE}	DC current gain						
BC	BC856AQB	$V_{CE} = -5 \text{ V}; I_{C} = -2 \text{ mA}$		125	-	250	
	BC856BQB			220	-	475	
V _{CEsat} collector-emitter		I _C = -10 mA; I _B = -0.5 mA		-	-	-300	mV
	saturation voltage	I _C = -100 mA; I _B = -5 mA	[1]	-	-	-650	mV
V _{BE}	base-emitter voltage	V _{CE} = -5 V ; I _C = -2 mA	[2]	-600	-	-750	mV
		V _{CE} = -5 V ; I _C = -10 mA	[2]	-	-	-820	mV
V _{BEsat}	base-emitter saturation	I _C = -10 mA ; I _B = -0.5 mA		-	-700	-	mV
	voltage	I _C = -100 mA ; I _B = -5 mA	[1]	-	-850	-	mV
f _T	transition frequency	V _{CE} = -5 V; I _C = -10 mA; f = 100 MHz		100	-	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = i _e = 0 A; f = 1 MHz		-	2	-	pF
C _e	emitter capacitance	V _{EB} = -0.5 V; I _C = i _c = 0 A; f = 1 MHz		-	10	-	pF
NF	noise figure	V _{CE} = -5 V; I _C = -200 μA; R _S = 2 kΩ; f = 1 kHz; B = 200 Hz		-	-	10	dB

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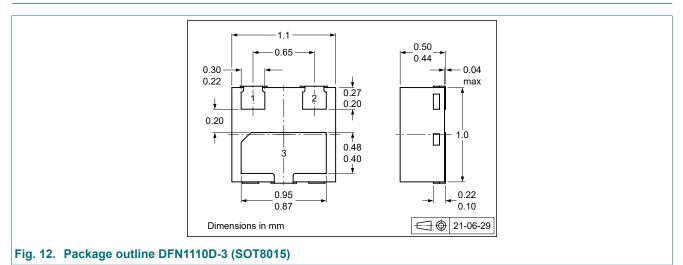
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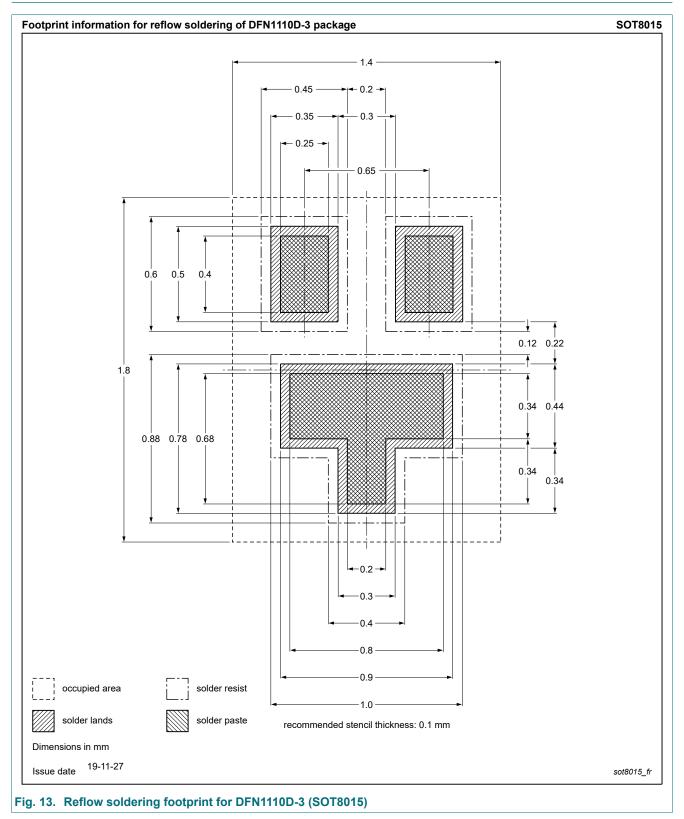
11. Package outline



BC856XQB_SER

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12. Soldering



13. Revision history

Table 9. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
BC856XQB_SER v.1	20210922	Product data sheet	-	-		

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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