Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a leadless ultra small SOD1608 (DFN1608D-2) Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 1.5 A
- Reverse voltage: V_R ≤ 40 V
- Low forward voltage V_F ≤ 610 mV
- Low reverse current
- · Solderable side pads
- · Package height typ. 0.37 mm
- · Ultra small and leadless SMD plastic package
- AEC-Q101 qualified

3. Applications

- · Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- LED backlight for mobile application
- Low power consumption applications
- · Ultra high-speed switching
- Reverse polarity protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{amb} \leq 65 °C	[1]	-	-	1.5	А
		δ = 0.5; f = 20 kHz; square wave; T _{sp} \leq 135 °C		-	-	1.5	А
V _R	reverse voltage	T _j = 25 °C		-	-	40	V
V _F	forward voltage	I_F = 1.5 A; pulsed; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 25 °C		-	540	610	mV
I _R	reverse current	V _R = 10 V; T _j = 25 °C		-	1	5	μΑ
t _{rr}	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$		-	4	-	ns

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		
2	A	anode	Transparent top view DFN1608D-2 (SOD1608)	K 【 A sym001

[1] The marking bar indicates the cathode.

6. Ordering information

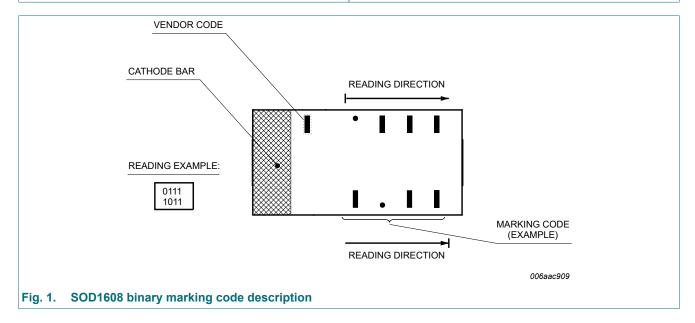
Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
PMEG4015EPK		plastic, leadless ultra small plastic package with sidewettable flanks (SWF); 2 terminals; 0.94 mm pitch; 1.6 mm x 0.8 mm x 0.37 mm body	SOD1608				

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG4015EPK	0110
	0000



8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage	T _j = 25 °C		-	40	V
I _F	forward current	T _{sp} ≤ 130 °C		-	2.1	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; $T_{amb} \le$ 65 °C	[1]	-	1.5	A
		δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 135 °C		-	1.5	А
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	4	A
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; square wave; $T_{j(init)}$ = 25 °C		-	5	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2] [3]	-	415	mW
			[4] [3]	-	895	mW
			[1] [3]	-	1565	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

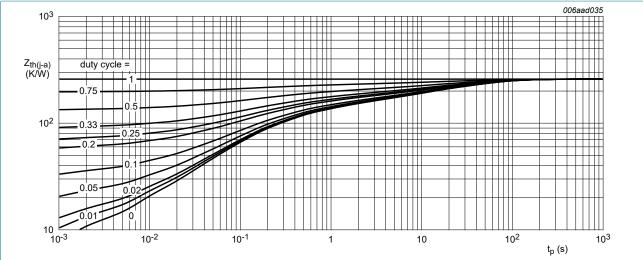
- [1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Reflow soldering is the only recommended soldering method.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

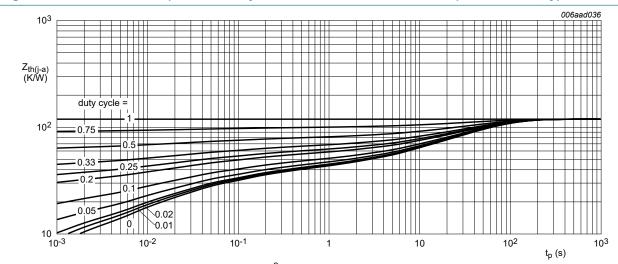
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	[:	[1] [2] [3]	-	-	300	K/W
			[1] [4] [3]	-	-	140	K/W
			[1] [5] [3]	-	-	80	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[6]	-	-	20	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Reflow soldering is the only recommended soldering method.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [5] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
- [6] Soldering point of cathode tab.



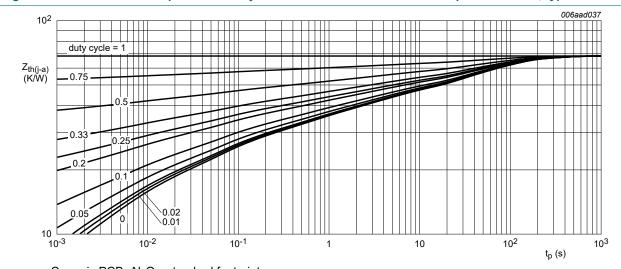
FR4 PCB, standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



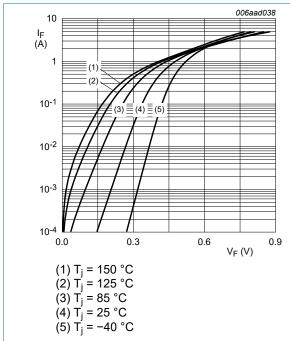
Ceramic PCB, Al₂O₃, standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

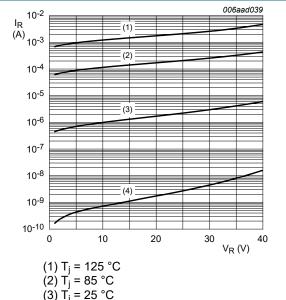
10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage	I_F = 100 mA; pulsed; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 25 °C	-	330	380	mV
		I_F = 500 mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$; T_j = 25 °C	-	415	480	mV
		I_F = 1 A; pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_j = 25 °C	-	490	550	mV
		I_F = 1.5 A; pulsed; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 25 °C	-	540	610	mV
I _R	reverse current	V _R = 10 V; T _j = 25 °C	-	1	5	μΑ
		V _R = 40 V; T _j = 25 °C	-	8	30	μΑ
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	75	90	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	30	40	pF
t _{rr}	reverse recovery time	$I_F = 0.5 \text{ A}$; $I_R = 0.5 \text{ A}$; $I_{R(meas)} = 0.1 \text{ A}$; $I_{j} = 25 \text{ °C}$	-	4	-	ns
V_{FRM}	peak forward recovery voltage	$I_F = 0.5 \text{ A}; \text{ d}I_F/\text{d}t = 20 \text{ A/}\mu\text{s}; T_j = 25 °C$	-	440	-	mV



Forward current as a function of forward Fig. 5. voltage; typical values



(3) $T_j = 25$ °C (4) $T_i = -40 \, ^{\circ}C$

Fig. 6. Reverse current as a function of reverse voltage; typical values

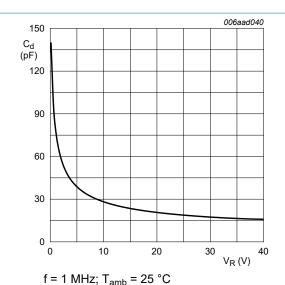
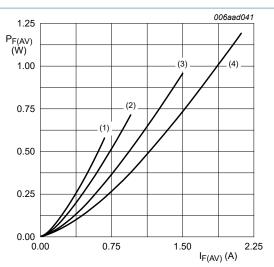
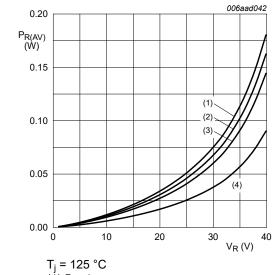


Fig. 7. Diode capacitance as a function of reverse voltage; typical values



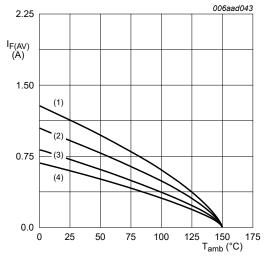
 $T_j = 150 \text{ °C}$ (1) $\delta = 0.1$ (2) $\delta = 0.2$ (3) $\delta = 0.5$ (4) $\delta = 1$

Fig. 8. Average forward power dissipation as a function of average forward current; typical values



 $(1) \delta = 1$ $(2) \delta = 0.9$ $(3) \delta = 0.8$ $(4) \delta = 0.5$

Fig. 9. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint $T_j = 150 \,^{\circ}\text{C}$

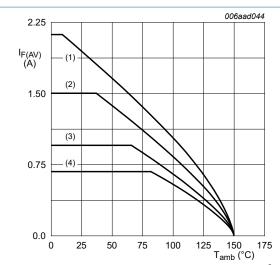
 $(1) \delta = 1 (DC)$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm^2

 $T_i = 150 \, ^{\circ}C$

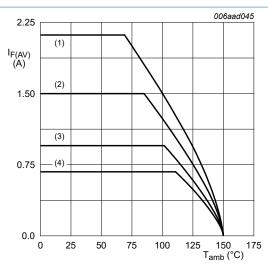
 $(1) \delta = 1 (DC)$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al₂O₃, standard footprint

T_i = 150 °C

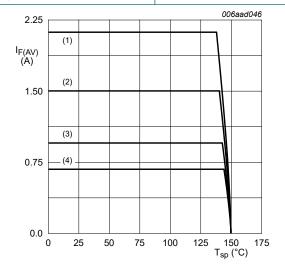
 $(1) \delta = 1 (DC)$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 12. Average forward current as a function of ambient temperature; typical values



T_i = 150 °C

 $(1) \delta = 1 (DC)$

(2) $\delta = 0.5$; f = 20 kHz

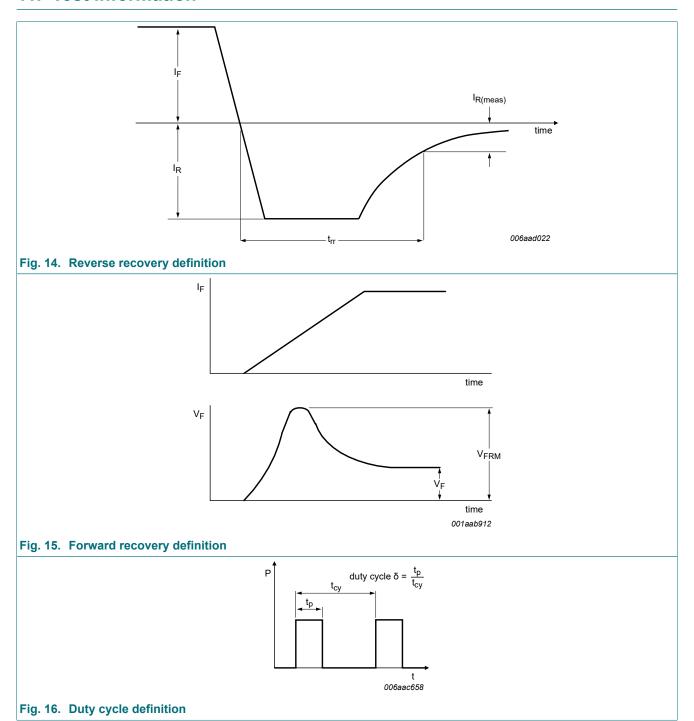
(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 13. Average forward current as a function of solder point temperature; typical values

Product data sheet

11. Test information



The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current,

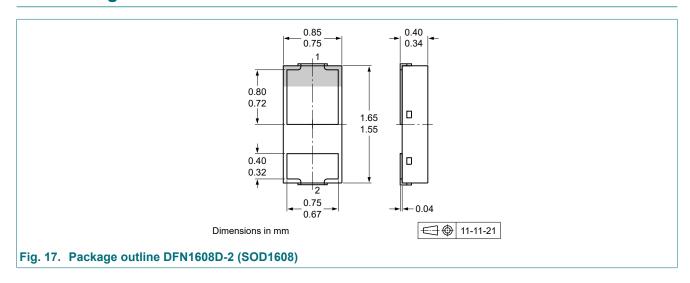
 $I_{RMS} = I_{F(AV)}$ at DC,

 $I_{RMS} = I_{M} \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

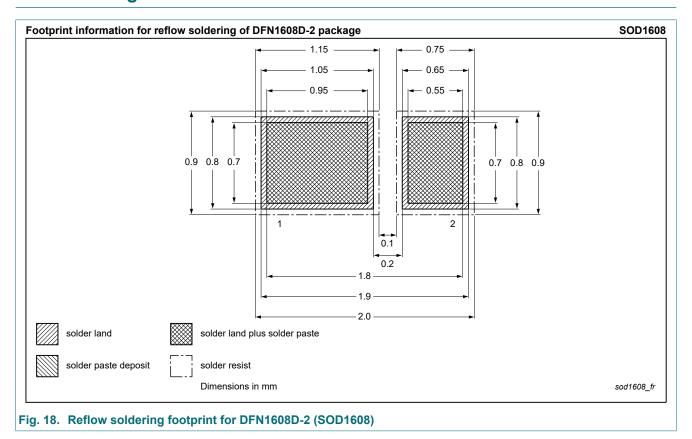
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

Table 6. Revision filetory							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG4015EPK v.3	20230920	Product data sheet	-	PMEG4015EPK v.2			
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 						
PMEG4015EPK v.2	20120306	Product data sheet	-	PMEG4015EPK v.1			
PMEG4015EPK v.1	20120302	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PMEG4015EPK

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