74AUP1G132

Low-power 2-input NAND Schmitt trigger

Rev. 10 — 23 September 2024

Product data sheet

1. General description

The 74AUP1G132 is a single 2-input NAND gate with Schmitt-trigger inputs. Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times. This device ensures very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- CMOS low power dissipation
- · High noise immunity
- Overvoltage tolerant inputs to 3.6 V
- Low static power consumption; I_{CC} = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.65 V to 1.95 V)
 - JESD8-5 (2.3 V to 2.7 V)
 - JESD8C (2.7 V to 3.6 V)
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 3A exceeds 5000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Applications

- Wave and pulse shaper
- Astable multivibrator
- Monostable multivibrator.



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4. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AUP1G132GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
74AUP1G132GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886
74AUP1G132GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	<u>SOT1115</u>
74AUP1G132GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202
74AUP1G132GX	-40 °C to +125 °C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.32 mm	SOT1226-3
74AUP1G132GZ	-40 °C to +125 °C	XSON5	plastic thermal enhanced extremely thin small outline package with side-wettable flanks (SWF); no leads; 5 terminals; body 1.1 × 0.85 × 0.5 mm	SOT8065-1

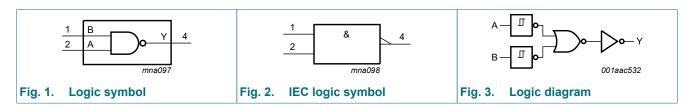
5. Marking

Table 2. Marking

and a marking							
Type number	Marking code [1]						
74AUP1G132GW	аЕ						
74AUP1G132GM	аЕ						
74AUP1G132GN	аЕ						
74AUP1G132GS	аЕ						
74AUP1G132GX	аЕ						
74AUP1G132GZ	аЕ						

^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

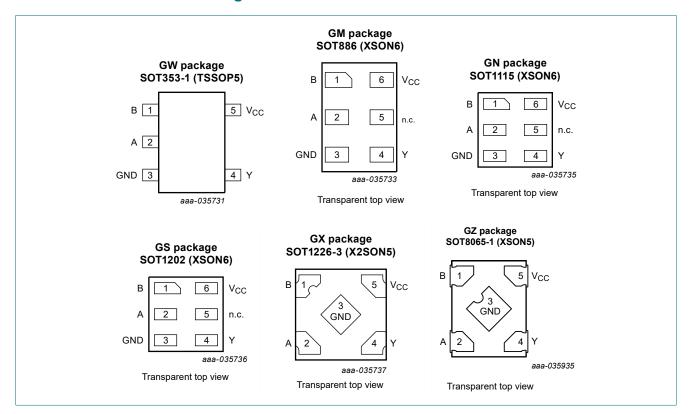
6. Functional diagram



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7. Pinning information

7.1. Pinning



7.2. Pin description

Table 3. Pin description

Symbol	Pin	Description	
	TSSOP5, XSON5 and X2SON5	XSON6	
В	1	1	data input
A	2	2	data input
GND	3	3	ground (0 V)
Υ	4	4	data output
n.c.	-	5	not connected
V _{CC}	5	6	supply voltage

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8. Functional description

Table 4. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$

Input	Output	
A	В	Υ
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

9. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
Io	output current	V _O = 0 V to V _{CC}	-	±20	mA
I _{CC}	supply current		-	50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$ [2]	-	250	mW

^[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

10. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
V _O	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V _{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C

^[2] For SOT353-1 (TSSOP5) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

For SOT886 (XSON6) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package: Ptot derates linearly with 3.2 mW/K above 71 °C.

For SOT1202 (XSON6) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

For SOT1226-3 (X2SON5) package: Ptot derates linearly with 3.0 mW/K above 67 °C.

For SOT8065-1 (XSON5) package: P_{tot} derates linearly with 3.2 mW/K above 72 $^{\circ}\text{C}.$

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11. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	5 °C					
V _{OH}	HIGH-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I _O = -20 μA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	2.05	-	-	V
		I_{O} = -3.1 mA; V_{CC} = 2.3 V	1.9	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I_{O} = 20 μ A; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.1	μΑ
I _{OFF}	power-off leakage current	V_{I} or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.2	μΑ
Δl _{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.2	μA
I _{CC}	supply current	V_{I} = GND or V_{CC} ; I_{O} = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.5	μΑ
ΔI _{CC}	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ [1]	-	-	40	μA
Cı	input capacitance	$V_I = GND \text{ or } V_{CC}$; $V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	1.1	-	pF
Co	output capacitance	V _O = GND; V _{CC} = 0 V	-	1.7	-	pF
T _{amb} = -	40 °C to +85 °C					
V _{OH}	HIGH-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.55	-	-	V
	1					

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OL}	LOW-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.5	μΑ
I _{OFF}	power-off leakage current	V_{I} or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.5	μA
Δl _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.6	μA
I _{CC}	supply current	V_{I} = GND or V_{CC} ; I_{O} = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.9	μΑ
ΔI_{CC}	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ [1]	-	-	50	μΑ
T _{amb} = -4	40 °C to +125 °C					
V_{OH}	HIGH-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I _O = -20 μA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I_{O} = -2.7 mA; V_{CC} = 3.0 V	2.40	-	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.30	-	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.75	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.75	μΑ
ΔI _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.75	μΑ
I _{CC}	supply current	V_{I} = GND or V_{CC} ; I_{O} = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	1.4	μA
ΔI_{CC}	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ [1]	-	-	75	μA

^[1] One input at V_{CC} - 0.6 V, other input at V_{CC} or GND.

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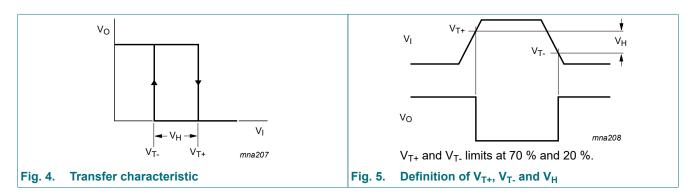
11.1. Transfer characteristics

Table 8. Transfer characteristics

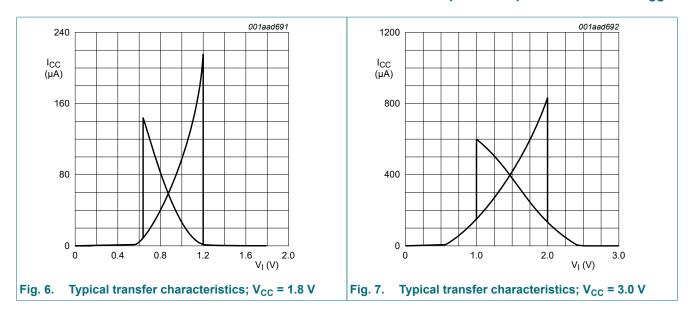
Voltages are referenced to GND (ground = 0 V; for test circuit see Fig. 9.

Symbol	Parameter	Conditions		25 °C			°C to 5 °C		°C to 5 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
V_{T+}	positive-going	see Fig. 4 and Fig. 5								
	threshold voltage	V _{CC} = 0.8 V	0.30	-	0.60	0.30	0.60	0.30	0.62	V
	Voltage	V _{CC} = 1.1 V	0.53	-	0.90	0.53	0.90	0.53	0.92	V
		V _{CC} = 1.4 V	0.74	-	1.11	0.74	1.11	0.74	1.13	V
		V _{CC} = 1.65 V	0.91	-	1.29	0.91	1.29	0.91	1.31	V
		V _{CC} = 2.3 V	1.37	-	1.77	1.37	1.77	1.37	1.80	V
		V _{CC} = 3.0 V	1.88	-	2.29	1.88	2.29	1.88	2.32	V
V _{T-}	negative-going	see Fig. 4 and Fig. 5								
	threshold voltage	V _{CC} = 0.8 V	0.10	-	0.60	0.10	0.60	0.10	0.60	V
	voltage	V _{CC} = 1.1 V	0.26	-	0.65	0.26	0.65	0.26	0.65	V
		V _{CC} = 1.4 V	0.39	-	0.75	0.39	0.75	0.39	0.75	V
		V _{CC} = 1.65 V	0.47	-	0.84	0.47	0.84	0.47	0.84	V
		V _{CC} = 2.3 V	0.69	-	1.04	0.69	1.04	0.69	1.04	V
		V _{CC} = 3.0 V	0.88	-	1.24	0.88	1.24	0.88	1.24	V
V_{H}	hysteresis voltage	(V _{T+} - V _{T-}); see <u>Fig. 4</u> , <u>Fig. 5</u> , <u>Fig. 6</u> and <u>Fig. 7</u>								
		V _{CC} = 0.8 V	0.07	-	0.50	0.07	0.50	0.07	0.50	V
		V _{CC} = 1.1 V	0.08	-	0.46	0.08	0.46	0.08	0.46	V
		V _{CC} = 1.4 V	0.18	-	0.56	0.18	0.56	0.18	0.56	V
		V _{CC} = 1.65 V	0.27	-	0.66	0.27	0.66	0.27	0.66	V
		V _{CC} = 2.3 V	0.53	-	0.92	0.53	0.92	0.53	0.92	V
		V _{CC} = 3.0 V	0.79	-	1.31	0.79	1.31	0.79	1.31	V

11.2. Waveforms transfer characteristics



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12. Dynamic characteristics

Table 9. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V; for test circuit see Fig. 9.

Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C _L = 5 p	F									
t _{pd}	propagation	A or B to Y; see Fig. 8 [2]								
	delay	V _{CC} = 0.8 V	-	22.5	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.6	6.3	13.4	2.4	15.1	2.4	16.6	ns
		V _{CC} = 1.4 V to 1.6 V	2.2	4.6	8.2	1.9	9.7	1.9	10.7	ns
		V _{CC} = 1.65 V to 1.95 V	1.9	3.9	6.6	1.7	7.9	1.7	8.7	ns
		V _{CC} = 2.3 V to 2.7 V	1.7	3.2	5.3	1.5	6.2	1.5	6.8	ns
		V _{CC} = 3.0 V to 3.6 V	1.6	2.9	4.7	1.4	5.6	1.4	6.2	ns
C _L = 10	pF					•			•	
t _{pd}	propagation	A or B to Y; see Fig. 8 [2]								
	delay	V _{CC} = 0.8 V	-	26.1	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.0	7.2	15.4	2.7	17.3	2.7	19.0	ns
		V _{CC} = 1.4 V to 1.6 V	2.5	5.2	9.3	2.2	11.0	2.2	12.1	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	4.5	7.5	2.0	9.0	2.0	9.9	ns
		V _{CC} = 2.3 V to 2.7 V	2.1	3.8	6.1	1.8	7.2	1.8	7.9	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	3.5	5.5	1.8	6.5	1.8	7.2	ns
C _L = 15	pF			•						
t _{pd}	propagation	A or B to Y; see Fig. 8 [2]								
	delay	V _{CC} = 0.8 V	-	29.6	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.3	8.0	17.2	3.0	19.4	3.0	21.3	ns
		V _{CC} = 1.4 V to 1.6 V	2.8	5.8	10.4	2.5	12.3	2.5	13.5	ns
		V _{CC} = 1.65 V to 1.95 V	2.6	5.0	8.3	2.3	10.0	2.3	11.0	ns
		V _{CC} = 2.3 V to 2.7 V	2.3	4.2	6.7	2.1	7.9	2.1	8.7	ns
		V _{CC} = 3.0 V to 3.6 V	2.2	3.9	6.1	2.0	7.3	2.0	8.0	ns

Low-power 2-input NAND Schmitt trigger

Symbol	Parameter	Conditions		25 °C		-40 °C to	-40 °C to +85 °C -40 °C to +125 °C			Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C _L = 30	pF									
t _{pd}	propagation	A or B to Y; see Fig. 8 [2]								
	delay	V _{CC} = 0.8 V	-	39.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.3	10.2	22.6	3.8	25.4	3.8	27.9	ns
		V _{CC} = 1.4 V to 1.6 V	3.6	7.3	13.3	3.2	15.8	3.2	17.4	ns
		V _{CC} = 1.65 V to 1.95 V	3.2	6.3	10.6	2.9	12.8	2.9	14.1	ns
		V _{CC} = 2.3 V to 2.7 V	3.0	5.3	8.5	2.7	10.1	2.7	11.1	ns
		V _{CC} = 3.0 V to 3.6 V	2.8	5.0	7.8	2.7	9.2	2.7	10.1	ns
$C_L = 5 p$	F, 10 pF, 15 pF	and 30 pF				'				
C _{PD}	power dissipation	$f_i = 1 \text{ MHz};$ [3] $V_I = \text{GND to } V_{CC}$								
	capacitance	V _{CC} = 0.8 V	-	2.6	-	-	-	-	-	pF
		V _{CC} = 1.1 V to 1.3 V	-	2.9	-	-	-	-	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	3.0	-	-	-	-	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	3.2	-	-	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	3.8	-	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.4	-	-	-	-	-	pF

- All typical values are measured at nominal V_{CC}.
- t_{pd} is the same as t_{PLH} and t_{PHL} . C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$ = sum of the outputs.

12.1. Waveforms and test circuit

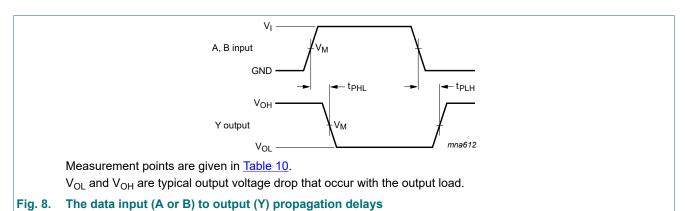
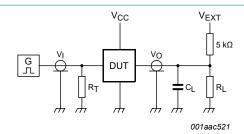


Table 10. Measurement points

Supply voltage	Output	Input					
V _{CC}	V _M	V _M	VI	$t_r = t_f$			
0.8 V to 3.6 V	0.5 × V _{CC}	0.5 × V _{CC}	V _{CC}	≤ 3.0 ns			

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Test data is given in Table 11.

Definitions for test circuit:

R_L = Load resistance;

C_L = Load capacitance including jig and probe capacitance;

 R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator;

 V_{EXT} = External voltage for measuring switching times.

Fig. 9. Test circuit for measuring switching times

Table 11. Test data

Supply voltage	Load		V _{EXT}		
V _{CC}	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V _{CC}

[1] For measuring enable and disable times R_L = 5 k Ω . For measuring propagation delays, setup and hold times and pulse width R_L = 1 M Ω .

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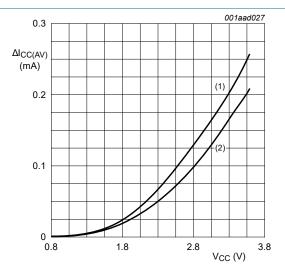
13. Application information

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $P_{add} = f_i \times (t_r \times \Delta I_{CC(AV)} + t_f \times \Delta I_{CC(AV)}) \times V_{CC} \text{ where:}$

- P_{add} = additional power dissipation (μW);
- f_i = input frequency (MHz);
- t_r = input rise time (ns); 10 % to 90 %;
- t_f = input fall time (ns); 90 % to 10 %;
- ΔI_{CC(AV)} = average additional supply current (µA).

Average $\Delta I_{CC(AV)}$ differs with positive or negative input transitions, as shown in Fig. 10.



- (1) Positive-going edge.
- (2) Negative-going edge.

Linear change of V_I between 0.8 V and 2.0 V. All values given are typical, unless otherwise specified.

Fig. 10. Average I_{CC} as a function of V_{CC}

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14. Package outline

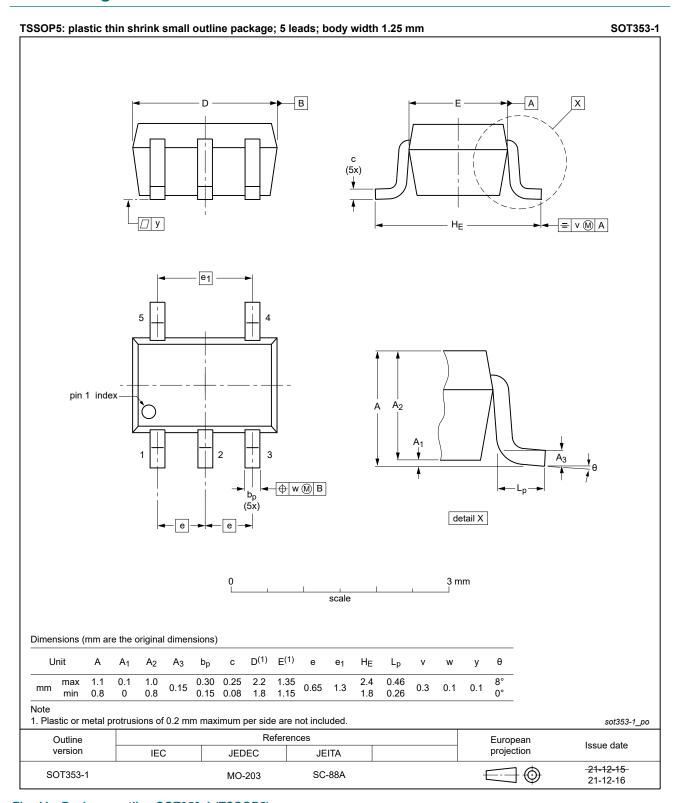


Fig. 11. Package outline SOT353-1 (TSSOP5)

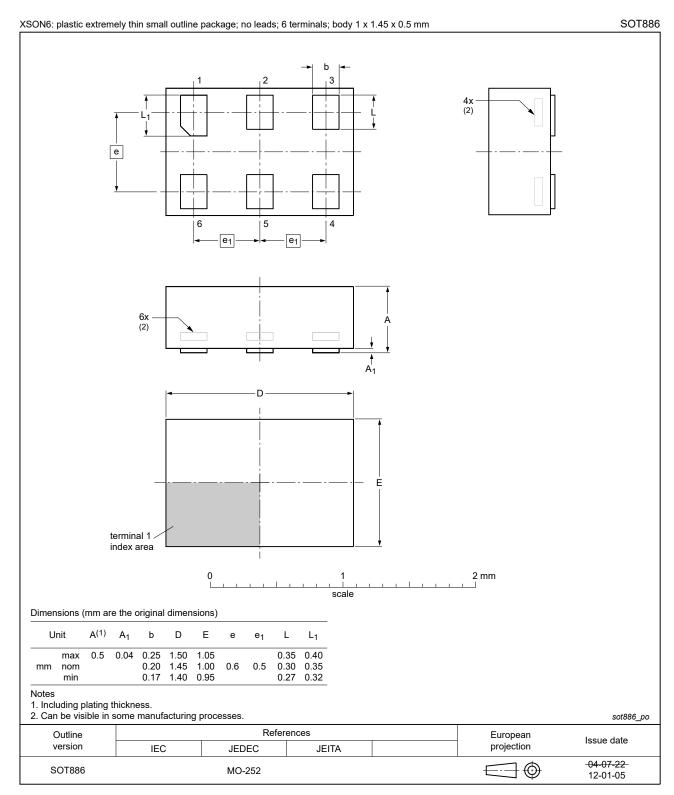


Fig. 12. Package outline SOT886 (XSON6)

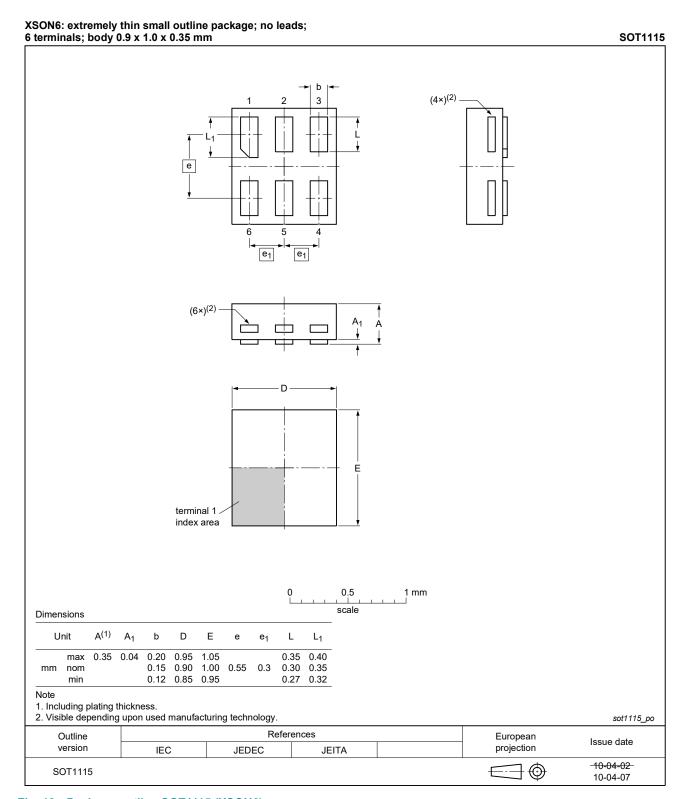


Fig. 13. Package outline SOT1115 (XSON6)

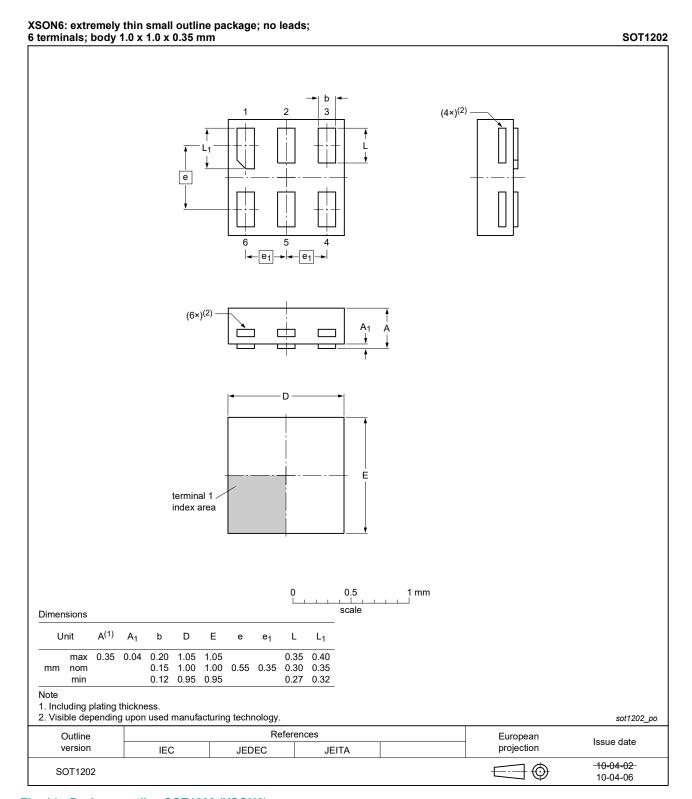


Fig. 14. Package outline SOT1202 (XSON6)

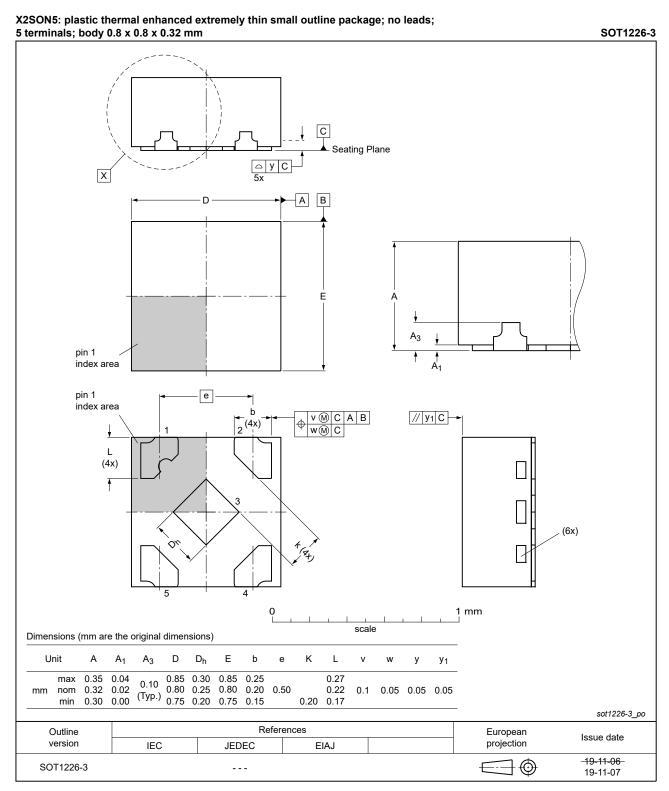


Fig. 15. Package outline SOT1226-3 (X2SON5)

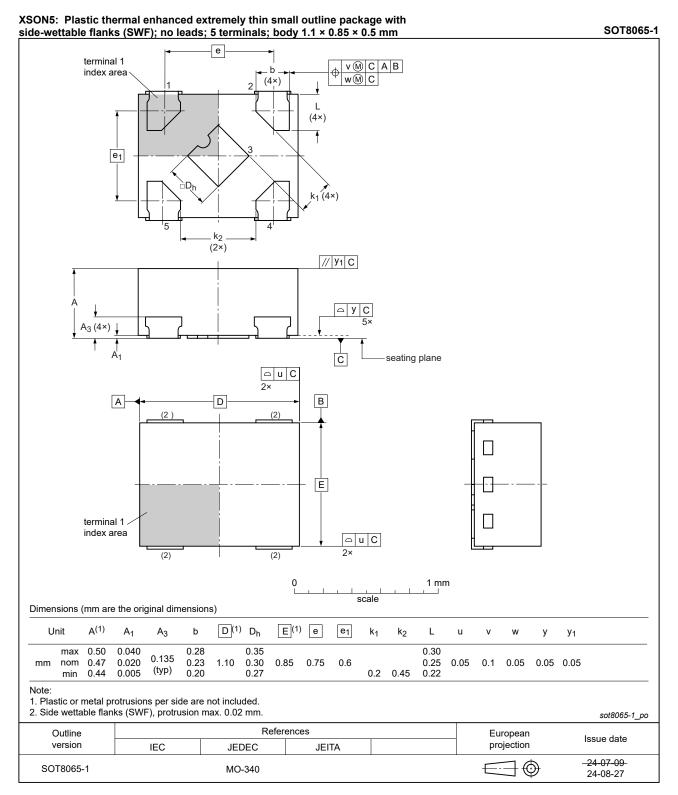


Fig. 16. Package outline SOT8065-1 (XSON5)

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15. Abbreviations

Table 12. Abbreviations

Acronym	Description
ANSI	American National Standards Institute
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
ESDA	ElectroStatic Discharge Association
НВМ	Human Body Model
JEDEC	Joint Electron Device Engineering Council

16. Revision history

Table 13. Revision history

Table 13. Revision histo Document ID	Release date	Data sheet status	Change notice	Supersedes	
			Change notice	•	
74AUP1G132 v.10	20240923	Product data sheet	-	74AUP1G132 v.9.1	
Modifications:	Type number	er 74AUP1G132GZ (SO	T8065-1/XSON5) add	ded.	
74AUP1G132 v.9.1	20230711	Product data sheet	-	74AUP1G132 v.8	
Modifications:	Section 2: E	<u>Section 2</u> : ESD specification updated according to the latest JEDEC standard.			
74AUP1G132 v.8	20220114	Product data sheet	-	74AUP1G132 v.7	
Modifications:	• <u>Fig. 11</u> : Pac	Fig. 11: Package outline drawing for SOT353-1 (TSSOP5) has changed.			
74AUP1G132 v.7	20210709	Product data sheet	-	74AUP1G132 v.6	
Modifications:	Type numberSection 1 a	X2SON5) package changer 74AUP1G132GF (SOon 1) and Section 2 updated. rating values for Ptot tota	T891) removed.	,, .	
74AUP1G132 v.6	20190501	Product data sheet	-	74AUP1G132 v.5	
Modifications:	of Nexperia • Legal texts		e new company nam	mply with the identity guidelines e where appropriate.	
74AUP1G132 v.5	20120629	Product data sheet	-	74AUP1G132 v.4	
Modifications:		number 74AUP1G132G	,		
74AUP1G132 v.4	20111124	Product data sheet	-	74AUP1G132 v.3	
Modifications:	Legal page:	Legal pages updated.			
74AUP1G132 v.3	20101029	Product data sheet	-	74AUP1G132 v.2	
74AUP1G132 v.2	20090615	Product data sheet	-	74AUP1G132 v.1	
74AUP1G132 v.1	20061020	Product data sheet	-	-	

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17. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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