Product data sheet

1. General description

Unidirectional ElectroStatic Discharge (ESD) protection diode in a SOD882 leadless ultra small Surface Mounted Device (SMD) plastic package, designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

- · Ultra small SMD plastic package
- · ESD protection of one line
- Max. peak pulse power: P_{PPM} = 150 W
- Ultra low leakage current: I_{RM} = 1 nA
- · ESD protection up to 23 kV
- IEC 61000-4-2; level 4 (ESD)
- IEC 61000-4-5; (surge); I_{PPM} = 3 A

3. Applications

- · Computers and peripherals
- · Audio and video equipment
- Parallel ports
- · Communication systems
- · High-speed data lines

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C	-	-	24	V
C _d	diode capacitance	$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$	-	23	50	pF



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		
2	Α	anode		к [(А
			Transparent top view	sym035
			DFN1006-2 (SOD882)	

^[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PESD24VS1UL		plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	SOD882		

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD24VS1UL	G5

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1]	-	150	W
I _{PPM}	rated peak pulse current		[1]	-	3	Α
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximu	m ratings			'	'	
V _{ESD}	electrostatic discharge	IEC 61000-4-2; contact discharge	[2]	-	23	kV
	voltage	MIL-STD-883; human body model (HBM)	[2]	-	10	kV

- [1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC61000-4-5.
- [2] Device stressed with ten non-repetitive ESD pulses.

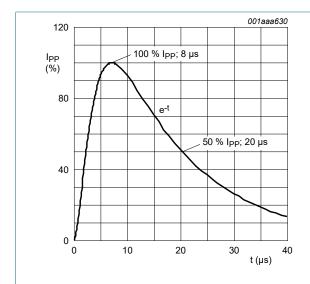


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

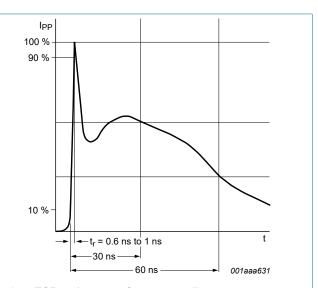


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	24	V
V_{BR}	breakdown voltage	I _R = 5 mA; T _{amb} = 25 °C	[1]	26.5	27	27.5	V
I _{RM}	reverse leakage current	V _{RWM} = 24 V; T _{amb} = 25 °C		-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	23	50	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; t _p = 8/20 μs; T _{amb} = 25 °C	[2]	-	-	36	V
		$I_{PPM} = 3 \text{ A}; t_p = 8/20 \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$	[2]	-	-	70	V
R _{diff}	differential resistance	I _R = 0.5 mA; T _{amb} = 25 °C		-	-	300	Ω

- Pulse test: $t_p \le 300~\mu s$; duty cycle ≤ 0.02 . Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC61000-4-5.

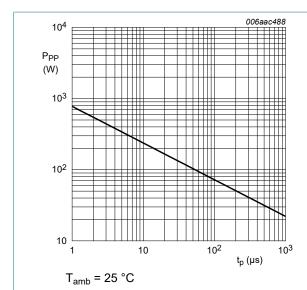
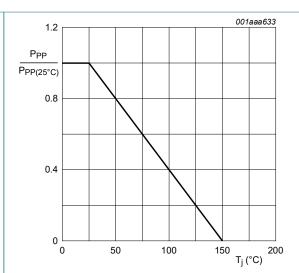


Fig. 3. Peak pulse power as a function of exponential pulse duration; typical values



Relative variation of peak pulse power as a function of junction temperature; typical values

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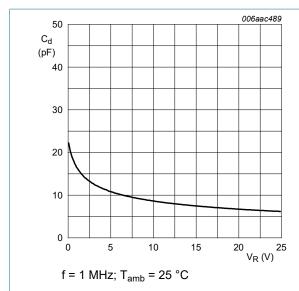


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

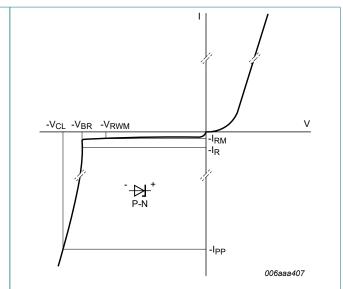
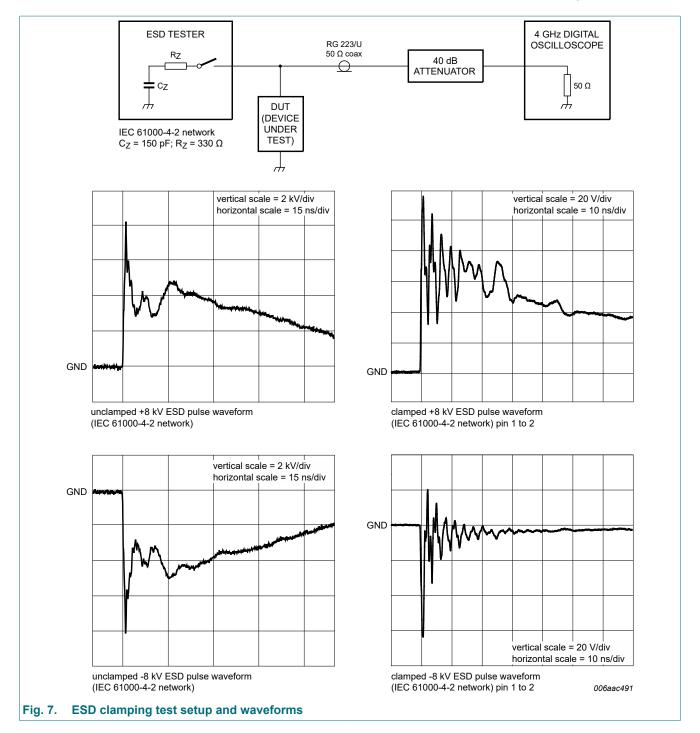
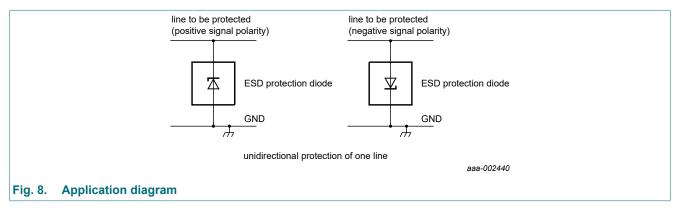


Fig. 6. V-I characteristics for a unidirectional ESD protection diode



10. Application information

The device is designed for protection of one unidirectional data or signal line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are either positive or negative with respect to ground.

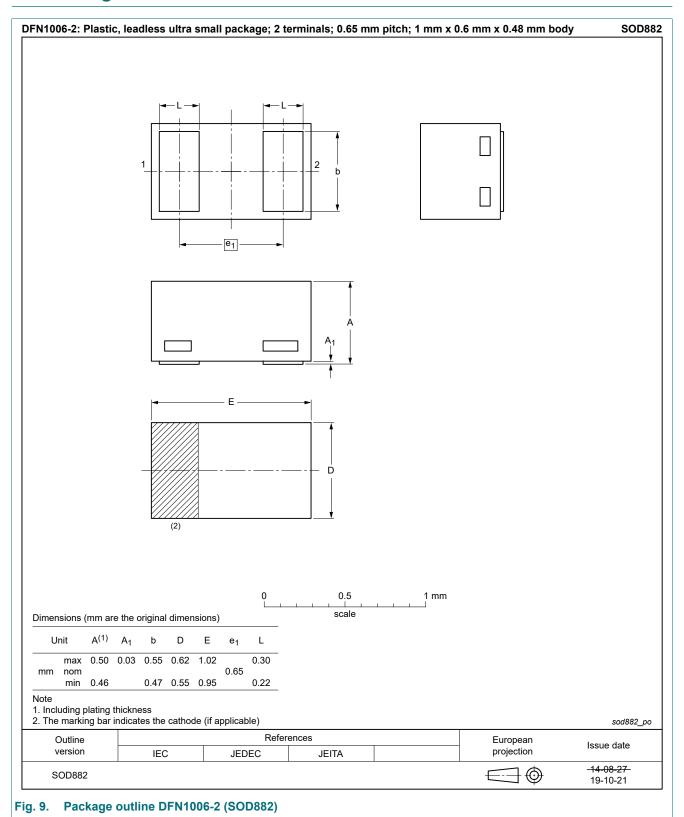


Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- **6.** Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Package outline

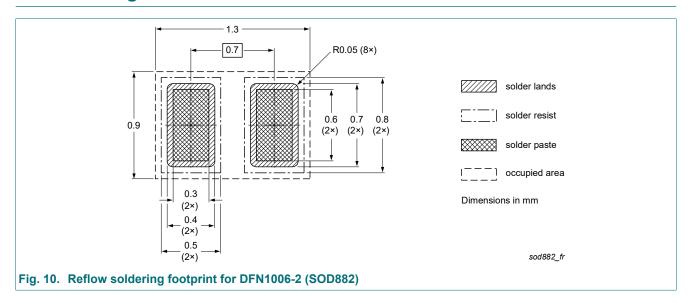


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PESD24VS1UL

12. Soldering



13. Revision history

Table 7. Revision history

Table 7. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD24VS1UL v.5	20240502	Product data sheet	-	PESD24VS1UL v.4
Modifications:		d to non-automotive qua product alternative(s).	alification. Please refe	er to nexperia.com for
PESD24VS1UL v.4	20200115	Product data sheet	-	PESDXS1UL_SER v.3
PESDXS1UL_SER v.3	20111025	Product data sheet	-	PESDXS1UL_SER v.2
PESDXS1UL_SER v.2	20090820	Product data sheet	-	PESDXS1UL_SER v.1
PESDXS1UL_SER v.1	20060331	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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