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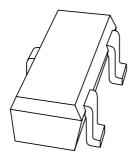
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



2PB709AWPNP general purpose transistor

Product data sheet 2002 Jun 26



PNP general purpose transistor

2PB709AW

FEATURES

- High collector current (max. 100 mA)
- Low collector-emitter saturation voltage (max. 500 mV).

APPLICATIONS

· General purpose switching and amplification.

DESCRIPTION

PNP transistor in an SC-70 (SOT323) plastic package. NPN complement: 2PD601AW

MARKING

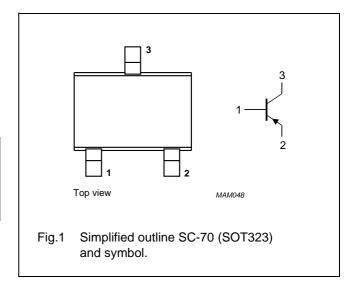
TYPE NUMBER	MARKING CODE ⁽¹⁾
2PB709AQW	N5*
2PB709ARW	N7*
2PB709ASW	N9*

Note

- 1. * = p: made in Hong Kong.
 - * = t: made in Malaysia.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-45	V
V _{CEO}	collector-emitter voltage	open base	_	-45	V
V _{EBO}	emitter-base voltage	open collector	_	-6	V
I _C	collector current (DC)		_	-100	mA
I _{CM}	peak collector current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	200	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. For mounting conditions, see "Thermal considerations and footprint design for SOT323 in the General Part of Data Handbook SC18".

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PNP general purpose transistor

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	625	K/W

Note

1. For mounting conditions, see "Thermal considerations and footprint design for SOT323 in the General Part of Data Handbook SC18".

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	I _E = 0; V _{CB} = -45 V	-	-10	nA
		$I_E = 0$; $V_{CB} = -45 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	-	-5	μΑ
I _{EBO}	emitter-base cut-off current	I _C = 0; V _{EB} = -5 V	_	-10	nA
h _{FE}	DC current gain	$I_C = -2 \text{ mA}; V_{CE} = -10 \text{ V}$			
	2PB709AQW		160	260	
	2PB709ARW		210	340	
	2PB709ASW		290	460	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -100 \text{ mA}$; $I_B = -10 \text{ mA}$; note 1	_	-500	mV
C _c	collector capacitance $I_E = i_e = 0; \ V_{CB} = -10 \ V; \\ f = 1 \ MHz$		_	5	pF
f _T	transition frequency	$I_C = -1 \text{ mA}; V_{CE} = -10 \text{ V};$			
	2PB709AQW	f = 100 MHz	60	_	MHz
	2PB709ARW		70	_	MHz
	2PB709ASW		80	_	MHz

Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

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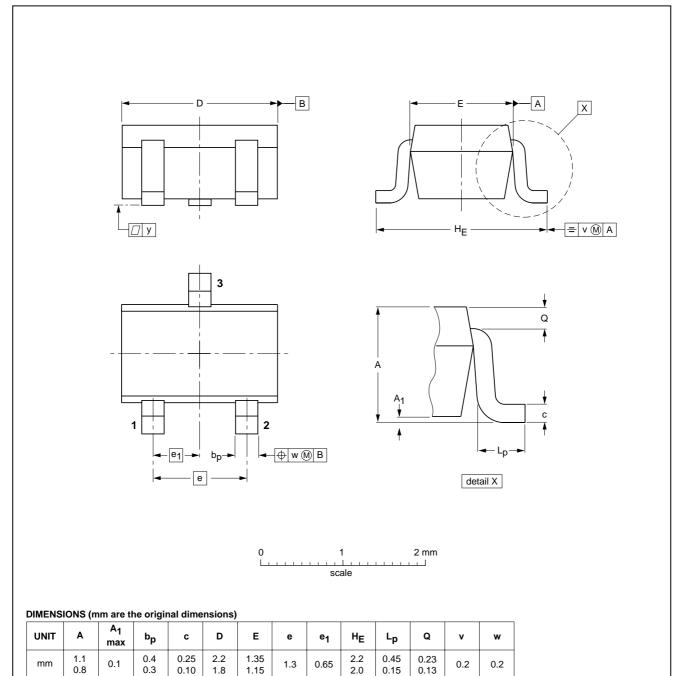
PNP general purpose transistor

2PB709AW

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT323



OUTLINE	REFERENCES		EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT323			SC-70			97-02-28

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PNP general purpose transistor

2PB709AW

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
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NXP Semiconductors

Customer notification

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Contact information

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