**Product data sheet** 

# 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN0606-3 (SOT8001) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

## 2. Features and benefits

- Logic-level compatible
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM
- Leadless ultra small and ultra thin SMD plastic package: 0.62 x 0.62 x 0.37 mm

## 3. Applications

- Relay driver
- High-speed line driver
- · Low-side load switch
- · Switching circuits

# 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	60	V
$V_{GS}$	gate-source voltage			-20	-	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	-	350	mA
Static characte	eristics				'	·	
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 200 \text{ mA}; T_j = 25 \text{ °C}$		-	2	2.8	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.



60 V, N-channel Trench MOSFET

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	S	source	2 1	
3	D	drain	Transparent top view  DFN0606-3 (SOT8001)	G S 017aaa255

# 6. Ordering information

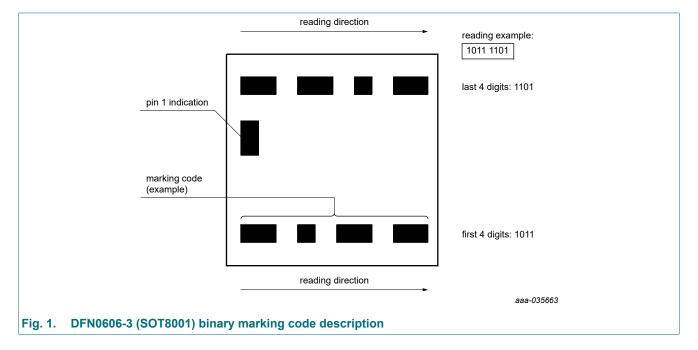
**Table 3. Ordering information** 

Type number	Package				
	Name	Description	Version		
NX7002BKH	DFN0606-3	plastic, leadless ultra small package; 3 terminals; body 0.62 x 0.62 x 0.37 mm	SOT8001		

# 7. Marking

Table 4. Marking codes

Type number	Marking code
NX7002BKH	0001 0011



# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	60	V
V <sub>GS</sub>	gate-source voltage			-20	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	350	mA
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 100 °C	[1]	-	220	mA
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \mu s$		-	1.4	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	380	mW
			[1]	-	710	mW
		T <sub>sp</sub> = 25 °C		-	2.8	W
T <sub>j</sub>	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-draii	n diode			'		,
Is	source current	T <sub>amb</sub> = 25 °C	[1]	-	350	mA

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

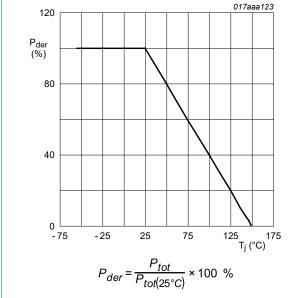


Fig. 2. Normalized total power dissipation as a function of junction temperature

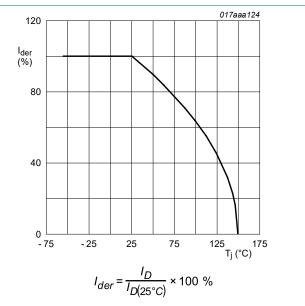


Fig. 3. Normalized continuous drain current as a function of junction temperature

## **60 V, N-channel Trench MOSFET**

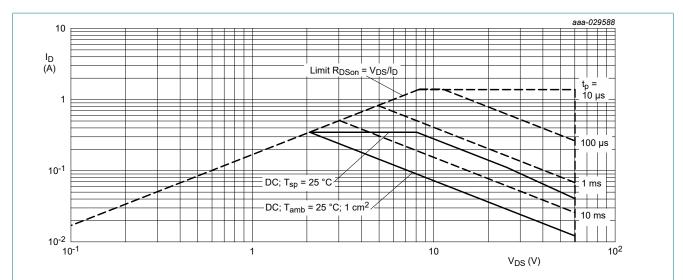


Fig. 4. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

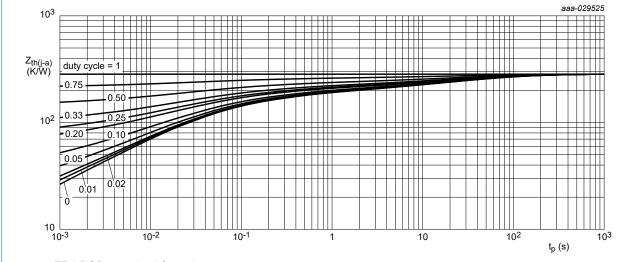
**60 V, N-channel Trench MOSFET** 

## 9. Thermal characteristics

**Table 6. Thermal characteristics** 

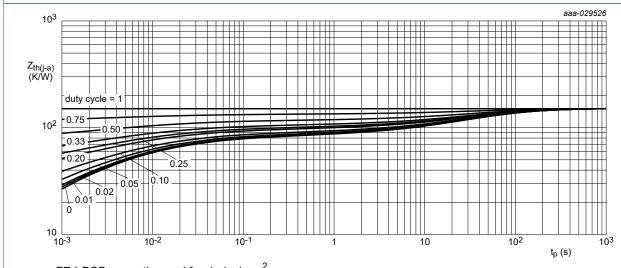
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from	in free air	[1]	-	285	330	K/W
junction to ambient		[2]	-	150	175	K/W	
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	40	45	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.



FR4 PCB, standard footprint

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for drain 1 cm<sup>2</sup>

Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

**60 V, N-channel Trench MOSFET** 

# 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	60	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	1.1	1.6	2.1	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	-	1	μΑ
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	10	μΑ
		V <sub>GS</sub> = -20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-10	μΑ
		V <sub>GS</sub> = 10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μΑ
		V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-1	μΑ
		V <sub>GS</sub> = 4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	100	nA
		V <sub>GS</sub> = -4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-100	nA
DOON	drain-source on-state	$V_{GS}$ = 10 V; $I_D$ = 200 mA; $T_j$ = 25 °C	-	2	2.8	Ω
	resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 200 mA; T <sub>j</sub> = 150 °C	-	4.3	6	Ω
		$V_{GS} = 4.5 \text{ V}; I_D = 200 \text{ mA}; T_j = 25 \text{ °C}$	-	2.1	3	Ω
9 <sub>fs</sub>	forward transconductance	$V_{DS} = 10 \text{ V}; I_D = 200 \text{ mA}; T_j = 25 \text{ °C}$	-	600	-	mS
R <sub>G</sub>	gate resistance	f = 1 MHz	-	2.4	-	Ω
Dynamic ch	naracteristics		'	'	'	
Q <sub>G(tot)</sub>	total gate charge	V <sub>DS</sub> = 30 V; I <sub>D</sub> = 200 mA; V <sub>GS</sub> = 10 V;	-	0.55	1	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.05	-	nC
Q <sub>GD</sub>	gate-drain charge	]	-	0.11	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 30 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	22.2	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	2.7	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	1.7	-	pF
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 30 V; I <sub>D</sub> = 200 mA; V <sub>GS</sub> = 10 V;	-	1	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	1	-	ns
t <sub>d(off)</sub>	turn-off delay time	]	-	3	-	ns
t <sub>f</sub>	fall time	1	-	5	-	ns
Source-dra	in diode		1	-		
$V_{SD}$	source-drain voltage	$I_S = 350 \text{ mA}; V_{GS} = 0 \text{ V}; T_i = 25 ^{\circ}\text{C}$	-	0.9	1.2	V

#### 60 V, N-channel Trench MOSFET

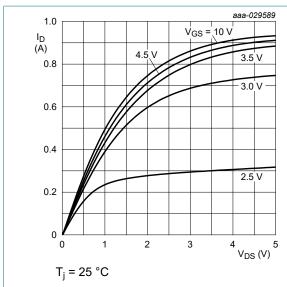


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values

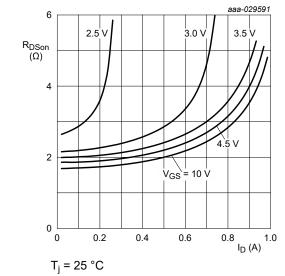


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

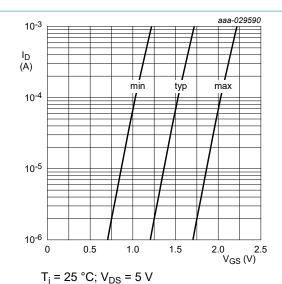


Fig. 8. Sub-threshold drain current as a function of gate-source voltage

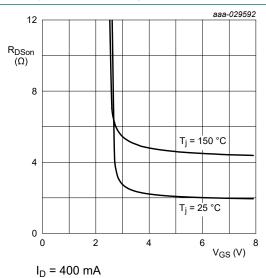


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values

#### **60 V, N-channel Trench MOSFET**

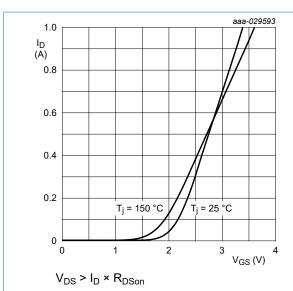


Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values

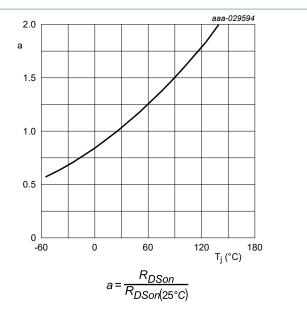


Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values

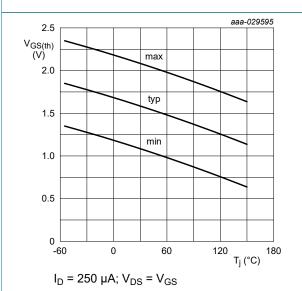


Fig. 13. Gate-source threshold voltage as a function of junction temperature

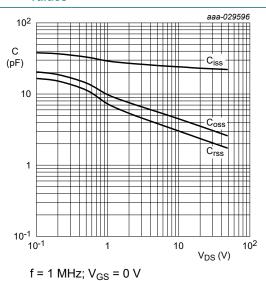


Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

#### **60 V, N-channel Trench MOSFET**

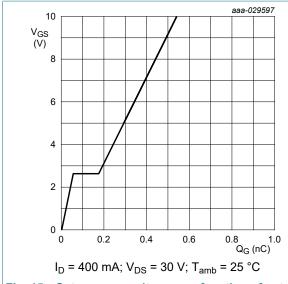


Fig. 15. Gate-source voltage as a function of gate charge; typical values

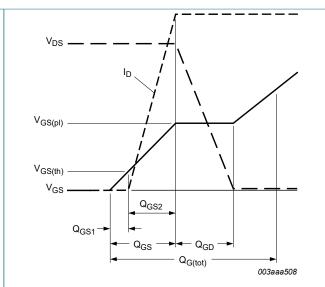


Fig. 16. Gate charge waveform definitions

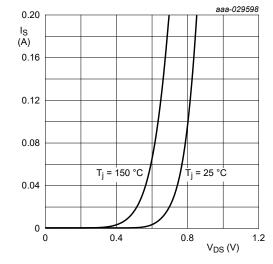
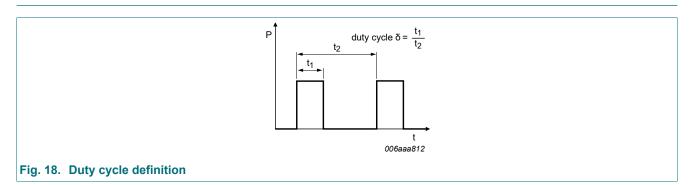


Fig. 17. Source current as a function of source-drain voltage; typical values

## 11. Test information

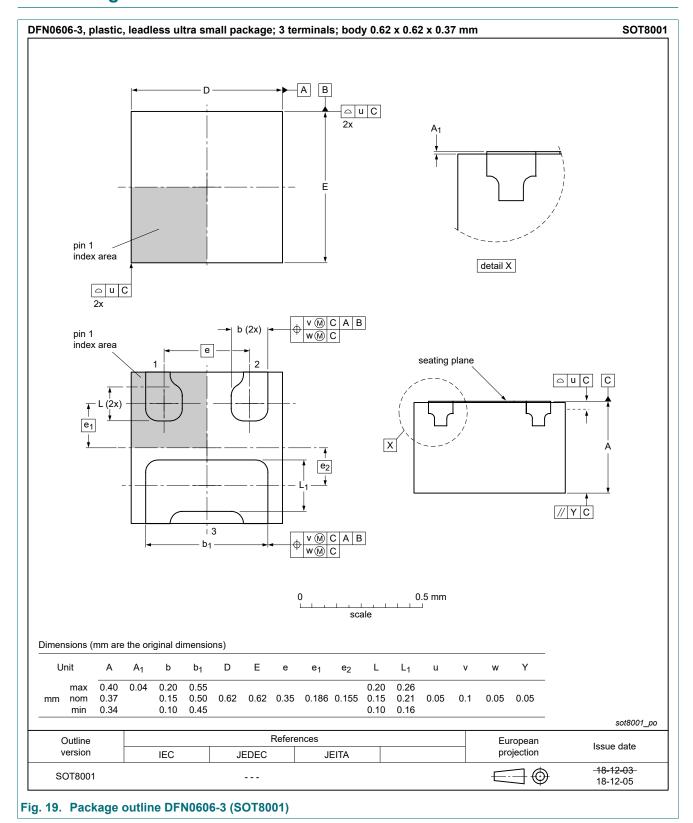
 $V_{GS} = 0 V$ 



NX7002BKH

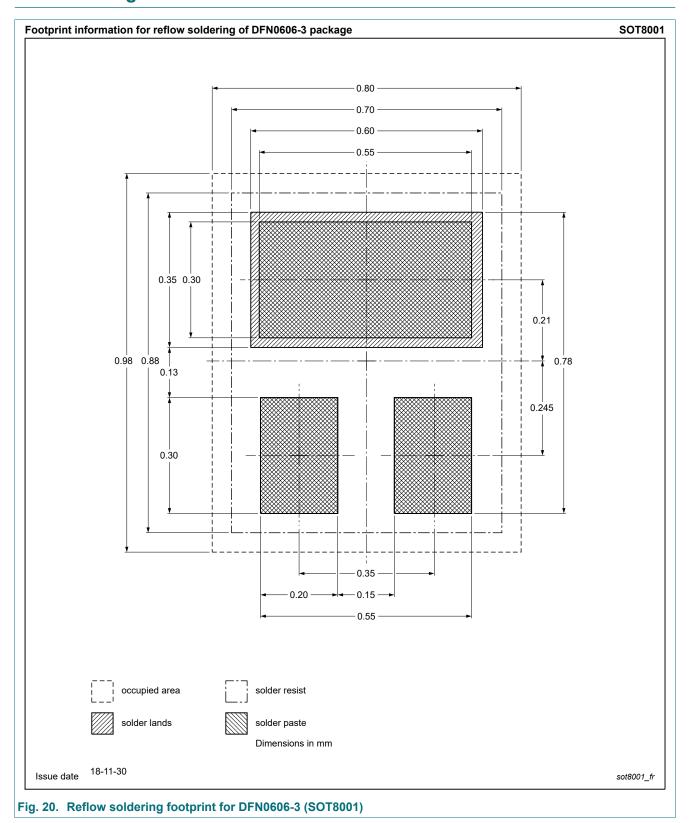
**60 V, N-channel Trench MOSFET** 

# 12. Package outline



**60 V, N-channel Trench MOSFET** 

# 13. Soldering



**60 V, N-channel Trench MOSFET** 

# 14. Revision history

## Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
NX7002BKH v.2	20230206	Product data sheet	-	NX7002BKH v.1		
Modifications:	Fig. 1, clarifying the reading example					
NX7002BKH v.1	20190312	Product data sheet	-	-		

**Product data sheet** 

#### 60 V, N-channel Trench MOSFET

## 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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## **60 V, N-channel Trench MOSFET**

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	Features and benefits

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