Product data sheet

1. General description

Planar Schottky barrier diode in a leadless ultra small DFN1006BD-2 (SOD882BD) Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

2. Features and benefits

- Low forward voltage
- Low capacitance
- · Leadless ultra small SMD plastic package
- Low package height of 0.5 mm
- · Suitable for Automatic Optical Inspection (AOI) of solder joint
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Ultra high-speed switching
- · Voltage clamping
- · Protection circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_R	reverse voltage		-	-	30	V
V _F		I_F = 10 mA; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; pulsed; T_{amb} = 25 °C	-	-	400	mV

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode		к _} А
2	Α	anode		sym001
			Transparent top view	
			DFN1006BD-2 (SOD882BD)	



6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BAT54LS-Q		Leadless ultra small plastic package with side-wettable flanks (SWF); 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.47 mm body	SOD882BD			

7. Marking

Table 4. Marking codes

Type number	Marking code
BAT54LS-Q	8L

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage			-	30	V
IF	forward current	T _{amb} ≤ 25 °C		-	200	mA
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ s; } \delta \le 0.5; T_{amb} = 25 \text{ °C}$		-	300	mA
I _{FSM}	non-repetitive peak forward current	square-wave pulse; $t_p \le 10 \text{ ms}$; $T_{j(init)} = 25 \text{ °C}$		-	600	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	345	mW
			[2]	-	640	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided 70 µm copper, tin-plated and standard footprint.

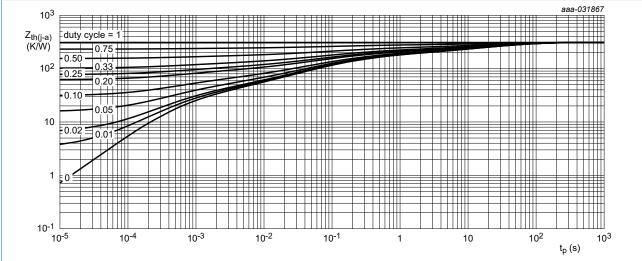
Device mounted on an FR4 Printed-Circuit Board (PCB), 70 µm single-sided copper, tin-plated; mounting pad for collector 1cm².

9. Thermal characteristics

Table 6. Thermal characteristics

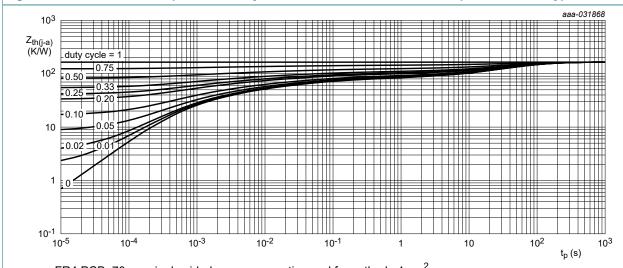
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from	in free air	[1] [2]	-	-	360	K/W
	junction to ambient		[3]	-	-	195	K/W

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided 70 µm copper, tin-plated and standard footprint.
- [2] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses PR are a significant part of the total power losses.
- [3] Device mounted on an FR4 Printed-Circuit Board (PCB), 70 µm single-sided copper, tin-plated; mounting pad for collector 1cm².



FR4 PCB, 70 µm single sided copper standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, 70 μm single sided copper, mounting pad for cathode 1 cm²

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage	I_F = 0.1 mA; t_p ≤ 300 μs; δ ≤ 0.02; pulsed; T_{amb} = 25 °C	-	-	240	mV
		I_F = 1 mA; $t_p \le 300$ μs; $δ \le 0.02$; pulsed; T_{amb} = 25 °C	-	-	320	mV
		I_F = 10 mA; $t_p \le 300$ μs; $δ \le 0.02$; pulsed; T_{amb} = 25 °C	-	-	400	mV
		I_F = 30 mA; t_p ≤ 300 μs; δ ≤ 0.02; pulsed; T_{amb} = 25 °C	-	-	500	mV
		I_F = 100 mA; $t_p \le$ 300 μs; $\delta \le$ 0.02; pulsed; T_{amb} = 25 °C	-	-	800	mV
I _R	reverse current	V_R = 25 V; $t_p \le 300 \ \mu s$; $\delta \le 2$; pulsed; T_{amb} = 25 °C	-	-	2	μΑ
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _{amb} = 25 °C	-	-	10	pF
t _{rr}	reverse recovery time	I_F = 10 mA; I_R = 10 mA; R_L = 100 Ω; $I_{R(meas)}$ = 1 mA; T_{amb} = 25 °C	-	-	5	ns

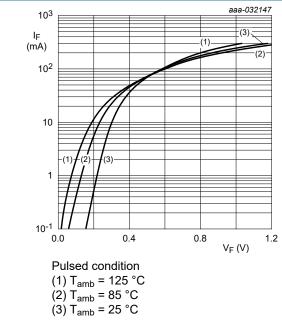


Fig. 3. Forward current as a function of forward voltage; typical values

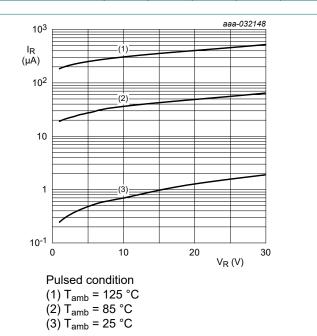
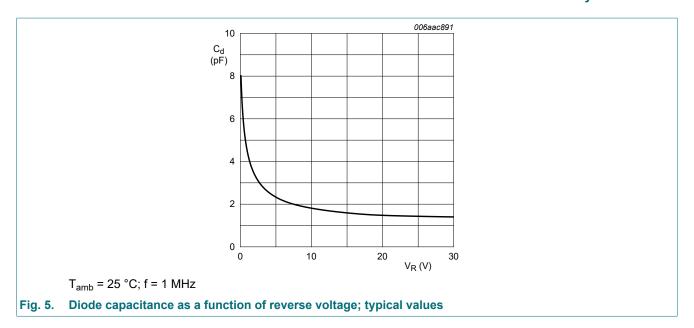


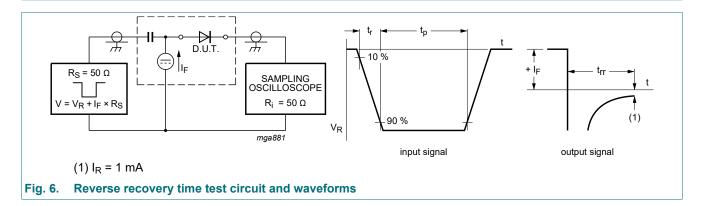
Fig. 4. Reverse current as a function of reverse voltage; typical values

Nexperia BAT54LS-Q

Schottky barrier diode



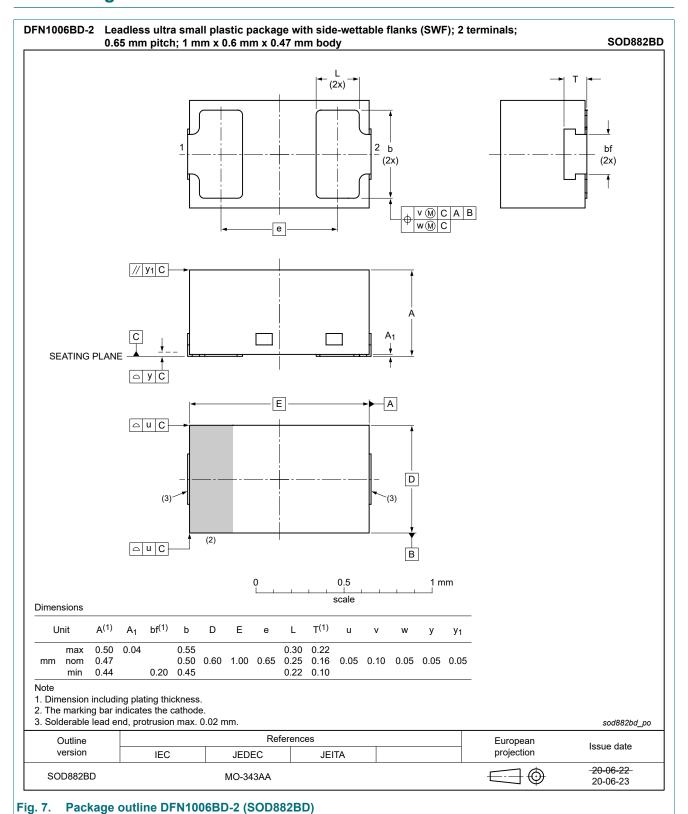
11. Test information



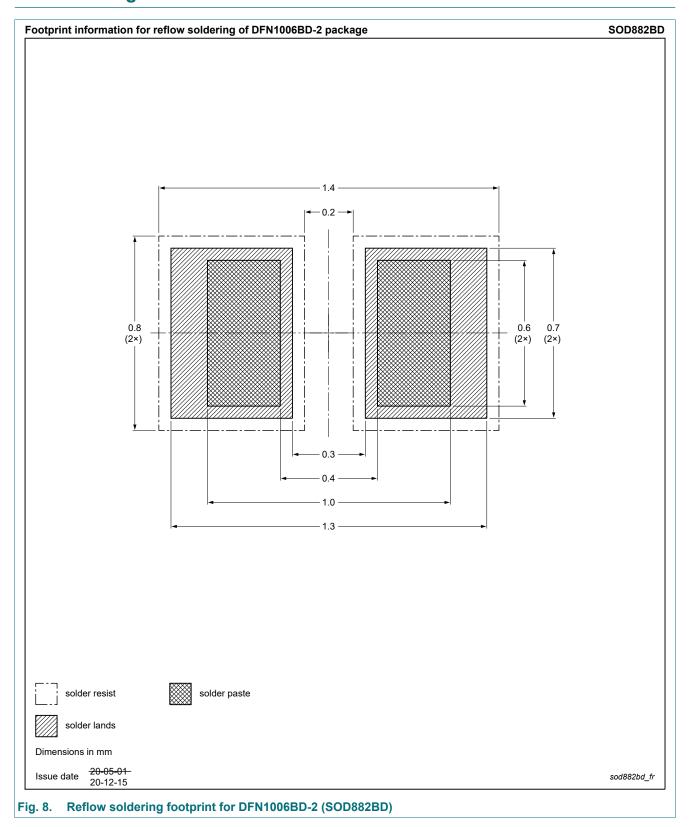
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



Nexperia BAT54LS-Q

Schottky barrier diode

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
BAT54LS-Q v.2	20210504	Product data sheet	-	BAT54LS-Q v.1				
Modifications:	Features and benefit	Features and benefits: added recommendation for automotive applications						
BAT54LS-Q v.1	20210125	Product data sheet	-	-				

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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BAT54LS-Q

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Date of release: 4 May 2021

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