

# MMBZ5V6AL

# Low capacitance unidirectional double ESD protection diode 14 June 2023 Product data sheet

# 1. General description

Unidirectional double ElectroStatic Discharge (ESD) protection diode in a common anode configuration, encapsulated in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package. The device is designed for ESD and transient overvoltage protection of up to two signal lines.

#### 2. Features

- Unidirectional ESD protection of two lines
- · Bidirectional ESD protection of one line
- Low diode capacitance: C<sub>d</sub> ≤ 280 pF
- Rated peak pulse power: P<sub>PPM</sub> = 24 W
- Ultra low leakage current: I<sub>RM</sub> = 5 μA
- ESD protection up to 30 kV (contact discharge)
- IEC 61000-4-2; level 4 (ESD)
- IEC 61643-321
- AEC-Q101 qualified

# 3. Applications

- · Computers and peripherals
- · Audio and video equipment
- · Cellular handsets and accessories
- · Automotive electronic control units
- Portable electronics

# 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	3	V
C <sub>d</sub>	diode capacitance	$f = 1 \text{ MHz}$ ; $V_R = 0 \text{ V}$ ; $T_{amb} = 25 ^{\circ}\text{C}$	-	210	280	pF



### Low capacitance unidirectional double ESD protection diode

# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode	3	3
2	K2	cathode		
3	A	common anode	1 2 SOT23	1 2 006aaa154

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
MMBZ5V6AL		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

# 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
MMBZ5V6AL	RR%

[1] % = placeholder for manufacturing site code

#### Low capacitance unidirectional double ESD protection diode

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Aboluste Maximum Rating System (IEC 60134)

Symbol	Parameter	Conditions		Min	Max	Unit
P <sub>PPM</sub>	rated peak pulse power	t <sub>p</sub> = 10/1000 μs	[1] [2]	-	24	W
I <sub>PPM</sub>	rated peak pulse current		[1] [2]	-	3	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[3]	-	265	mW
			[4]	-	290	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maxim	um ratings					
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2; contact discharge	[5] [2]	-	30	kV
	voltage	IEC 61000-4-2; air discharge		-	15	kV
		machine model	[2]	-	2	kV

- [1] In accordance with IEC 61643-321 (10/1000 µs current waveform).
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [5] Device stressed with ten non-repetitive ESD pulses.

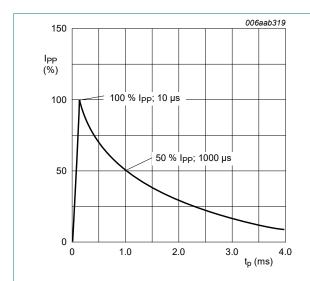


Fig. 1. 10/1000 μs pulse waveform according to IEC 61643-321

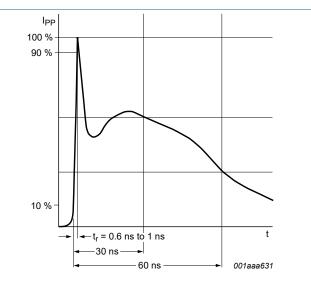


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

#### Low capacitance unidirectional double ESD protection diode

## 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub> thermal resistance from junction to ambient	in free air	[1]	-	-	460	K/W	
		[2]	-	-	420	K/W	
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[3]	-	-	150	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [3] Measured from pin 1 or 2 to pin 3.

### 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 10 mA; T <sub>amb</sub> = 25 °C		-	-	0.9	V
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	3	V
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C		5.32	5.6	5.88	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 3 V; T <sub>amb</sub> = 25 °C		-	0.24	5	μA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	210	280	pF
V <sub>CL</sub>	clamping voltage	$I_{PPM} = 3 \text{ A; } t_p = 10/1000  \mu\text{s;}$ $T_{amb} = 25 \text{ °C}$	[1] [2]	-	-	8	V
S <sub>Z</sub>	temperature coefficient	I <sub>Z</sub> = 20 mA		-	1.7	-	mV/K

- [1] In accordance with IEC 61643-321(10/1000 µs current waveform).
- [2] Measured from pin 1 or 2 to pin 3.

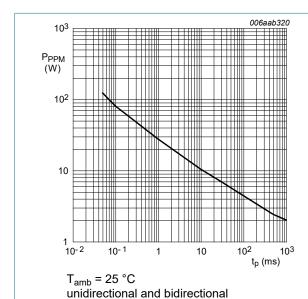


Fig. 3. Rated peak pulse power as a function of exponential pulse duration (rectangular waveform); typical values

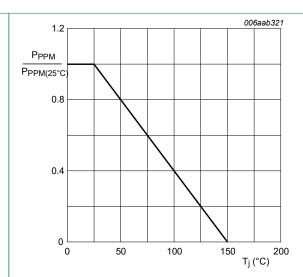


Fig. 4. Relative variation of rated peak pulse power as a function of junction temperature; typical values

#### Low capacitance unidirectional double ESD protection diode

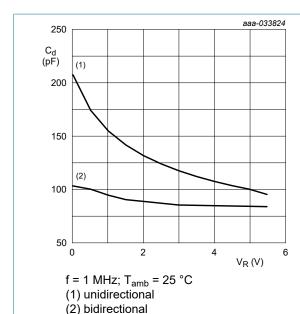


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

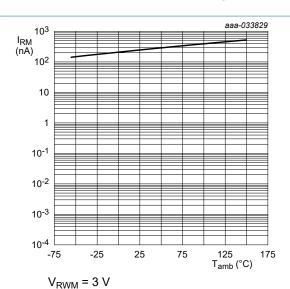


Fig. 6. Reverse leakage current as a function of ambient temperature; typical values

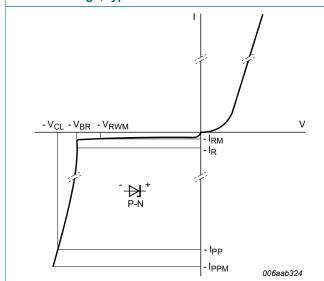


Fig. 7. V-I characteristics for a bidirectional ESD protection diode

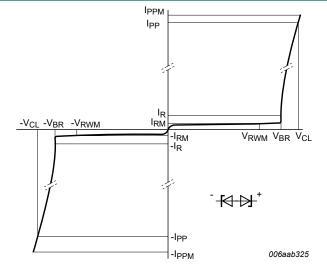
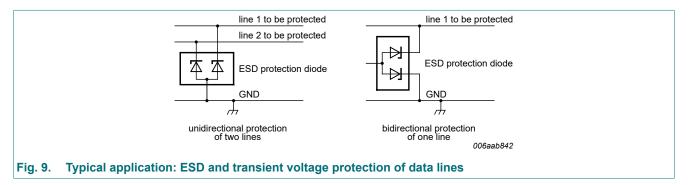


Fig. 8. V-I characteristics for a bidirectional ESD protection diode

#### Low capacitance unidirectional double ESD protection diode

# 11. Application information

The device is designed for the protection of up to two unidirectional data lines from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarities are either positive or negative with respect to ground.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

### Low capacitance unidirectional double ESD protection diode

# 12. Package outline

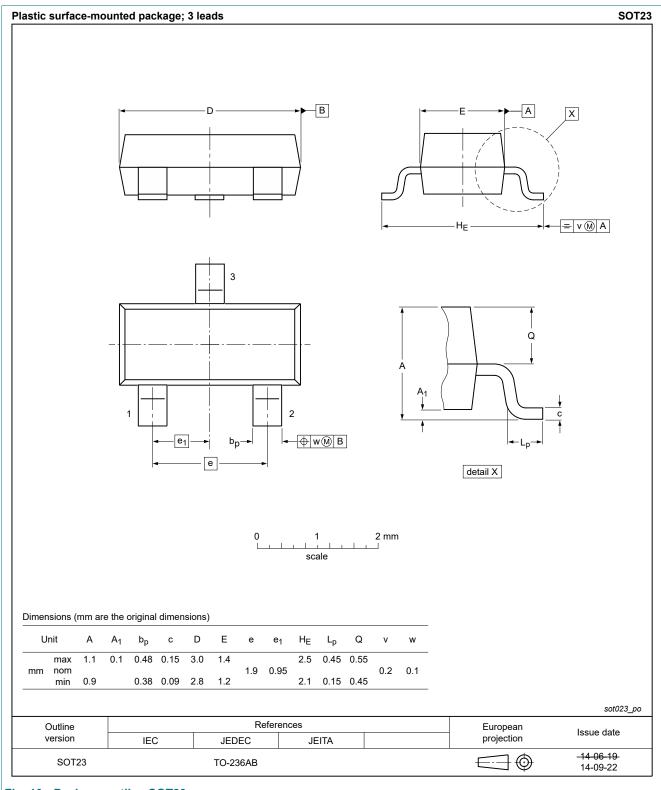
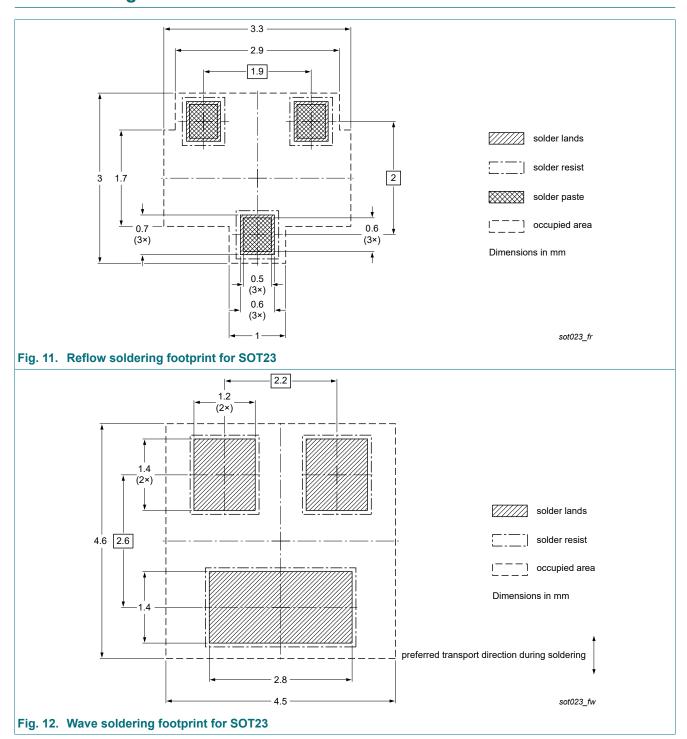


Fig. 10. Package outline SOT23

### Low capacitance unidirectional double ESD protection diode

# 13. Soldering



## Low capacitance unidirectional double ESD protection diode

# 14. Revision history

#### Table 8. Revision history

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Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
MMBZ5V6AL v.3	20230614	Product data sheet	-	MMBZXAL_SER_2				
Modifications:	<ul> <li>Family data sheet re</li> </ul>	Family data sheet reduced to single type data sheet						
MMBZXAL_SER_2	20091210	Product data sheet	-	MMBZXVAL_SER_1				
MMBZXVAL_SER_1	20080901	Product data sheet	-	-				

#### Low capacitance unidirectional double ESD protection diode

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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