Product data sheet

1. General description

PNP general-purpose transistor in a small SOT23 plastic package. NPN complement: PMBTA06.

2. Features and benefits

- High current (max. 500 mA)
- Low voltage (max. 80 V).
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

• General purpose switching and amplification, e.g. telephony and professional communication equipment.

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-80	V
Ic	collector current		-	-	-500	mA
h _{FE}	DC current gain	V_{CE} = -1 V; I_{C} = -10 mA; T_{amb} = 25 °C	100	-	-	



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	Е	emitter		C
3	С	collector		В
				E
			SOT23	sym132

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMBTA56-Q		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PMBTA56-Q	%2G

[1] % = placeholder for manufacturing site code

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-80	V
V_{CEO}	collector-emitter voltage	open base		-	-80	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-500	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-1	А
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
uiy-a)	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

^[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

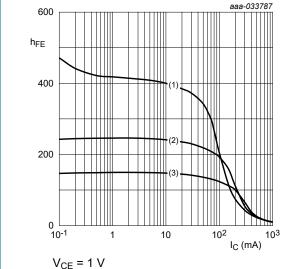
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10. Characteristics

Table 7. Characteristics

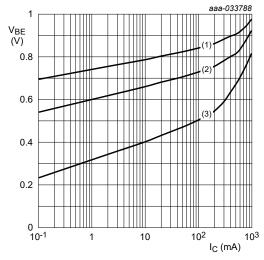
 T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = -100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$	-80	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = -1 mA; I _B = 0 A; T _{amb} = 25 °C	-80	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage (collector open)	$I_E = -100 \mu A; I_C = 0 A; T_{amb} = 25 °C$	-5	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = -80 V; I _E = 0 A	-	-	-50	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}$	-	-	-50	nA
h _{FE}	DC current gain	V _{CE} = -1 V; I _C = -10 mA; T _{amb} = 25 °C	100	-	-	
		V _{CE} = -1 V; I _C = -100 mA	100	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -100 mA; I _B = -10 mA	-	-	-0.25	V
V _{BE}	base-emitter voltage	V _{CE} = -1 V; I _C = -100 mA	-	-	-1.2	V
f _T	transition frequency	V _{CE} = -1 V; I _C = -100 mA; f = 100 MHz	50	-	-	MHz



(3)
$$T_{amb} = -40 \, ^{\circ}C$$

Fig. 1. DC current gain as a function of collector current; typical values



 $V_{CE} = 5 V$

(1)
$$T_{amb} = -40 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 2. Base-emitter voltage as a function of collector current; typical values

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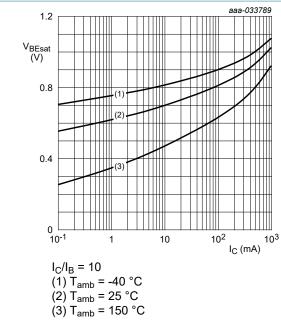
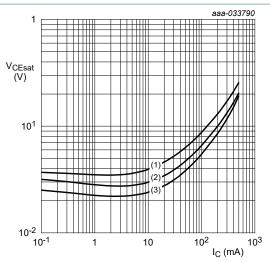
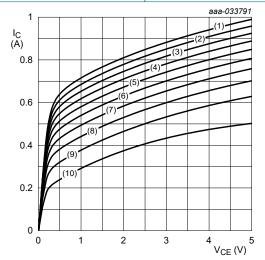


Fig. 3. Base-emitter saturation voltage as a function of Fig. 4. collector current; typical values



 $I_{C}/I_{B} = 10$ (1) $T_{amb} = 150 \,^{\circ}C$ (2) $T_{amb} = 25 \,^{\circ}C$ (3) $T_{amb} = -55 \,^{\circ}C$

Collector-emitter saturation voltage as a function of collector current; typical values



 T_{amb} = 25 °C (1) I_B = 50 mA

 $(2) I_B = 45 \text{ mA}$

(3) $I_B = 40 \text{ mA}$ (4) $I_B = 35 \text{ mA}$

(5) $I_B = 30 \text{ mA}$

(6) $I_B = 35 \text{ mA}$ (6) $I_B = 25 \text{ mA}$ (7) $I_B = 20 \text{ mA}$ (8) $I_B = 15 \text{ mA}$ (9) $I_B = 10 \text{ mA}$

 $(10) I_B = 5 \text{ mA}$

Fig. 5. Collector current as a function of collector-emitter voltage; typical values

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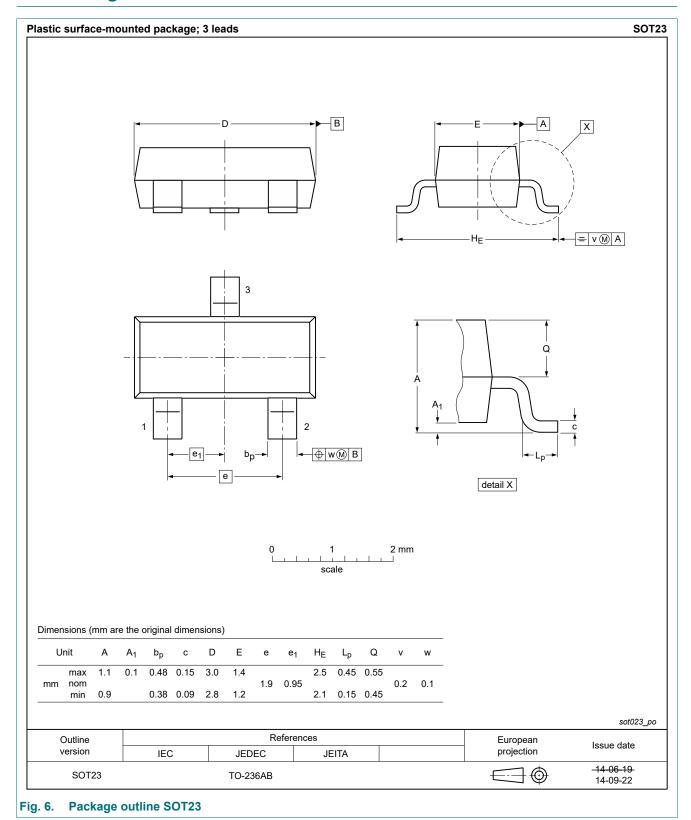
11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

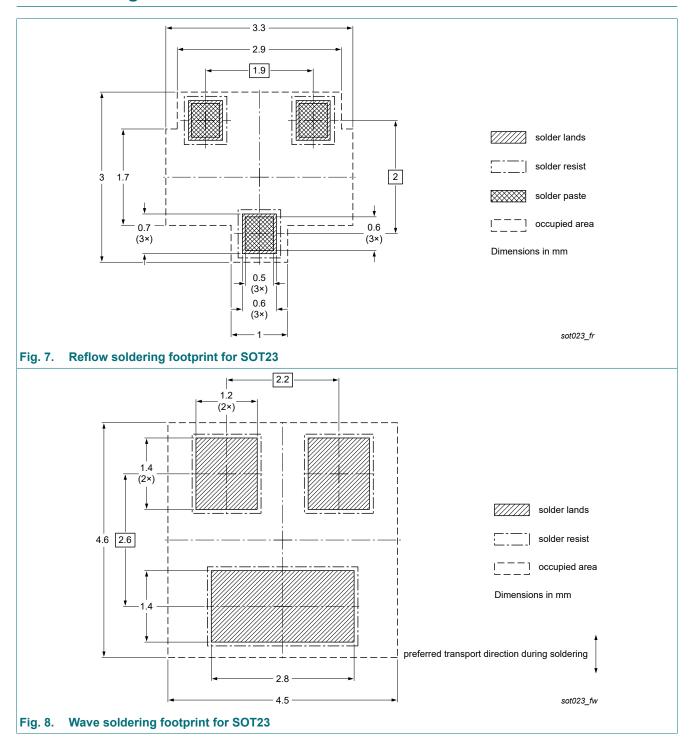
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12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBTA56-Q v.1	20210804	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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