

ESD protection for In-vehicle networks 2 October 2021

**Product data sheet** 

### 1. General description

ESD protection device in a small SOT323 (SC70) Surface-Mounted Device (SMD) plastic package designed to protect two automotive In-vehicle network bus lines from the damage caused by ElectroStatic discharge (ESD) and other transients.

### 2. Features and benefits

- Reverse stand-off voltage: V<sub>RWM</sub> = 27 V
- Low clamping voltage: V<sub>CL</sub>= 31 V at I<sub>PP</sub> = 1 A
- ESD protection up to 30 kV (IEC 61000-4-2)
- Ultra low capacitance: C<sub>d</sub> = 10 pF
- ESD protection up to 30 kV (ISO 10605; C = 150 pF; R = 330 Ω)
- High temperature capability: T<sub>i</sub> = 175 °C
- AEC-Q101 qualified

### 3. Applications

ESD protection for In-vehicle network lines in automotive environments

- CAN-FD
- CAN
- FlexRay
- SENT

### 4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	27	V
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1] [2]	-	-	3.9	A
V <sub>CL</sub>	clamping voltage	I <sub>PPM</sub> = 1 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[3] [2]	-	31	42	V

[1] According to IEC 61000-4-5.

[2] Measured from pin 1 or 2 to pin 3.

[3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

# nexperia

# 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	3	
2	K2	cathode (diode 2)		
3	CC	common cathode		
			1 2 2 SC-70 (SOT323)	006aaa155

### 6. Ordering information

#### Table 3. Ordering information

Type number	Package			
	Name	Description	Version	
PESD2CANFD27L-U		plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	SOT323	

### 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
PESD2CANFD27L-U	3N%

[1] % = placeholder for manufacturing site code

### 8. Limiting values

#### Table 5. Limiting values

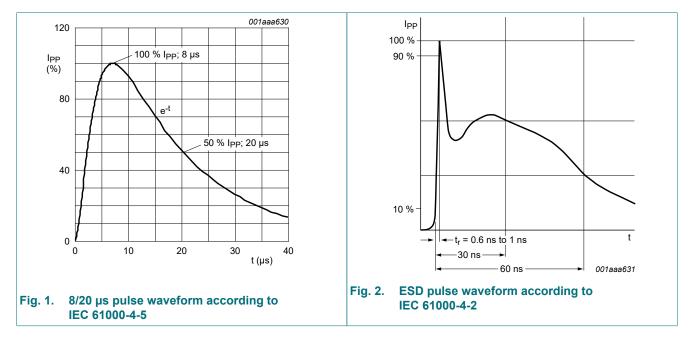
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1] [2]	-	3.9	А
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C
ESD maximu	um ratings	·				
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2; contact discharge	[2] [3]	-	30	kV
	voltage	ISO 10605; contact discharge; C = 330 pF, R = 330 $\Omega$	[2] [3]	-	30	kV
		ISO 10605; contact discharge; C = 150 pF, R = 330 $\Omega$	[2] [3]	-	30	kV

[1] According to IEC 61000-4-5.

[2] Measured from pin 1 or 2 to pin 3.

[3] Device stressed with ten non-repetitive ESD pulses.



### 9. Characteristics

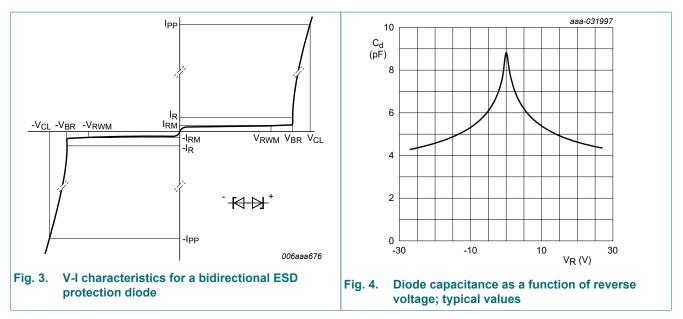
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	27	V
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 10 mA; T <sub>amb</sub> = 25 °C	[1]	28	-	38	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 27 V; T <sub>amb</sub> = 25 °C	[1]	-	1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	[1]	-	9	10	pF
$\Delta C_d/C_d$	diode capacitance matching		[2]	-	0.5	-	%
V <sub>CL</sub>	clamping voltage	I <sub>PPM</sub> = 1 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[3] [1]	-	31	42	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C	[4] [1]	-	0.55	-	Ω

[1] Measured from pin 1 or 2 to pin 3.

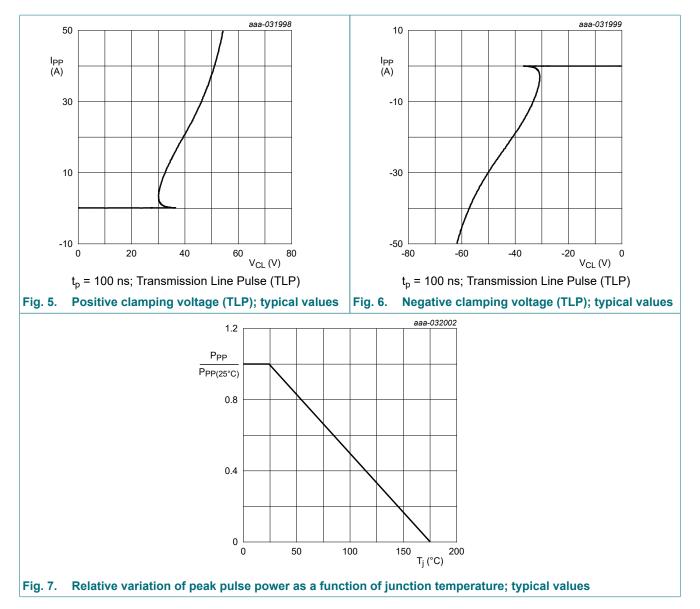
[2]  $\Delta C_d$  is the difference of the capacitance measured between pin 1 and pin 3 and the capacitance measured between pin 2 and pin 3.

[3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

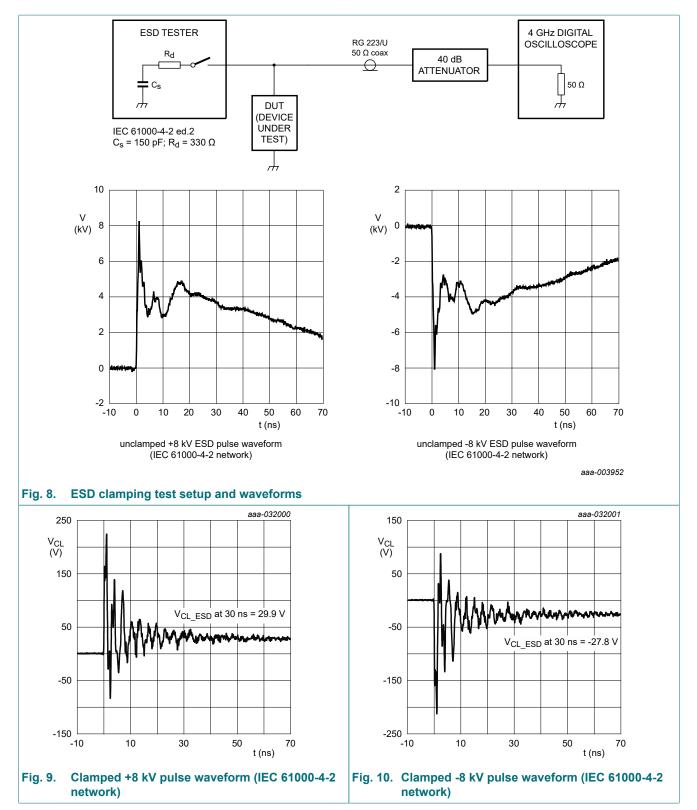
[4] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008



### ESD protection for In-vehicle networks

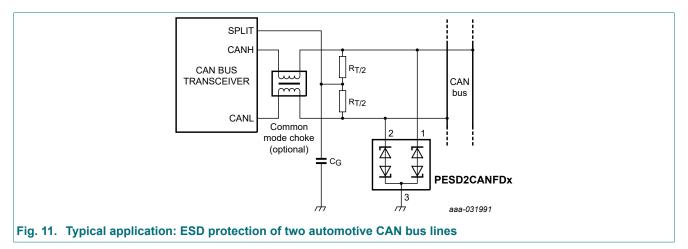


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### **10.** Application information

The device is designed for the protection of two automotive in-vehicle bus lines, e.g. CAN (FD), from the damage caused by ESD and surge pulses.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

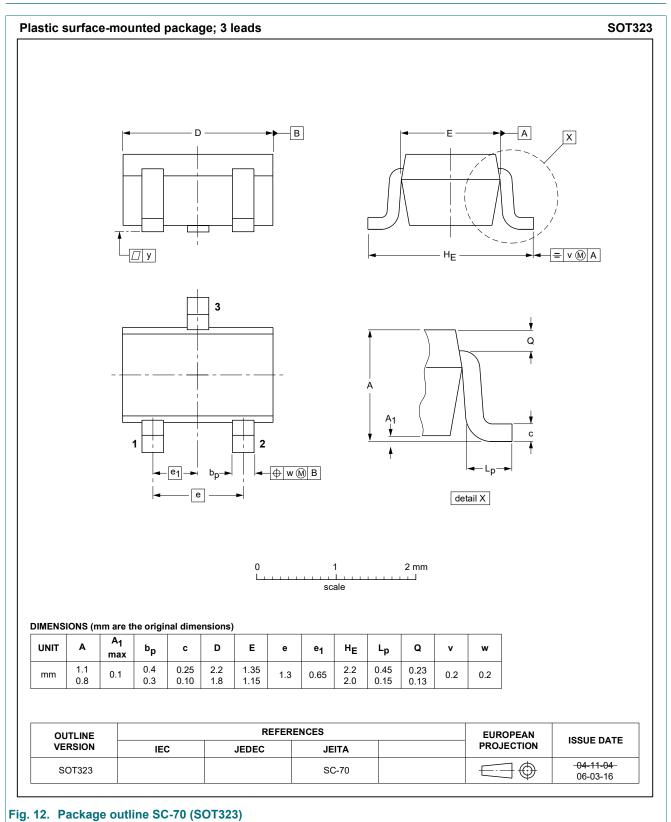
- **1.** Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

### 11. Test information

#### **Quality information**

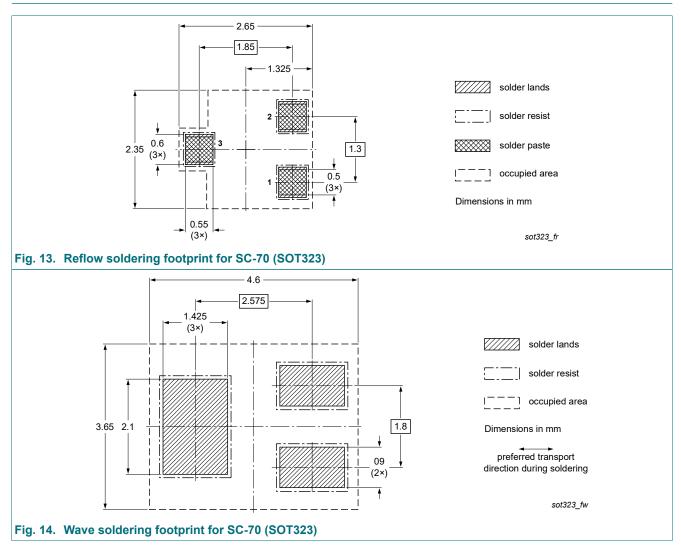
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### 12. Package outline



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### 13. Soldering



# 14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PESD2CANFD27L-U v.2	20211002	Product data sheet	-	PESD2CANFD27L-U v.1	
Modifications:	Chapter "Marking": corrected the marking code				
PESD2CANFD27L-U v.1	20200908	Product data sheet	-	-	

PESD2CANFD27L-U

### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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