Product data sheet

1. General description

High-voltage switching diode, encapsulated in an ultra small DFN1412D-3 (SOT8009, JEDEC MO340-CA) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

2. Features and benefits

- High switching speed: t_{rr} ≤ 50 ns
- Low leakage current
- High reverse voltage: V_R ≤ 200 V
- Low capacitance: C_d ≤ 5 pF
- · Leadless ultra small SMD plastic package
- Low package height of 0.5 mm
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- · High-speed switching
- General-purpose switching
- Voltage clamping
- Reverse polarity protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _F	forward current	T _j = 25 °C	[1]	-	-	250	mA
V _R	reverse voltage			-	-	200	V
V _F	forward voltage	I _F = 200 mA; T _j = 25 °C		-	-	1.25	V
V_{RRM}	repetitive peak reverse voltage	T _j = 25 °C		-	-	250	V
I _R	reverse current	V _R = 200 V; T _j = 25 °C		-	-	100	nA
t _{rr}	reverse recovery time	I_F = 30 mA; I_R = 30 mA; R_L = 100 Ω; $I_{R(meas)}$ = 3 mA; T_{amb} = 25 °C		-	-	50	ns

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided 70 µm copper, tin-plated and standard footprint.



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	А	anode		
2	n.c.	not connected		A
3	К	cathode	3	n.c
			Bottom view DFN1412D-3 (SOT8009)	aaa-021941

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BAS21QC-Q	DFN1412D-3	plastic, leadless ultra small outline package with sidewettable flanks (SWF); 3 terminals; 0.8 mm pitch; 1.4 mm x 1.2 mm x 0.48 mm body	SOT8009		

7. Marking

Table 4. Marking codes

Type number	Marking code
BAS21QC-Q	9Q

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating Sytem (IEC 60134)

Symbol	Parameter	Conditions		Min	Max	Unit
V_{RRM}	repetitive peak reverse voltage	T _j = 25 °C		-	250	V
V_R	reverse voltage			-	200	V
I _F	forward current		[1]	-	250	mA
I _{FSM}	non-repetitive peak	t _p = 1 μs; square wave; T _{j(init)} = 25 °C		-	9	А
	forward current	t _p = 100 μs; square wave; T _{j(init)} = 25 °C		-	3	Α
		t _p = 10 ms; square wave; T _{j(init)} = 25 °C		-	1.7	Α
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	625	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	440	mW
			[2]	-	750	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided 70 µm copper, tin-plated and standard footprint.

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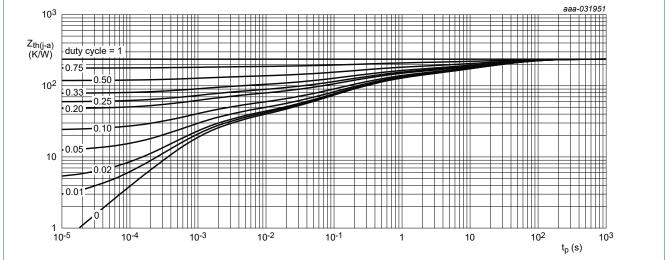
^[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided 70 µm copper, tin-plated and mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)} thermal resistance from	In free air	[1]	-	-	285	K/W	
	junction to ambient		[2]	-	-	160	K/W

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided 70 µm copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided 70 µm copper, tin-plated and mounting pad for cathode 1 cm².



FR4 PCB, single-sided 70 µm copper, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

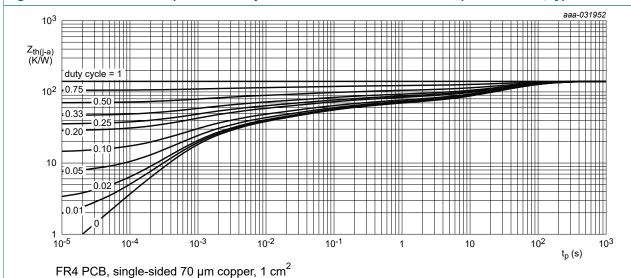


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage	I _F = 100 mA; T _j = 25 °C	-	-	1	V
		I _F = 200 mA; T _j = 25 °C	-	-	1.25	V
I _R	reverse current	V _R = 200 V; T _j = 25 °C	-	-	100	nA
		V _R = 200 V; T _j = 150 °C	-	-	100	μA
C _d	diode capacitance	V _R = 0 V; f = 1 MHz; T _{amb} = 25 °C	-	-	5	pF
t _{rr}	reverse recovery time	I_F = 30 mA; I_R = 30 mA; R_L = 100 Ω; $I_{R(meas)}$ = 3 mA; T_{amb} = 25 °C	-	-	50	ns

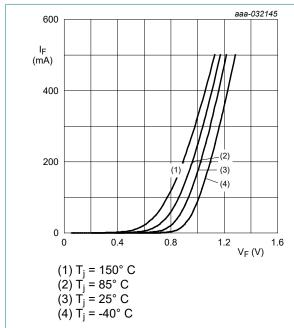


Fig. 3. Forward current as a function of forward voltage; typical values

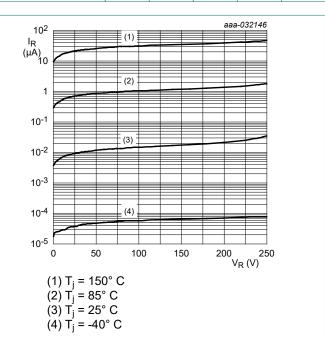


Fig. 4. Reverse current as a function of reverse voltage; typical values

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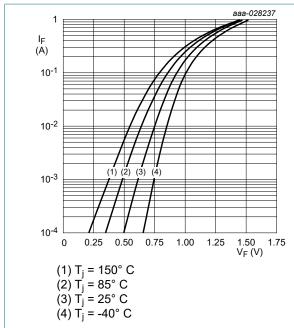


Fig. 5. Forward current as a function of forward voltage; typical values; (logarithmic scale)

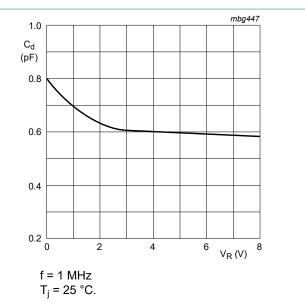
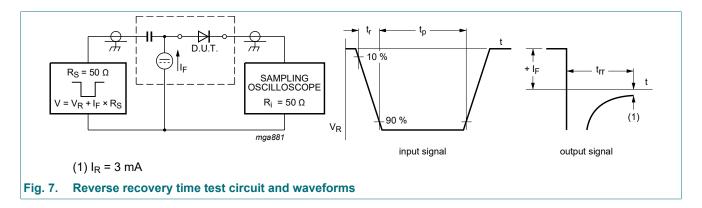


Fig. 6. Diode capacitance as a function of reverse voltage; typical values.

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11. Test information



Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

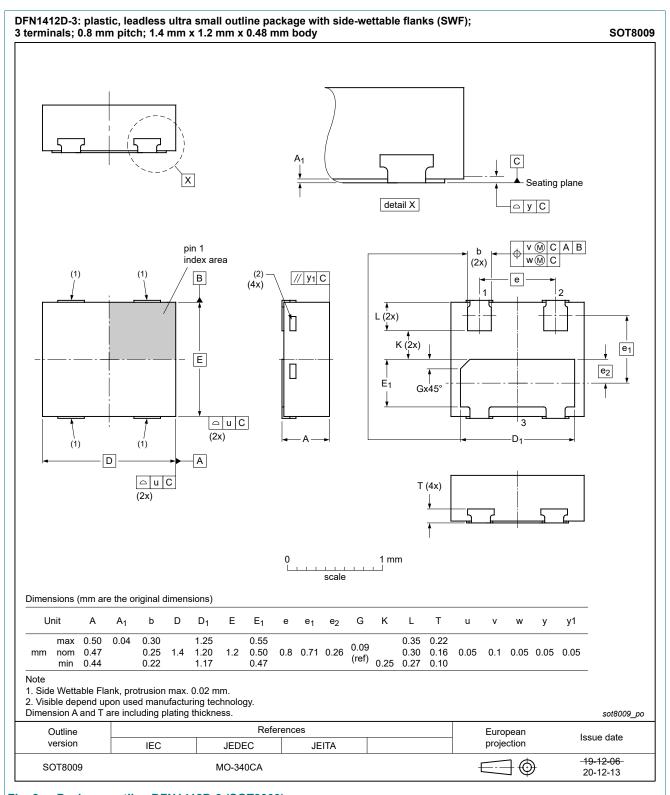
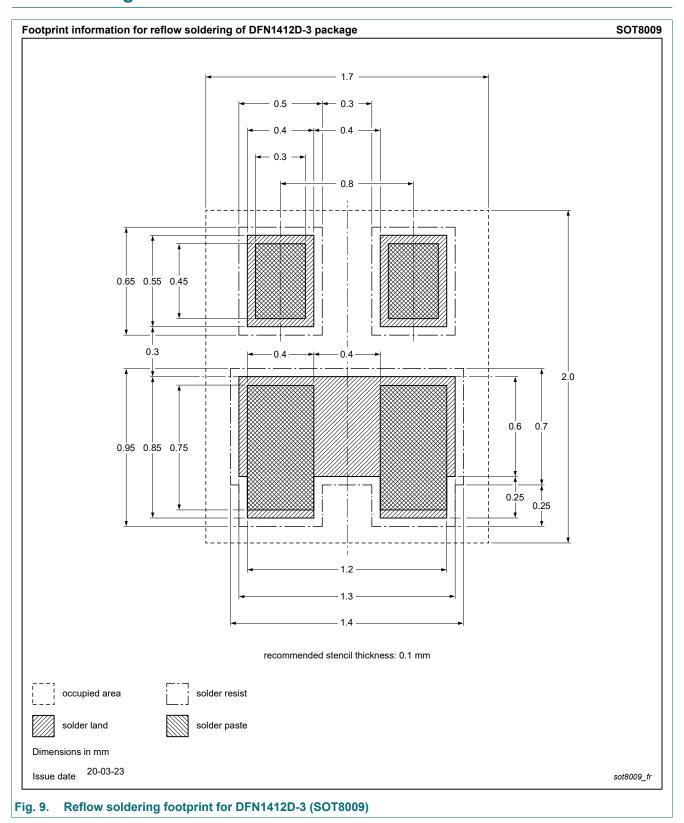


Fig. 8. Package outline DFN1412D-3 (SOT8009)

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13. Soldering



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14. Revision history

Table 8. Revision history

Table 6. Reviolet flictory							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BAS21QC-Q v.2	20210504	Product data sheet	-	BAS21QC-Q v.1			
Modifications:	Features and benefit	Features and benefits: added recommendation for automotive applications					
BAS21QC-Q v.1	20210221	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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