Product data sheet

1. General description

NPN high-voltage transistor in a SOT223 (SC73) Surface-Mounted Device plastic package.

2. Features and benefits

- Low current (max. 300 mA)
- High voltage (max. 400 V)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

Telecommunication

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	500	V
I _C	collector current		-	-	300	mA
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 1 mA; T _{amb} = 25 °C	40	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	4	С
2	С	collector		
3	В	base		B—
4	С	collector	□1 □2 □3	Ė
			SC-73 (SOT223)	sym123



NPN high-voltage transistor

6. Ordering information

Table 3. Ordering information

	, , , , , , , , , , , , , , , , , , ,					
Type number	Package	e e				
	Name	Description	Version			
PZTA44-Q		plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223			

7. Marking

Table 4. Marking codes

Type number	Marking code
PZTA44-Q	PZTA44

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	500	V
V_{CEO}	collector-emitter voltage	open base		-	500	V
V_{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	300	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	300	mA
I _{BM}	peak base current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	1.35	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	91	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	10	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

^[2] For other mounting conditions, see "Thermal considerations for SOT223 in the General Part of associated Handbook".

^[2] For other mounting conditions, see "Thermal considerations for SOT223 in the General Part of associated Handbook".

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 400 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
	current	V _{CB} = 400 V; I _E = 0 A; T _j = 150 °C	-	-	10	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 1 mA; T _{amb} = 25 °C	40	-	-	
		V_{CE} = 10 V; I_{C} = 10 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	50	-	200	
		V_{CE} = 10 V; I_{C} = 50 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	45	-	-	
		V_{CE} = 10 V; I_{C} = 100 mA; pulsed; $t_{p} \le$ 300 µs; T_{amb} = 25 °C	40	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 1 mA; I _B = 0.1 mA; T _{amb} = 25 °C	-	-	400	mV
		I_C = 10 mA; I_B = 1 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	500	mV
		I_C = 50 mA; I_B = 5 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	750	V
V _{BEsat}	base-emitter saturation voltage	I_C = 10 mA; I_B = 1 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	850	mV
f _T	transition frequency	V_{CE} = 10 V; I_{C} = 10 mA; f = 100 MHz; T_{amb} = 25 °C	20	-	-	MHz
C _c	collector capacitance	$V_{CB} = 20 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 ^{\circ}\text{C}$	-	-	7	pF
C _e	emitter capacitance	V_{EB} = 500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	180	pF

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline

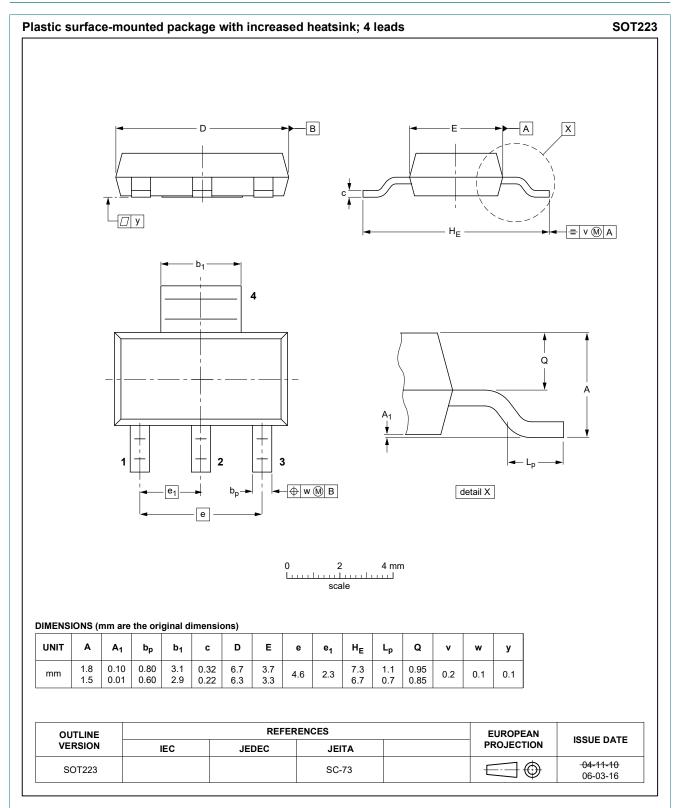
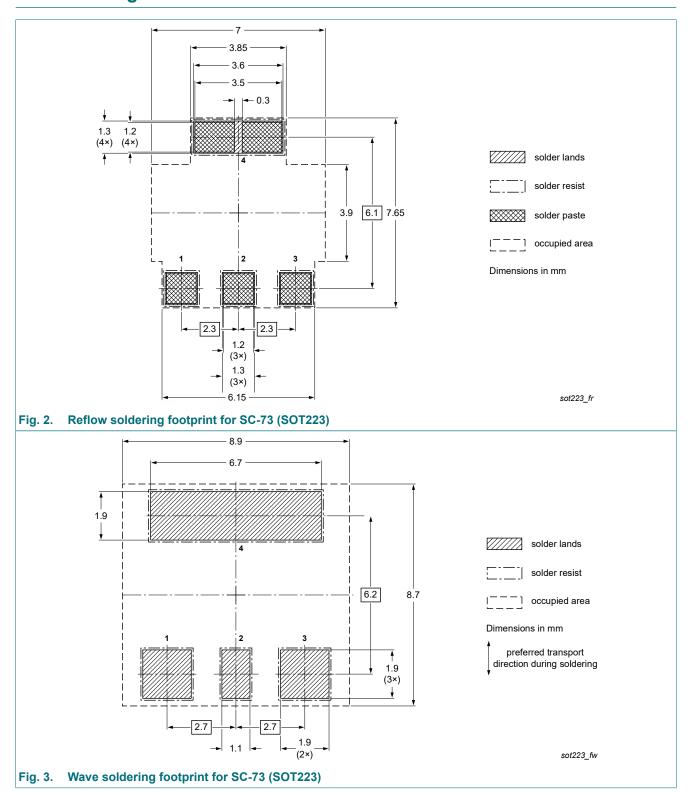


Fig. 1. Package outline SC-73 (SOT223)

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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PZTA44-Q v.1	20211209	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PZTA44-Q

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