

100 V, 10 A low leakage current Trench Schottky barrier rectifier 19 July 2024

Product data sheet

## 1. General description

Trench Schottky barrier rectifier encapsulated in a CFP15B (SOT1289B) power and flat lead Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Low forward voltage •
- Low Q<sub>rr</sub> and low I<sub>RM</sub> •
- · Low leakage current
- High power capability due to clip-bonding technology
- Small and flat lead SMD power plastic package
- AEC-Q101 gualified

## 3. Applications

- High efficiency DC-to-DC conversion
- Automotive LED lighting •
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- . **OR-ing**

## 4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> ≤ 162 °C		-	-	10	A
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	100	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 10 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	750	810	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 100 V; pulsed; T <sub>j</sub> = 25 °C	[1]	-	0.85	5	μA
		V <sub>R</sub> = 100 V; pulsed; T <sub>j</sub> = 125 °C	[1]	-	1.25	6	mA

[1] Very short pulse, in order to maintain a stable junction temperature.

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## 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	А	anode		
2	А	anode		
3	К	cathode		
			CFP15B (SOT1289B)	aaa-1

## 6. Ordering information

Table 3. Ordering information						
Type number						
	Name	Description	Version			
PMEG100T100ELPE	CFP15B	plastic, thermal enhanced ultra thin SMD package; 3 leads; 2.13 mm pitch; 5.8 x 4.3 x 0.95 mm body	<u>SOT1289B</u>			

## 7. Marking

Table 4. Marking codes						
Type number	Marking code					
PMEG100T100ELPE	100T L10E					

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	100	V
l <sub>F</sub>	forward current	δ = 1; T <sub>sp</sub> ≤ 158 °C		-	14.1	А
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> ≤ 162 °C		-	10	A
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8.3 ms; half sine wave; $T_{j(init)}$ = 25 °C		-	180	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

## 9. Thermal characteristics

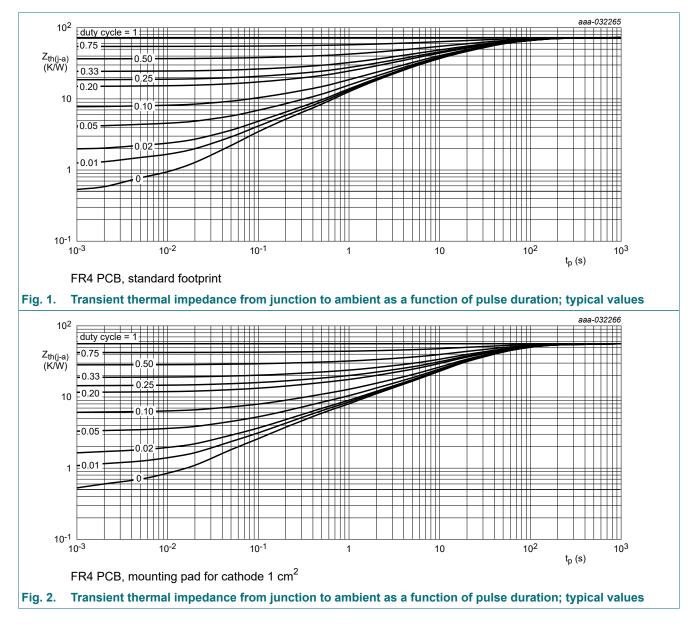
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub>	thermal resistance from	in free air	[1] [2]	-	-	90	K/W
	junction to ambient		[1] [3]	-	-	70	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[4]	-	-	3	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

[4] Soldering point of cathode tab.



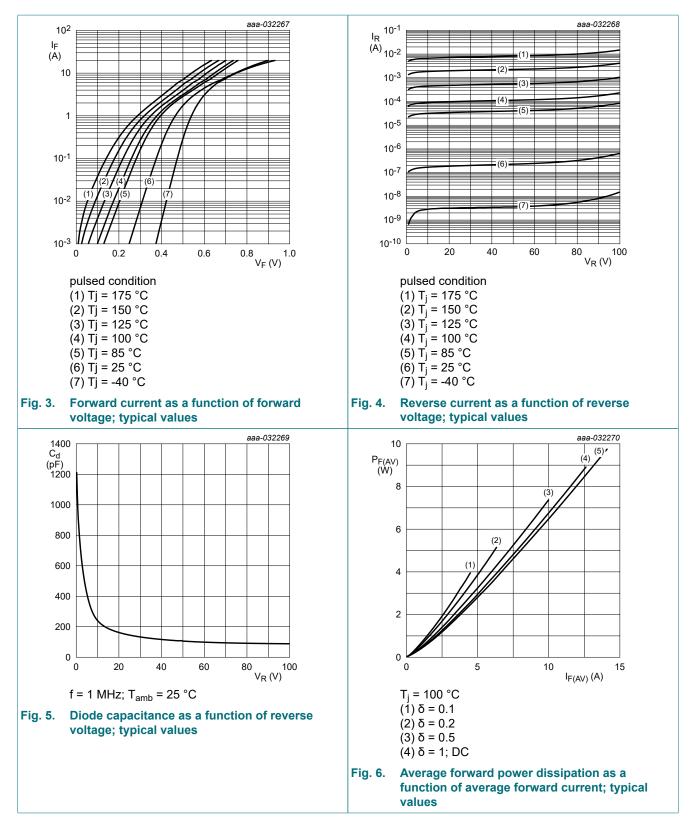
## **10. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)R</sub>	reverse breakdown voltage	I <sub>R</sub> = 1 mA; T <sub>j</sub> = 25 °C	[1]	100	-	-	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	460	520	mV
		I <sub>F</sub> = 3 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	545	610	mV
		I <sub>F</sub> = 5 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	620	690	mV
		I <sub>F</sub> = 8 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	705	780	mV
		I <sub>F</sub> = 10 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	750	810	mV
		I <sub>F</sub> = 10 A; pulsed; T <sub>j</sub> = -40 °C	[1]	-	755	820	mV
		I <sub>F</sub> = 10 A; pulsed; T <sub>j</sub> = 125 °C	[1]	-	615	690	mV
		I <sub>F</sub> = 10 A; pulsed; T <sub>j</sub> = 150 °C	[1]	-	580	650	mV
I <sub>R</sub>	reverse current	$V_R$ = 60 V; pulsed; T <sub>j</sub> = 25 °C	[1]	-	0.3	1.5	μA
		$V_R$ = 100 V; pulsed; $T_j$ = 25 °C	[1]	-	0.85	5	μA
		V <sub>R</sub> = 100 V; pulsed; T <sub>j</sub> = 125 °C	[1]	-	1.25	6	mA
		$V_R$ = 100 V; pulsed; T <sub>j</sub> = 150 °C	[1]	-	4.8	25	mA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	850	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	240	-	pF
t <sub>rr</sub>	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A}; T_j = 25 \ ^{\circ}\text{C}$		-	22	-	ns
	reverse recovery time ramp recovery	dI <sub>F</sub> /dt = 200 A/µs; I <sub>F</sub> = 6 A; V <sub>R</sub> = 26 V; T <sub>j</sub> = 25 °C		-	13	-	ns
I <sub>RM</sub>	peak reverse recovery current			-	1.3	-	A
Q <sub>rr</sub>	reverse recovery charge	1		-	11.5	-	nC
V <sub>FRM</sub>	peak forward recovery voltage	I <sub>F</sub> = 0.5 A; dI <sub>F</sub> /dt = 20 A/μs; T <sub>j</sub> = 25 °C		-	415	-	mV

[1] Very short pulse, in order to maintain a stable junction temperature.

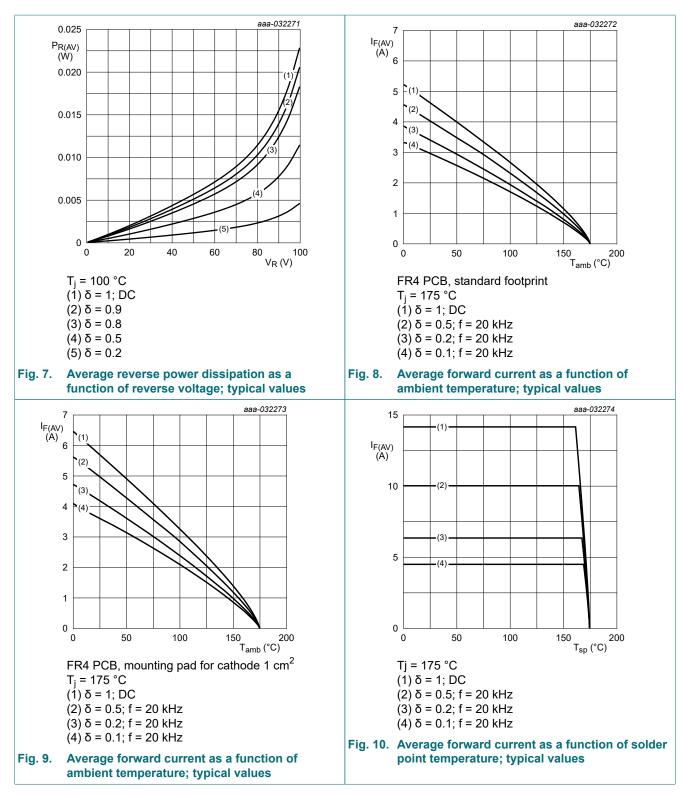
PMEG100T100ELPE

#### 100 V, 10 A low leakage current Trench Schottky barrier rectifier

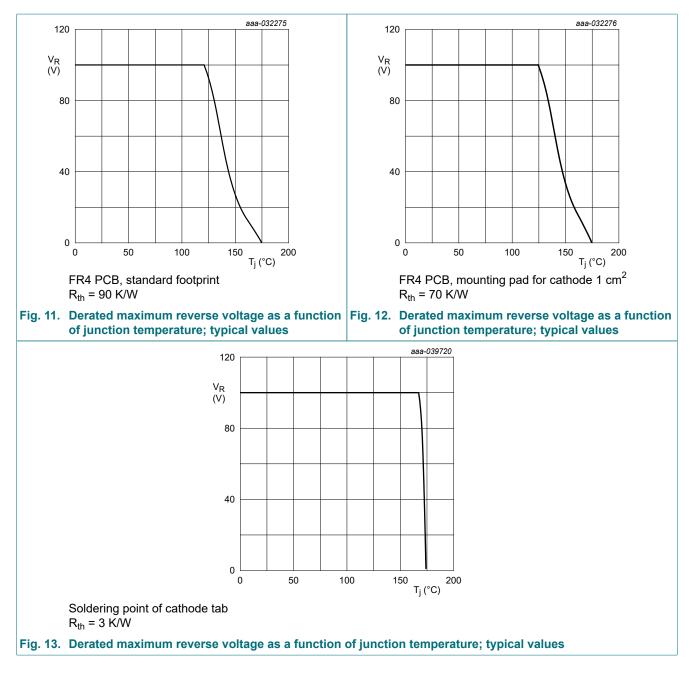


**Product data sheet** 

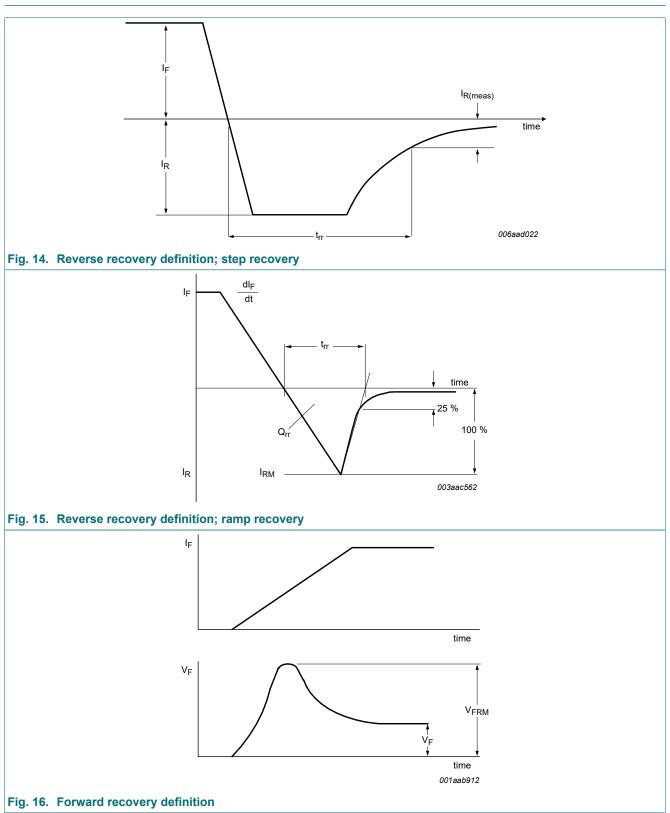
#### 100 V, 10 A low leakage current Trench Schottky barrier rectifier



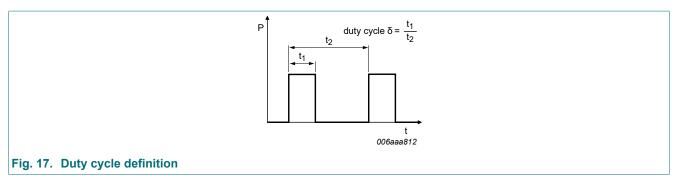
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## **11. Test information**



#### 100 V, 10 A low leakage current Trench Schottky barrier rectifier



The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)}=I_M \times \delta$  with  $I_M$  defined as peak current

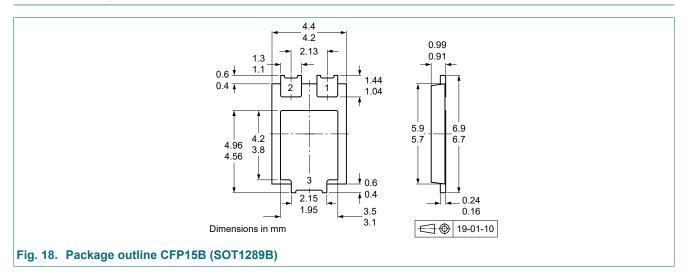
I<sub>RMS</sub>=I<sub>F(AV)</sub> at DC, and I<sub>RMS</sub>=I<sub>M</sub>×√δ

with  $\mathsf{I}_{\mathsf{RMS}}$  defined as RMS current.

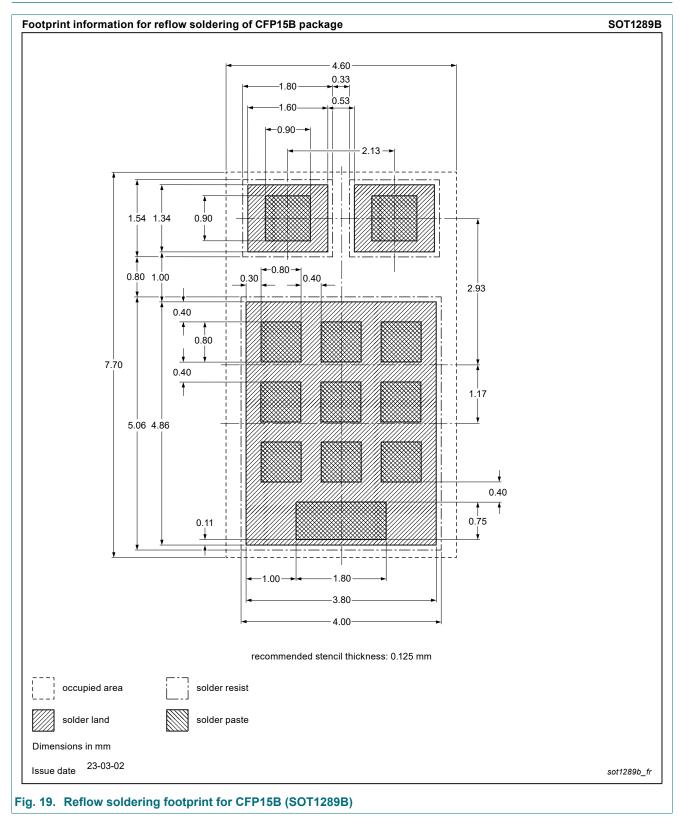
#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### 12. Package outline



## 13. Soldering



## 14. Revision history

Table 8. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG100T100ELPE v.4	20240719	Product data sheet	-	PMEG100T100ELPE v.3
Modifications:	Characteristic	acteristics: R <sub>th(j-sp)</sub> valu s:Q <sub>rr</sub> conditions chang s: Fig 13 changed		
PMEG100T100ELPE v.3	20240715	Product data sheet	-	PMEG100T100ELPE v.2
PMEG100T100ELPE v.2	20201203	Product data sheet	-	PMEG100T100ELPE v.1
PMEG100T100ELPE v.1	20200923	Preliminary data sheet	-	-

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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