



2PC4081S

NPN general-purpose transistor

1 January 2023

Product data sheet

1. General description

NPN transistor in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

PNP complement: 2PA1576S

2. Features and benefits

- Low current (max. 150 mA)
- Low voltage (max. 50 V)

3. Applications

- General-purpose switching
- Small signal amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CE0}	collector-emitter voltage	open base	-	-	50	V
I_C	collector current		-	-	150	mA
h_{FE}	DC current gain	$V_{CE} = 6\text{ V}$; $I_C = 1\text{ mA}$; $T_{amb} = 25\text{ °C}$	270	-	560	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SC-70 (SOT323)	 aaa-027673
2	E	emitter		
3	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
2PC4081S	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	SOT323

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
2PC4081S	Z%S

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V _{EBO}	emitter-base voltage	open collector		-	7	V
I _C	collector current			-	150	mA
I _{CM}	peak collector current			-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.

12. Soldering

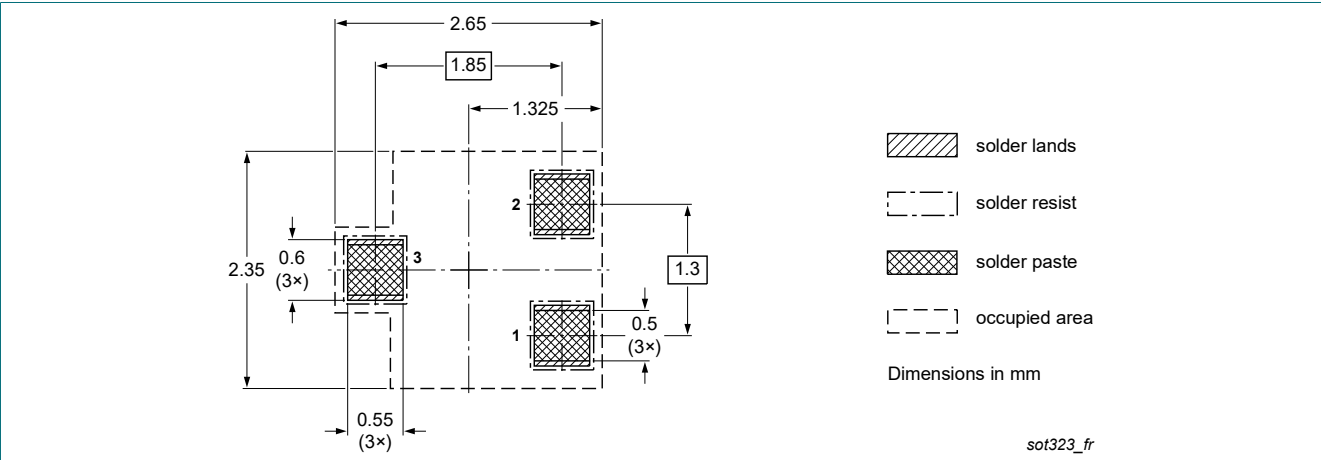


Fig. 2. Reflow soldering footprint for SC-70 (SOT323)

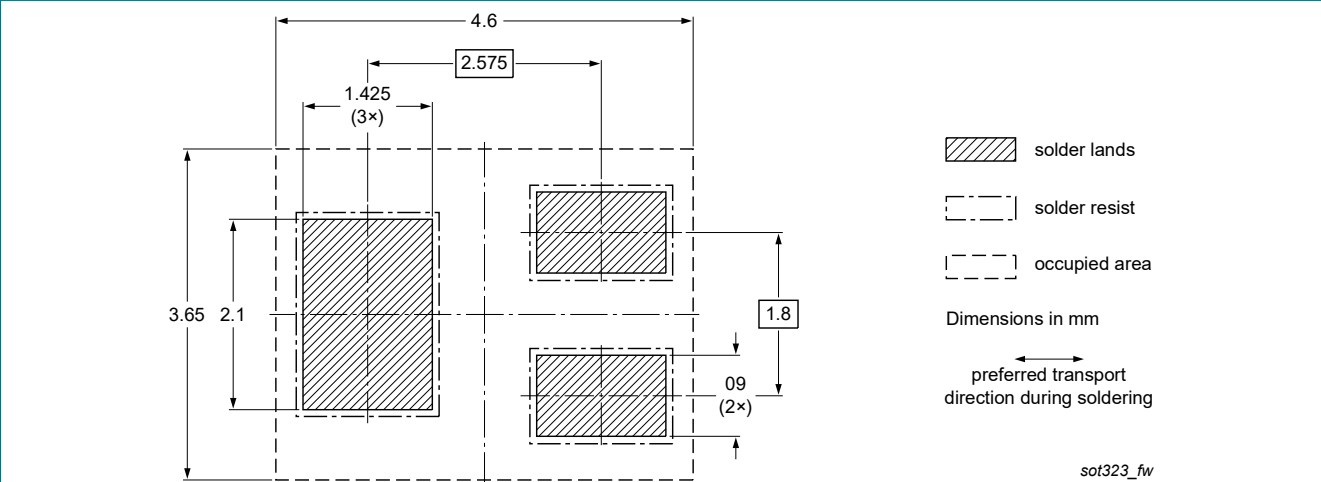


Fig. 3. Wave soldering footprint for SC-70 (SOT323)

13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
2PC4081S v.7	20230101	Product data sheet	-	2PC4081_6
Modifications:	<ul style="list-style-type: none">The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.Legal texts have been adapted to the new company name where appropriate.Family data sheet reduced to single type data sheet.Product changed to non automotive. Please refer to the automotive product(s) with -Q.			
2PC4081_6	20091117	Product data sheet	-	2PC4081_5
2PC4081_5	20041125	Product data sheet	-	2PC4081_4
2PC4081_4	19990408	Product specification	-	2PC4081_3
2PC4081_3	19970704	Product specification	-	2PC4081_2
2PC4081_2	19931213	-	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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