## 1. General description

Extremely symmetrical bidirectional ElectroStatic Discharge (ESD) protection diode. This device is housed in a DSN0603-2 (SOD962) leadless ultra small Surface-Mounted Device (SMD) package designed to protect one signal line from the damage caused by ESD and other transients.

## 2. Features and benefits

- · Bidirectional ESD protection of one line
- · Extremely symmetrical layout
- Very low diode capacitance C<sub>d</sub> = 6.5 pF maximum values
- Low clamping to protect sensitive I/Os
- · Low inductance protection path to ground
- ESD protection up to ±12 kV according to IEC 61000-4-2
- Ultra small SMD package

## 3. Applications

- Cellular handsets and accessories
- Portable electronics
- · Communication systems
- Computers and peripherals

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage			-	-	16	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	5.7	6.5	pF
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1] [2]	-	-	1.3	Α

- [1] According to IEC 61000-4-5 and IEC 61643-321.
- [2] In positive and negative direction.



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		K1   K2
2	K2	cathode (diode 2)		sym045
			Transparent top view	
			DSN0603-2 (SOD962-2)	

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
PESD16VV1BSF		silicon, leadless ultra small package; 2 terminals; 0.4 mm pitch; 0.6 mm x 0.3 mm x 0.3 mm body	SOD962-2

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PESD16VV1BSF	W

# 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit	
$V_{RWM}$	reverse standoff voltage			-	16	V	
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1] [2]	-	1.3	Α	
Tj	junction temperature			-	150	°C	
T <sub>amb</sub>	ambient temperature			-40	125	°C	
T <sub>stg</sub>	storage temperature			-65	150	°C	
ESD maximum i	ESD maximum ratings						
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[3]	-	12	kV	

- [1] According to IEC 61000-4-5 and IEC 61643-321.
- [2] In positive and negative direction.
- [3] Device stressed with ten non-repetitive ESD pulses.

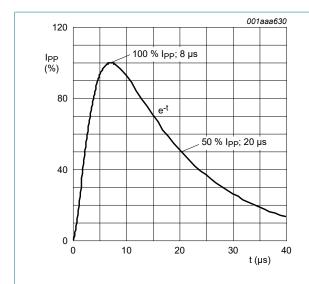


Fig. 1.  $8/20~\mu s$  pulse waveform according to IEC 61000-4-5

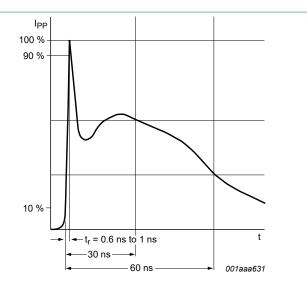


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

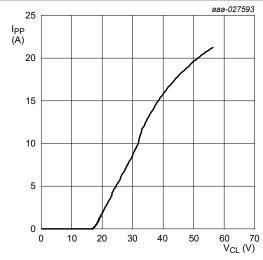
## 9. Characteristics

#### **Table 6. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C		16.2	18	-	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 16 V; T <sub>amb</sub> = 25 °C		-	1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	5.7	6.5	pF
V <sub>CL</sub>	clamping voltage	$I_{PP}$ = 1.3 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[1]	-	22.6	-	V
		$I_{PP}$ = 8 A; $t_p$ = TLP; $T_{amb}$ = 25 °C	[2]	-	29	-	V
		I <sub>PP</sub> = 16 A; t <sub>p</sub> = TLP; T <sub>amb</sub> = 25 °C	[2]	-	40.5	-	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C	[2]	-	1.1	-	Ω
		I <sub>R</sub> = -10 A; T <sub>j</sub> = 25 °C	[2]	-	1.1	-	Ω
normalized to	attenuation at 1 MHz						
f <sub>-3dB</sub>	-3 dB cut-off frequency	T <sub>amb</sub> = 25 °C		-	0.97	-	GHz

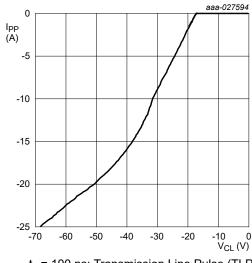
<sup>[1]</sup> According to IEC 61000-4-5 and IEC 61643-321.

<sup>[2]</sup> Non-repetitive current pulse, Transmission Line Pulse (TLP) t<sub>p</sub> = 100 ns; square pulse; ANSI / ESD STM5.5.1-2008.



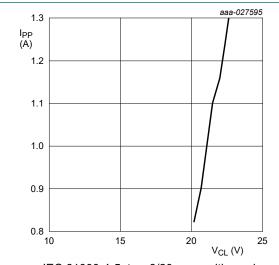
 $t_p$  = 100 ns; Transmission Line Pulse (TLP)

Fig. 3. Dynamic resistance with positive clamping; typical values



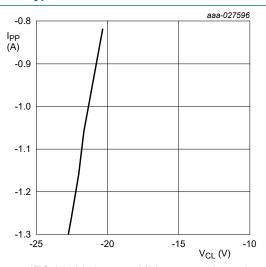
t<sub>p</sub> = 100 ns; Transmission Line Pulse (TLP)

Fig. 4. Dynamic resistance with negative clamping; typical values



IEC 61000-4-5;  $t_p$  = 8/20  $\mu$ s; positive pulse

Fig. 5. Dynamic resistance with positive clamping; typical values



IEC 61000-4-5;  $t_p$  = 8/20  $\mu$ s; negative pulse

Fig. 6. Dynamic resistance with negative clamping; typical values

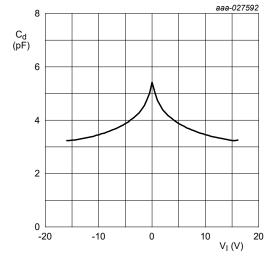


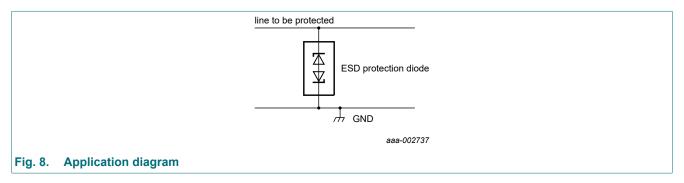
Fig. 7. Diode capacitance as a function of input voltage; typical values

**Product data sheet** 

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## 10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- **3.** Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

# 11. Package outline

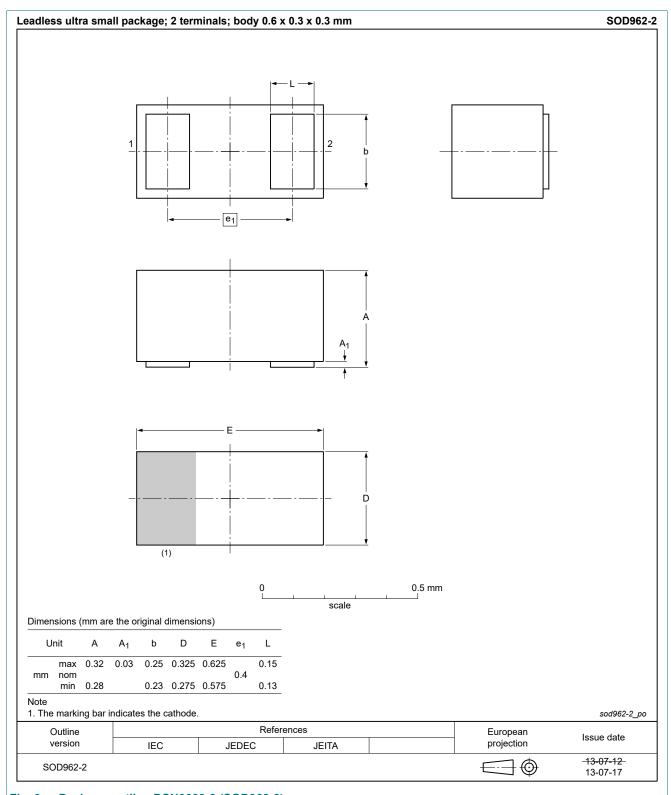


Fig. 9. Package outline DSN0603-2 (SOD962-2)

# 12. Soldering

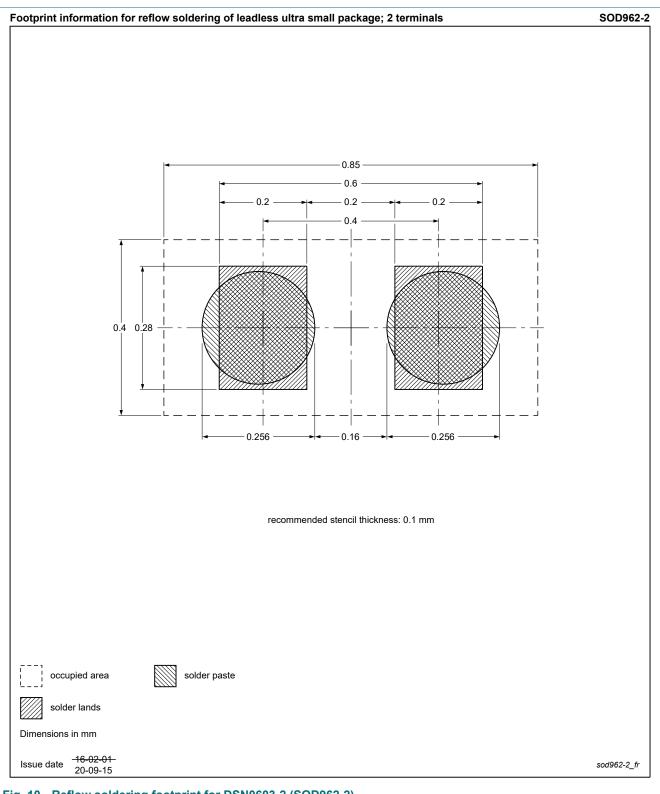


Fig. 10. Reflow soldering footprint for DSN0603-2 (SOD962-2)

# 13. Revision history

## Table 7. Revision history

Table 7. Nevision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PESD16VV1BSF v.4	20210303	Product data sheet	-	PESD16VV1BSF v.3			
Modifications:	Figure "Reflow solde	ring footprint" updated					
PESD16VV1BSF v.3	20180705	Product data sheet	-	PESD16VV1BSF v.2			
PESD16VV1BSF v.2	20180420	Product data sheet	-	PESD16VV1BSF v.1			
PESD16VV1BSF v.1	20180404	Product data sheet	-	-			

## 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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