

**BC807QC-Q** series

45 V, 500 mA PNP general-purpose transistors Rev. 2 — 4 May 2021

**Product data sheet** 

### 1. General description

PNP general-purpose transistor in an ultra small DFN1412D-3 (SOT8009) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

Table 1. Product overview								
Type number	Type number Package							
	Name	JEDEC	Version					
BC807-16QC-0	Q DFN1412D-3	MO-340CA	SOT8009	BC817-16QC-Q				
BC807-25QC-0	Q			BC817-25QC-Q				
BC807-40QC-0	Q			BC817-40QC-Q				

### 2. Features and benefits

- High power dissipation capability
- High current
- Three current gain selections
- Suitable for Automatic Optical Inspection (AOI) of solder joint •
- Smaller footprint compared to conventional leaded SMD packages
- Low package height of 0.5 mm
- Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

- General-purpose switching and amplification
- Space restricted applications

### 4. Quick reference data

Table 2. Qu	ick reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base; T <sub>amb</sub> = 25 °C		-	-	-45	V
I <sub>C</sub>	collector current	T <sub>amb</sub> = 25 °C		-	-	-500	mA
I <sub>CM</sub>	peak collector current	single pulse; $t_p \le 1 \text{ ms}$ ; $T_{amb} = 25 \text{ °C}$		-	-	-1	А
h <sub>FE</sub>	DC current gain						
	BC807-16QC-Q	$V_{CE}$ = -1 V; I <sub>C</sub> = -100 mA T <sub>amb</sub> = 25 °C	[1]	100	-	250	
	BC807-25QC-Q		[1]	160	-	400	
	BC807-40QC-Q	7	[1]	250	-	600	

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

# nexperia

# 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		C
2	E	emitter		в
3	С	collector	3	E sym132
			Bottom view	
			DFN1412D-3 (SOT8009)	

### 6. Ordering information

### Table 4. Ordering information

Type number	Package	ackage						
	Name	Description	Version					
BC807-16QC-Q		DFN1412D-3: plastic thermal enhanced ultra thin small outline	SOT8009					
BC807-25QC-Q		package; no leads; 3 terminals; body: 1.4 x 1.2 x 0.5 mm	(MO-340CA)					
BC807-40QC-Q								

# 7. Marking

Table 5. Marking	
Type number	Marking code
BC807-16QC-Q	9J
BC807-25QC-Q	9К
BC807-40QC-Q	9L

### 8. Limiting values

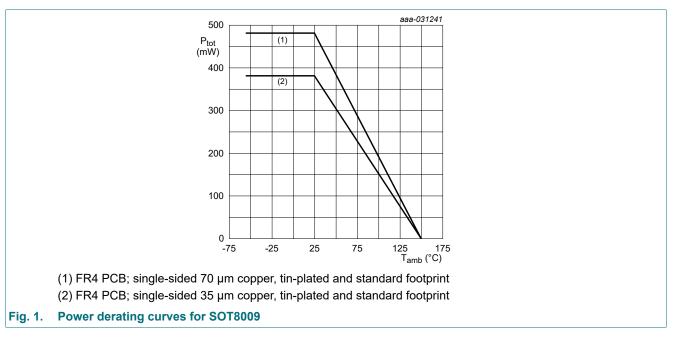
#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Conditions		Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter; T <sub>amb</sub> = 25 °C	open emitter; T <sub>amb</sub> = 25 °C		-50	V
V <sub>CEO</sub>	collector-emitter voltage	open base; T <sub>amb</sub> = 25 °C		-	-45	V
V <sub>EBO</sub>	emitter-base voltage	open collector; T <sub>amb</sub> = 25 °C	open collector; T <sub>amb</sub> = 25 °C		-5	V
l <sub>C</sub>	collector current	T <sub>amb</sub> = 25 °C	$T_{amb} = 25 \text{ °C}$ ·		-500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms; T <sub>amb</sub>	single pulse; t <sub>p</sub> ≤ 1 ms; T <sub>amb</sub> = 25 °C		-1	А
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms; T <sub>amb</sub>	single pulse; t <sub>p</sub> ≤ 1 ms; T <sub>amb</sub> = 25 °C		-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	380	mW
			[2]	-	480	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided 35  $\mu m$  copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided 70 µm copper, tin-plated and standard footprint.



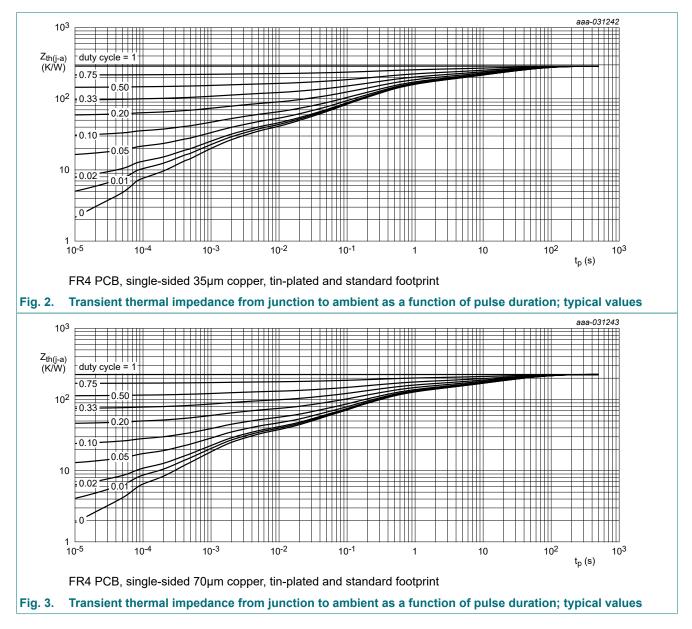
### 9. Thermal characteristics

#### **Table 7. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air;	[1]	-	-	329	K/W
		T <sub>amb</sub> = 25 °C	[2]	-	-	261	K/W

 $\label{eq:compared} \begin{tabular}{ll} [1] & Device mounted on an FR4 PCB, single-sided 35 \mbox{ } \mu m \ copper, tin-plated and standard footprint. \end{tabular}$ 

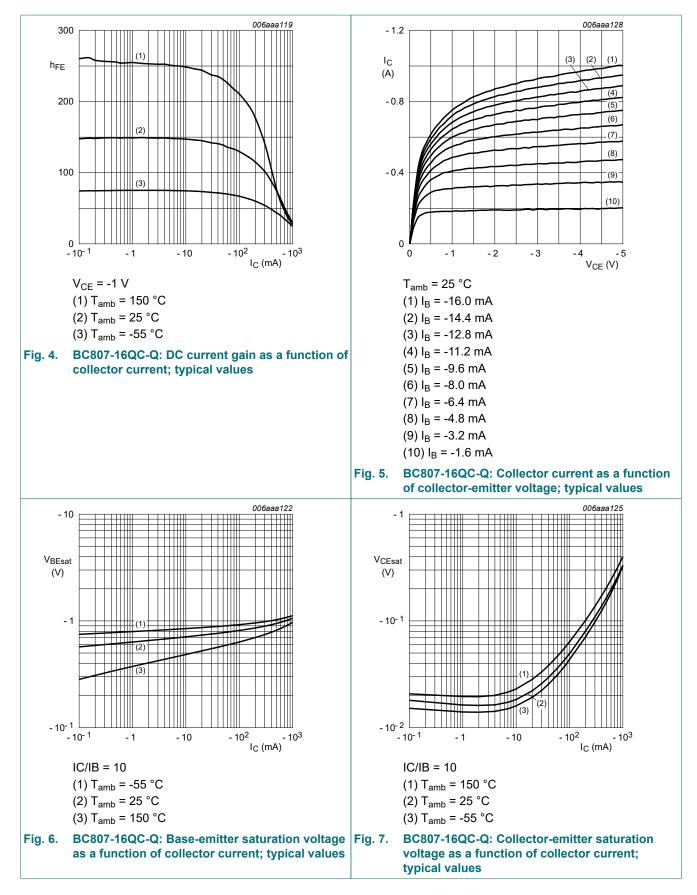
[2] Device mounted on an FR4 PCB, single-sided 70 µm copper, tin-plated and standard footprint.



# **10. Characteristics**

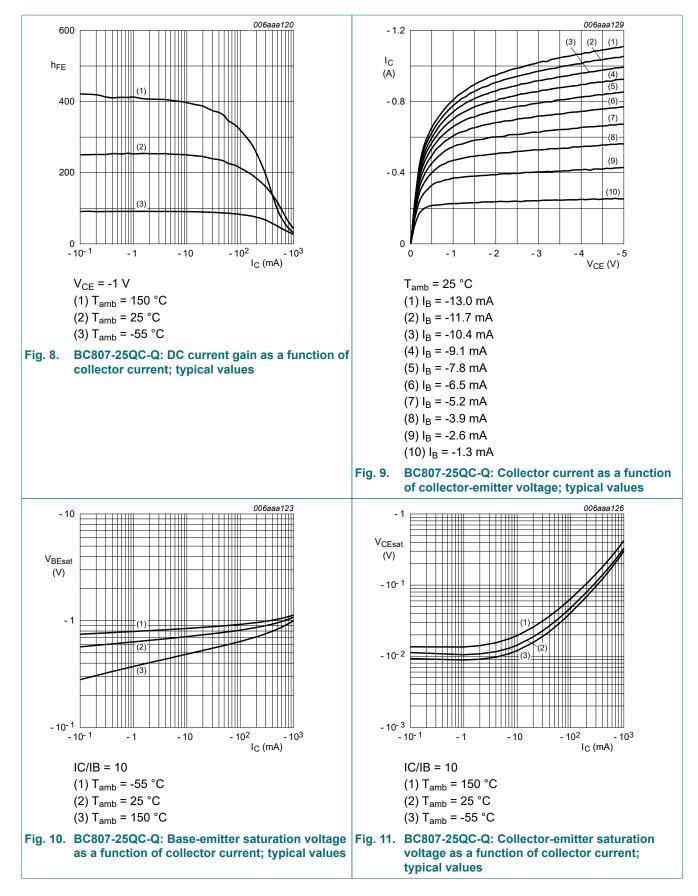
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	$I_{C}$ = -100 µA; $I_{E}$ = 0 A; $T_{amb}$ = 25 °C		-50	-		V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = -10 mA; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-45	-		V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	$I_E$ = -100 µA; $I_C$ = 0 A; $T_{amb}$ = 25 °C		-5	-		V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-100	nA
	cut-off current	V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-100	nA
h <sub>FE</sub> DC current gain							
BC80	BC807-16QC-Q	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA; T <sub>amb</sub> = 25 °C	[1]	100	-	250	
	BC807-25QC-Q	_	[1]	160	-	400	
	BC807-40QC-Q	_	[1]	250	-	600	
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -500 mA; T <sub>amb</sub> = 25 °C	[1]	40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -500 mA; I <sub>B</sub> = -50 mA; T <sub>amb</sub> = 25 °C	[1]	-	-	-700	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -500 mA; T <sub>amb</sub> = 25 °C	[1] [2]	-	-	-1.2	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C		80	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	5	-	pF

### 45 V, 500 mA PNP general-purpose transistors

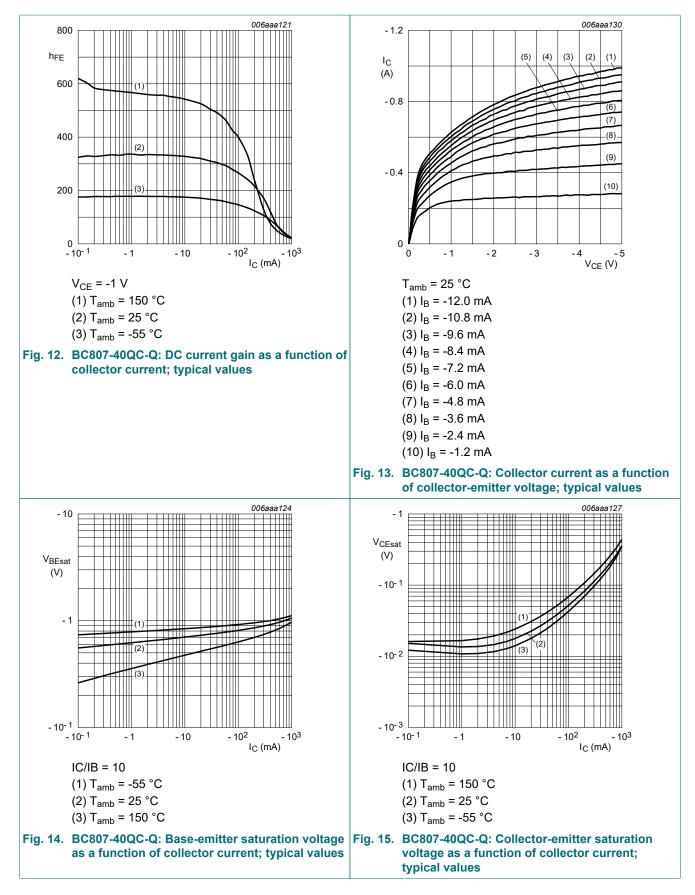


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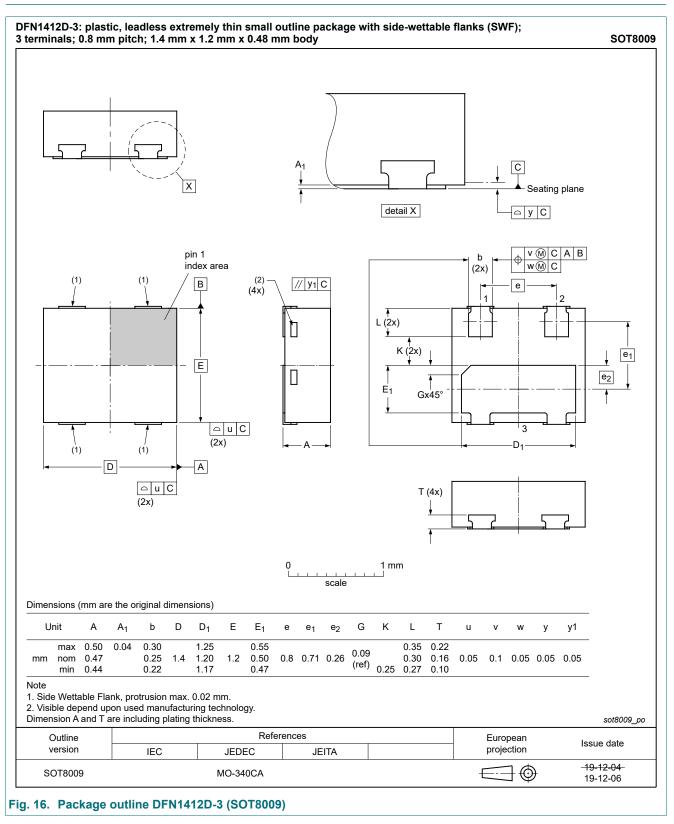
### **11. Test information**

### 11.1. Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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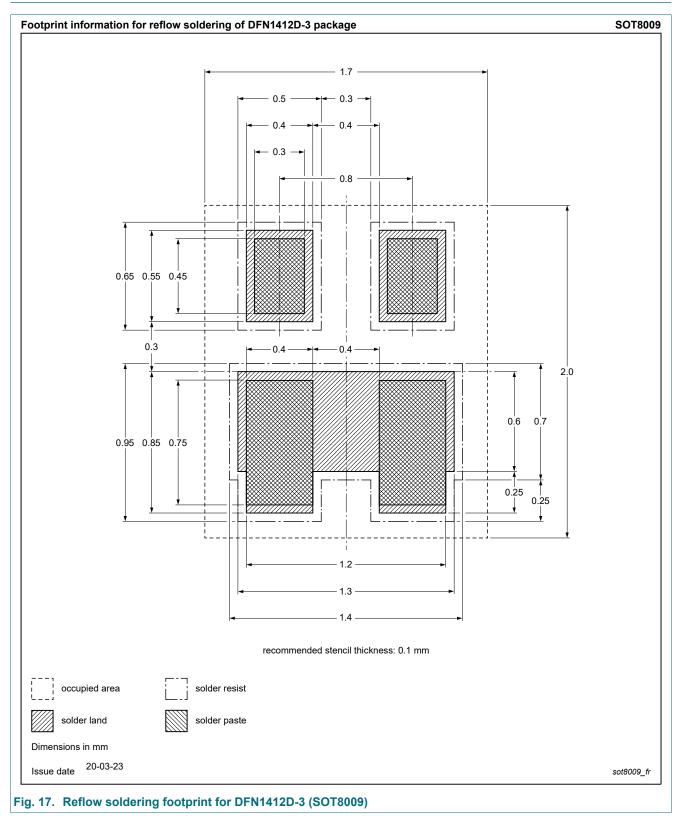
# 12. Package outline



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#### 45 V, 500 mA PNP general-purpose transistors

# 13. Soldering



# 14. Revision history

Table 9. Revision history						
Document ID	Release date	Data sheet status	Change notice	Supersedes		
BC807QC-Q_SER v.2	20210504	Product data sheet	-	BC807QC-Q_SER v.1		
Modifications:	Features and benefits: added recommendation for automotive applications					
BC807QC-Q_SER v.1	20210216	Product data sheet	-	-		

BC807QC-Q series

### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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