Product data sheet

1. General description

PNP switching transistor in a medium power flat lead SOT89 (SC-62/TO-243) Surface-Mounted Device (SMD) plastic package.

NPN complement: PXT2222A

2. Features and benefits

High current: max. 600 mA Low voltage: max. 60 V AEC-Q101 qualified

3. Applications

Switching and linear amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-60	V
I _C	collector current		-	-	-600	mA
h _{FE}	DC current gain	V_{CE} = -1 V; I_{C} = -10 mA; T_{amb} = 25 °C	100	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic cymbol
PIII	Syllibol	Description	Simplified outline	Graphic symbol
1	E	emitter		C
2	С	collector		В
3	В	base	3 2 1	E
			SOT89	sym132



60 V, 600 mA, PNP switching transistor

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PXT2907A	SOT89	plastic surface-mounted package; die pad for good heat transfer; 3 leads	SOT89		

7. Marking

Table 4. Marking codes

Type number	Marking code
	បា
PXT2907A	%2F

[1] % = placeholder for manufacturing site code

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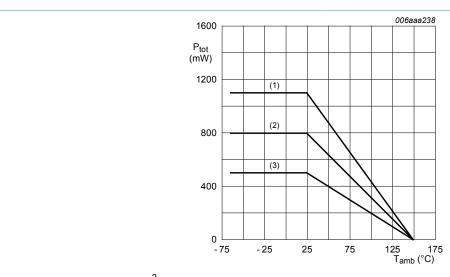
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-60	V
V_{CEO}	collector-emitter voltage	open base		-	-60	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-800	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.5	W
			[2]	-	0.8	W
			[3]	-	1.1	W
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².



- (1) FR4 PCB; 6 cm² mounting pad for collector.
- (2) FR4 PCB; 1 cm² mounting pad for collector.
- (3) FR4 PCB; standard footprint.

Fig. 1. Power derating curves

PXT2907A

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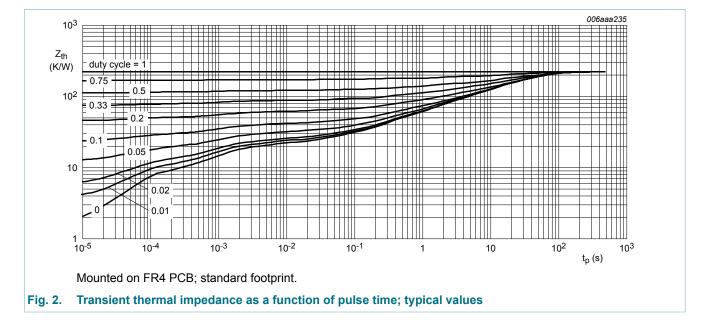
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9. Thermal characteristics

Table 6. Thermal characteristics

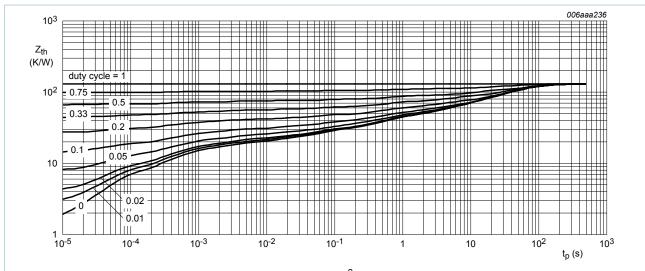
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ung a)	thermal resistance		[1]	-	-	250	K/W
	from junction to		[2]	-	-	156	K/W
	ambient		<u>[3]</u>	-	-	113	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	30	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm².
- Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².



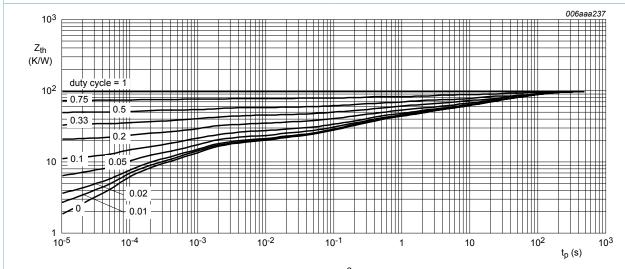
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Mounted on FR4 PCB; mounting pad for collector 1 cm².

Fig. 3. Transient thermal impedance as a function of pulse time; typical values



Mounted on FR4 PCB; mounting pad for collector 6 cm².

Fig. 4. Transient thermal impedance as a function of pulse time; typical values

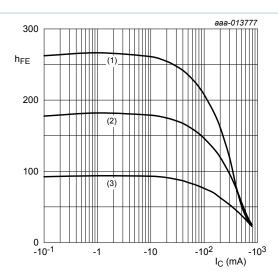
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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -50 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-10	nA
	current	V _{CB} = -50 V; I _E = 0 A; T _j = 125 °C	-	-	-10	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-50	nA
h _{FE}	DC current gain	$V_{CE} = -1 \text{ V; } I_{C} = -0.1 \text{ mA; } T_{amb} = 25 \text{ °C}$	75	-	-	
		V_{CE} = -1 V; I_{C} = -1 mA; T_{amb} = 25 °C	100	-	-	
		V_{CE} = -1 V; I_{C} = -10 mA; T_{amb} = 25 °C	100	-	-	
		V_{CE} = -2 V; I_{C} = -150 mA; T_{amb} = 25 °C	100	-	300	
		$V_{CE} = -10 \text{ V; } I_{C} = -500 \text{ mA;}$ $T_{amb} = 25 ^{\circ}\text{C}$	50	-	-	
02001	collector-emitter saturation voltage	I_{C} = -150 mA; I_{B} = -15 mA; T_{amb} = 25 °C	-	-	-400	mV
		I_{C} = -500 mA; I_{B} = -50 mA; T_{amb} = 25 °C	-	-	-1.6	V
V _{BEsat}	base-emitter saturation voltage	I _C = -150 mA; I _B = -15 mA; T _{amb} = 25 °C	-	-	-1.3	V
		I _C = -500 mA; I _B = -50 mA; T _{amb} = 25 °C	-	-	-2.6	V
t _d	delay time	I _C = -150 mA; I _{Bon} = -15 mA;	-	-	12	ns
t _r	rise time	I _{Boff} = 15 mA; T _{amb} = 25 °C	-	-	30	ns
t _{on}	turn-on time		-	-	40	ns
ts	storage time		-	-	300	ns
t _f	fall time		-	-	65	ns
t _{off}	turn-off time		-	-	365	ns
C _C	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	-	-	8	pF
C _E	emitter capacitance	V_{EB} = -500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	35	pF
f _T	transition frequency	V_{CE} = -20 V; I_{C} = -50 mA; f = 100 MHz; T_{amb} = 25 °C	200	-	-	MHz

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$$V_{CE} = -2 V$$

(1)
$$T_{amb}$$
 = 100 °C

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

$$(3) T_{amb} = -55 °C$$

Fig. 5. DC current gain as a function of collector current; typical values

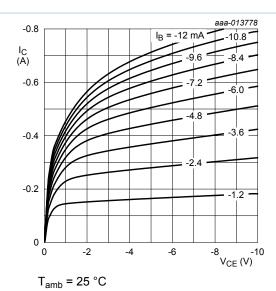
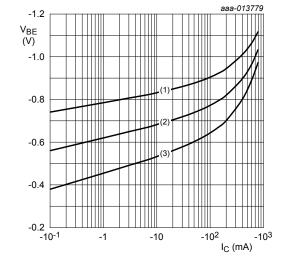


Fig. 6. Collector current as a function of collectoremitter voltage; typical values



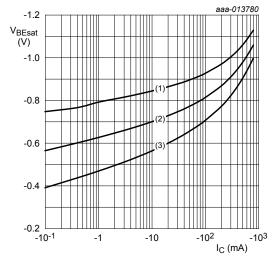
$$V_{CE} = -2 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

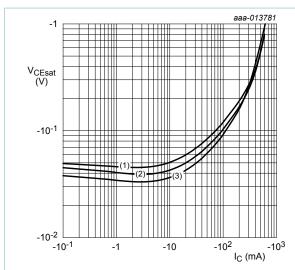
(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

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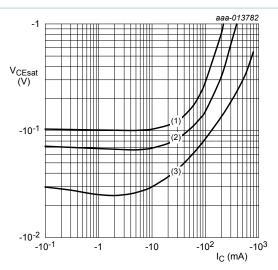
$$I_{\rm C}/I_{\rm B}=20$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55$$
 °C

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values



$$T_{amb}$$
 = 25 °C

(1)
$$I_C/I_B = 100$$

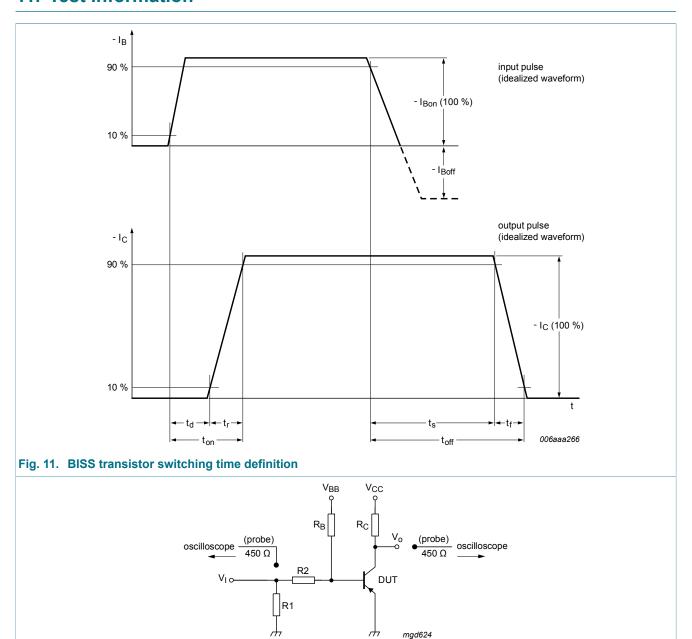
(2)
$$I_C/I_B = 50$$

(3)
$$I_C/I_B = 10$$

Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values

60 V, 600 mA, PNP switching transistor

11. Test information



11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

Fig. 12. Test circuit for switching times

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12. Package outline

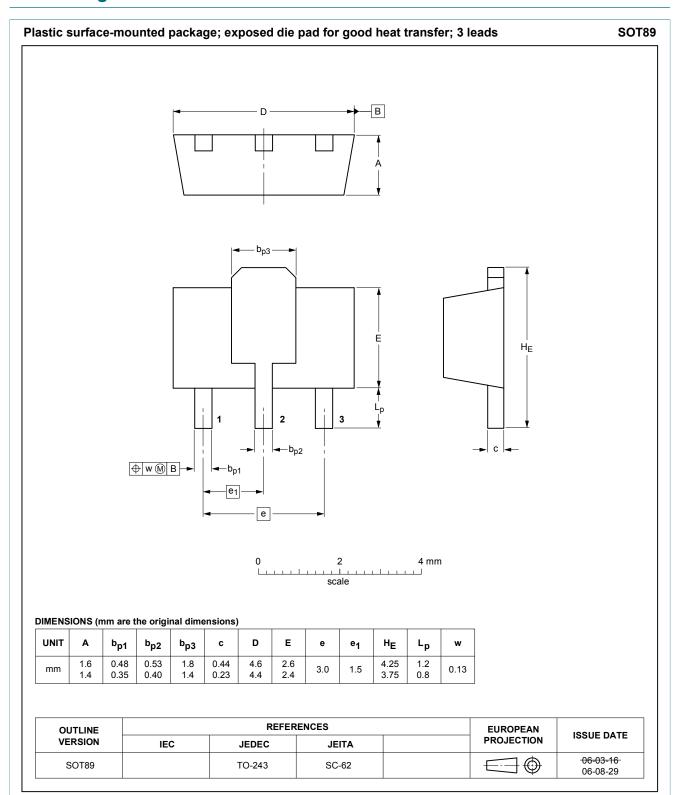


Fig. 13. Package outline SOT89

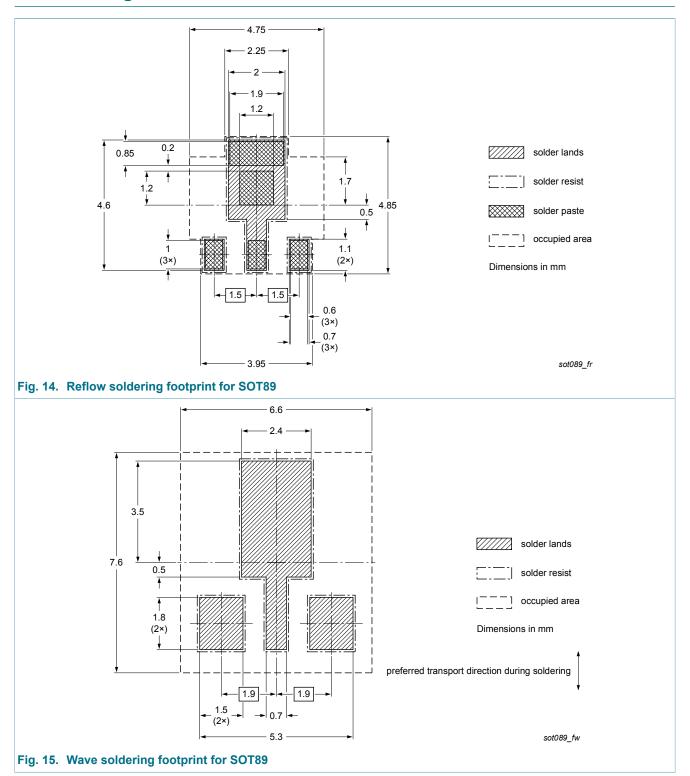
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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PXT2907A v.7	20150803	Product data sheet	-	PXT2907A v.6
Modifications:	 Marking code corre 	cted		
PXT2907A v.6	20141010	Product data sheet	-	PXT2907A v.5
PXT2907A v.5	20041209	Product data sheet	-	PXT2907A v.4
PXT2907A v.4	20020320	Product data sheet	-	-

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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