Product data sheet

1. General description

NPN medium power transistors in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- High collector current capability I_C and I_{CM}
- Two current gain selections
- · High power dissipation capability

3. Applications

- Linear voltage regulators
- MOSFET drivers
- Low-side switches
- Power management
- Amplifiers
- Battery-driven devices

4. Quick reference data

Table 1. Quick reference data

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	20	V
I _C	collector current			-	-	2	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	3	Α
h _{FE}	DC current gain						
	BCP68	V _{CE} = 1 V; I _C = 500 mA	[1]	85	-	375	
	BCP68-25		[1]	160	-	375	

[1] pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02$



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5. Pinning information

Table 2. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	C; C
2	С	collector		в
3	Е	emitter		
4	С	collector	□ 1 □ 2 □ 3	Ė
				sym016

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BCP68	SC-73	plastic, surface-mounted package with increased heatsink; 4	SOT223
BCP68-25		leads	

7. Marking

Table 4. Marking

Type number	Marking code
BCP68	BCP68
BCP68-25	BCP68/25

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8. Limiting values

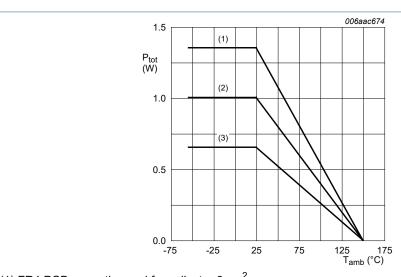
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	32	V
V _{CEO}	collector-emitter voltage	open base		-	20	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	2	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	3	Α
l _B	base current			-	0.4	Α
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	0.4	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.00	W
			[3]	-	1.35	W
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm². Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm².



- (1) FR4 PCB, mounting pad for collector 6 cm²
- (2) FR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

Fig. 1. Power derating curves

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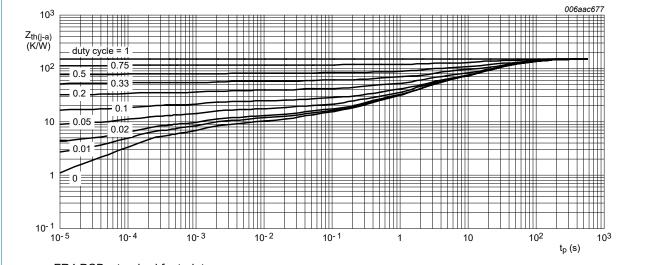
9. Thermal characteristics

Table 6. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

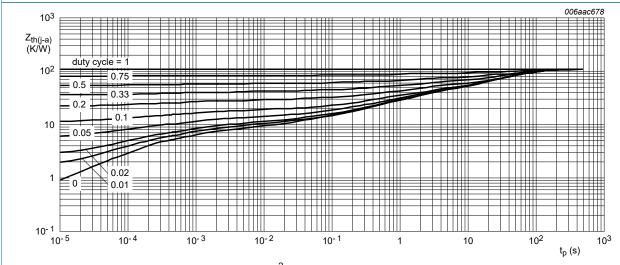
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	192	K/W
			[2]			125	K/W
			[3]			93	K/W
R _(j-sp)	thermal resistance from junction to solder point			-	-	16	K/W

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm².



FR4 PCB, standard footprint

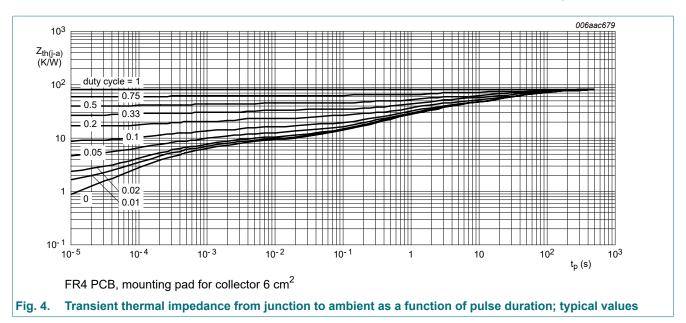
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for collector 1 cm²

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

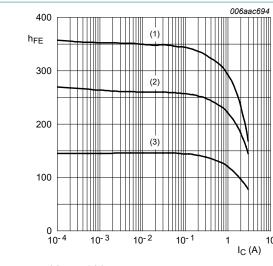
Table 7. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A		32	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 30 mA; I _B = 0 A		20	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = 100 μA; I _C = 0 A		5	-	-	V
I _{CBO}	collector-base	V _{CB} = 25 V; I _E = 0 A		-	-	100	nA
	cut-off current	V _{CB} = 25 V; I _E = 0 A; T _j = 150 °C		-	-	10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A		-	-	100	nA
h _{FE}	DC current gain		,	'	'	'	
	BCP68	V _{CE} = 10 V; I _C = 5 mA	[1]	50	-	-	
		V _{CE} = 1 V; I _C = 500 mA	[1]	85	-	375	
		V _{CE} = 1 V; I _C = 1 A	[1]	60	-	-	
		V _{CE} = 1 V; I _C = 2 A	[1]	40	-	-	
	BCP68-25	V _{CE} = 10 V; I _C = 5 mA	[1]	50	-	-	
		V _{CE} =1 V; I _C = 500 mA	[1]	160	-	375	
		V _{CE} = 1 V; I _C = 1 A	[1]	60	-	-	
		V _{CE} = 1 V; I _C = 2 A	[1]	40	-	-	
V _{CEsat}	collector-emitter	I _C = 1 A; I _B = 100 mA	[1]	-	-	0.5	V
	saturation voltage	I _C = 2 A; I _B = 200 mA	[1]	-	-	0.6	V
V_{BE}	base-emitter voltage	V _{CE} = 10 V; I _C = 5 mA	[1]	-	-	0.7	V
		V _{CE} = 1 V; I _C = 1 A	[1]	-	-	1	V
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz		-	22	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 50 mA; f = 100 MHz		40	170	-	MHz

^[1] pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02$

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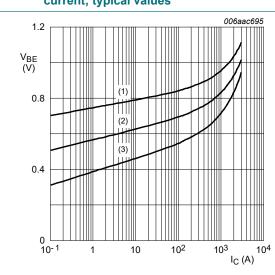
$$V_{CE} = 1 V$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55$$
 °C

Fig. 5. DC current gain as a function of collector current; typical values



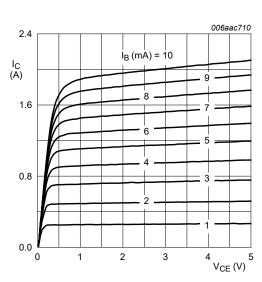
$$V_{CE} = 1 V$$

(1)
$$T_{amb} = -55$$
 °C

(2)
$$T_{amb}$$
 = 25 °C

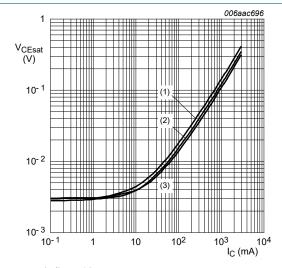
(3)
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



 T_{amb} = 25 °C

Fig. 6. Collector current as a function of collectoremitter voltage; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

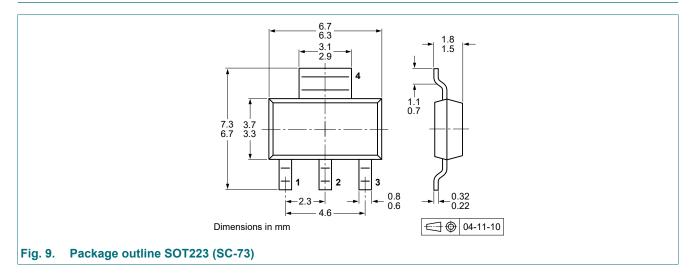
(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

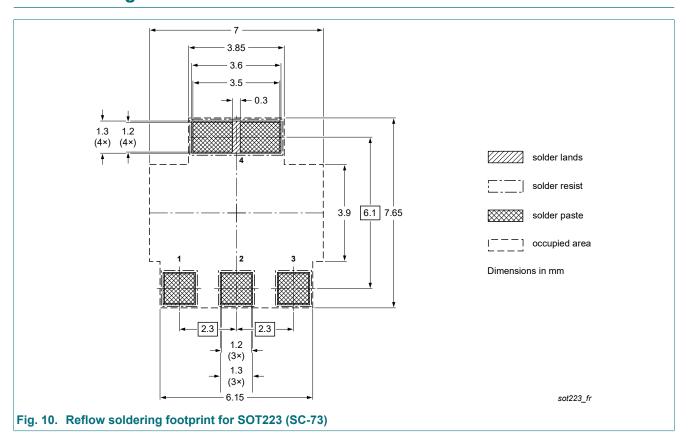
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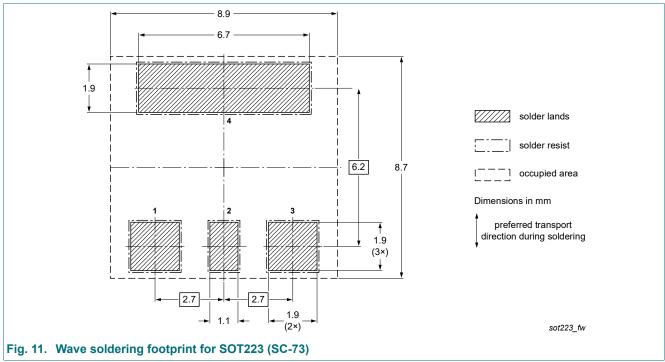
11. Package outline



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12. Soldering





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13. Revision history

Table 8. Revision history

Table 8. Revision history				T
Document ID	Release date	Data sheet status	Change notice	Supersedes
BCP68_SER v.9	20230701	Product data sheet	-	BCP68_BC868_BC68PA v.8
Modifications:	Section "Packing in Product(s) change	splitted to 3 data sheets. information" removed. ed to non-automotive quaroduct alternative(s).		ase refer to nexperia.com for
BCP68_BC868_BC68PA v.8	20111018	Product data sheet		BC868 v.7
BC868 v.7	20041108	Product specification	-	BC868 v.6
BC868 v.6	20031202	Product specification	-	BC868 v.5
BC868 v.5	19990408	Product specification	-	BC868 v.4
BC868 v.4	19980716	Product specification	-	BC868_CNV v.3
BC868_CNV v.3	19970319	Product specification	-	BC868_CNV v.2
BC868_CNV v.2	19970307	Product specification	-	
BCP68 v.4	20031125	Product specification	-	BCP68 v.3
BCP68 v.3	19990408	Product specification	-	BCP68_CNV v.2
BCP68_CNV v.2	19970409	Product specification	-	-

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Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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