

PESD5V0L1ULD

Low capacitance unidirectional ESD protection diode

14 April 2023

Product data sheet

1. General description

Low capacitance unidirectional ElectroStatic Discharge (ESD) protection diode designed to protect one signal line from the damage caused by ESD and other transients. The device is housed in a SOD882D leadless ultra small Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

2. Features and benefits

- ESD protection of one line
- Ultra small SMD plastic package
- Solderable side pads
- Package height typ. 0.37 mm
- Low diode capacitance C_d = 25 pF
- ESD protection up to 26 kV
- IEC 61000-4-2; level 4 (ESD)
- IEC 61000-4-5 (surge); I_{PPM} = 3.5 A
- Low clamping voltage: V_{CL} = 12 V

3. Applications

- Computers and peripherals
- Audio and video equipment
- Communication systems
- Portable electronics

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	5	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	25	30	pF

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode[1]		
2	A	anode		K-KA-A
			Transparent top view	006aaa152
			DFN1006D-2 (SOD882D)	

[1] The marking bar indicates the cathode.

6. Ordering information

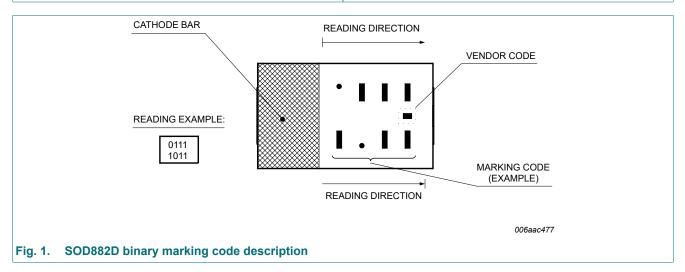
Table 3. Ordering information

Type number	Package			
	Name	Description	Version	
PESD5V0L1ULD		leadless ultra small plastic package with side-wettable flanks (SWF); 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.4 mm body	SOD882D	

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD5V0L1ULD	1110
	0000



8. Limiting values

Table 5. Limiting values

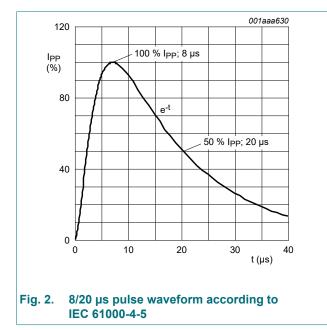
In accordance with the Absolute Maximum Rating System (IEC 60134).

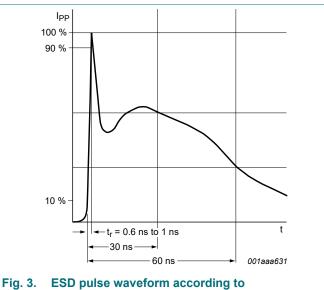
Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1] [2]	-	42	W
I _{PPM}	rated peak pulse current		[1] [2]	-	3.5	А
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximum	ratings		-			
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge), $T_{amb} = 25 \text{ °C}$	[3] [2]	-	26	kV
		IEC 61000-4-2 (air discharge), T _{amb} = 25 °C		-	15	kV
		machine model, T _{amb} = 25 °C		-	400	V
		MIL-STD-883 (human body model), T _{amb} = 25 °C	[2]	-	10	kV

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

[2] Measured from pin 1 to pin 2.

[3] Device stressed with ten non-repetitive ESD pulses.





IEC 61000-4-2

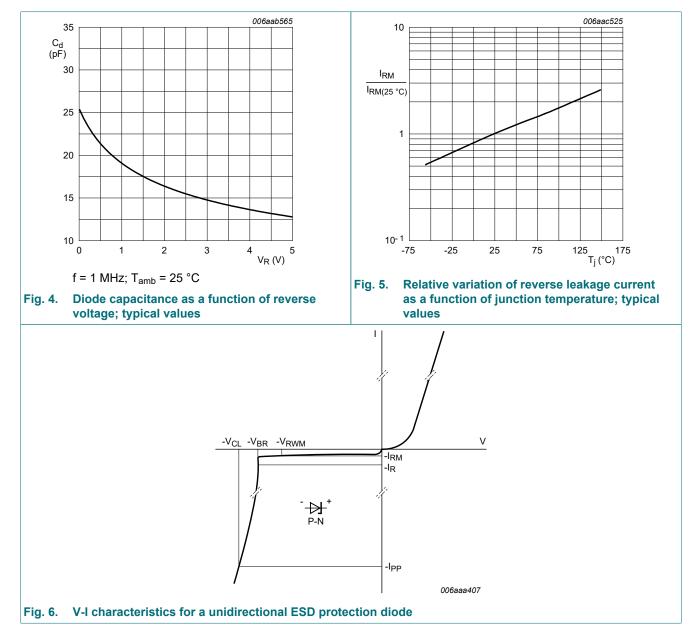
9. Characteristics

Table 6. Cha	racteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	5	V
V _{BR}	breakdown voltage	I _R = 5 mA; T _{amb} = 25 °C		6.4	6.8	7.2	V
I _{RM}	reverse leakage current	V _{RWM} = 5 V; T _{amb} = 25 °C		-	10	100	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	25	30	pF
V _{CL}	clamping voltage	I _{PPM} = 1 A; T _{amb} = 25 °C	[1] [2]	-	-	9	V
		I _{PPM} = 3.5 A; T _{amb} = 25 °C	[1] [2]	-	-	12	V
R _{dyn}	dynamic resistance	I _R = 10 A; T _{amb} = 25 °C	[3]	-	0.7	-	Ω

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

[2] Measured from pin 1 to pin 2.

[3] Non-repetitive current pulse, Transmission Line Pulse (TLP) tp = 100 ns; square pulse; ANSI / ESD STM5.5.1-2008.

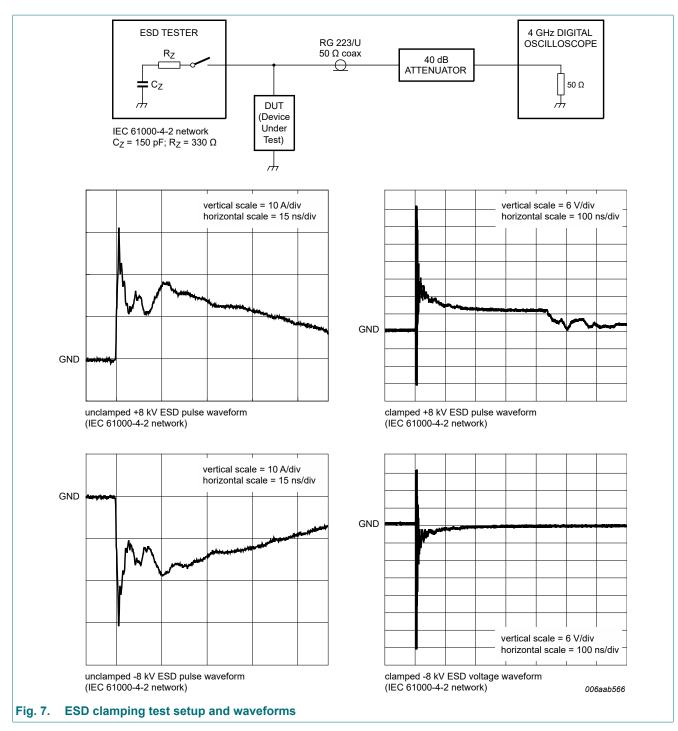


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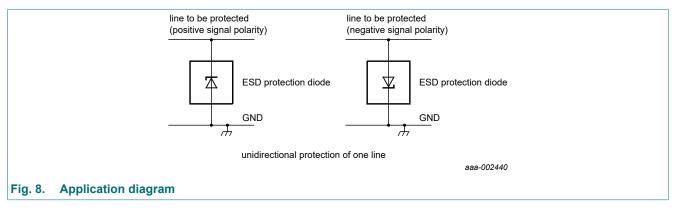
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10. Application information

The device is designed for protection of one unidirectional data or signal line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are either positive or negative with respect to ground.

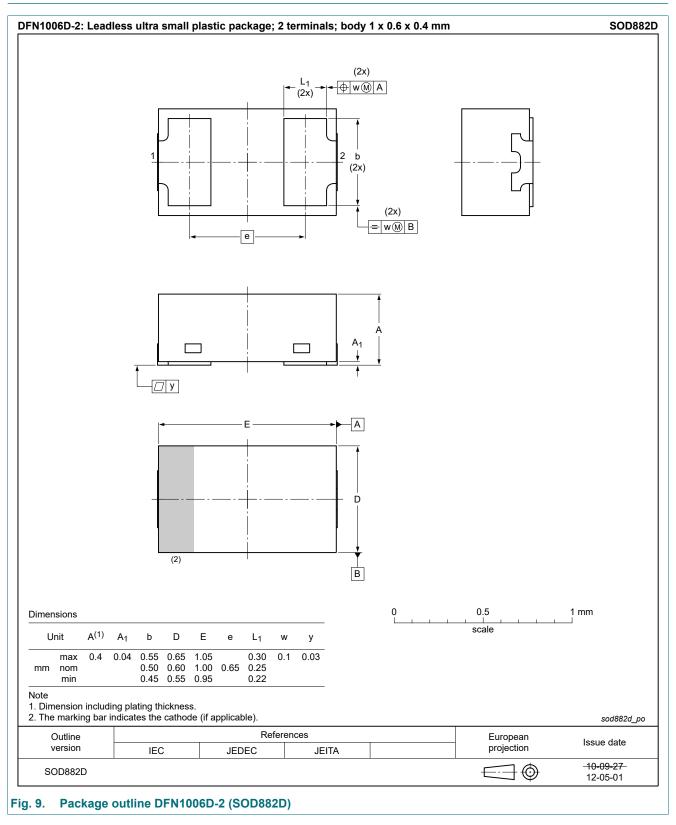


Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- **6.** Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

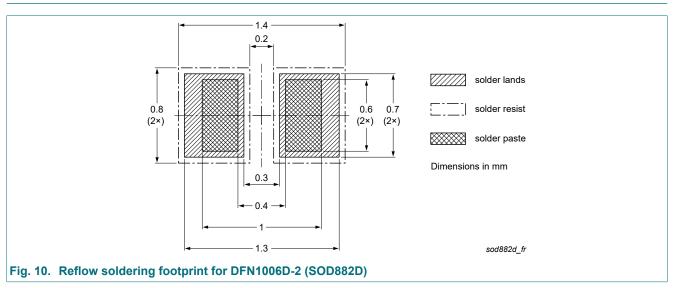
11. Package outline



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12. Soldering



13. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PESD5V0L1ULD v.2	20230414	Product data sheet	-	PESD5V0L1ULD v.1		
Modifications:	 Product changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s). 					
PESD5V0L1ULD v.1	20110419	Product data sheet	-	-		

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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