



PESD15VS2UAT

Double ESD protection diode in SOT23 package

13 April 2023

Product data sheet

1. General description

Unidirectional double ESD protection diode in common cathode configuration in a small SOT23 Surface-Mounted Device (SMD) plastic package, designed to protect up to two data lines against damage from ElectroStatic Discharge (ESD) and other transients.

2. Features and benefits

- Unidirectional ESD protection of up to two lines
- Common-cathode configuration
- Max. peak pulse power: $P_{PPM} = 160 \text{ W}$ at $t_p = 8/20 \mu\text{s}$
- Ultra-low reverse leakage current: $I_{RM} = 1 \text{ nA}$
- ESD protection: 30 kV
- IEC 61000-4-2; level 4 (ESD)
- IEC 61000-4-5 (surge); $I_{PPM} = 5 \text{ A}$ at $t_p = 8/20 \mu\text{s}$

3. Applications

- Computers and peripherals
- Communication systems
- Audio and video equipment
- Data lines

4. Quick reference data

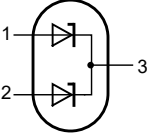
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	$T_j = 25^\circ\text{C}$	[1]	-	-	15	V
C_d	diode capacitance	$f = 1 \text{ MHz}$; $V_R = 0 \text{ V}$; $T_j = 25^\circ\text{C}$	[1]	-	32	70	pF

[1] Measured across either pins 1 and 3 or pins 2 and 3.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode (diode 1)	 SOT23	 sym002
2	A2	anode (diode 2)		
3	CC	common cathode		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD15VS2UAT	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PESD15VS2UAT	%7D

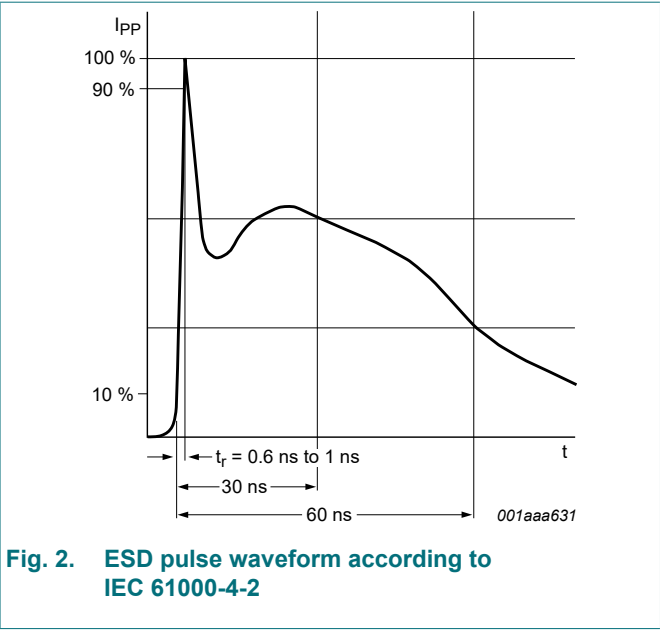
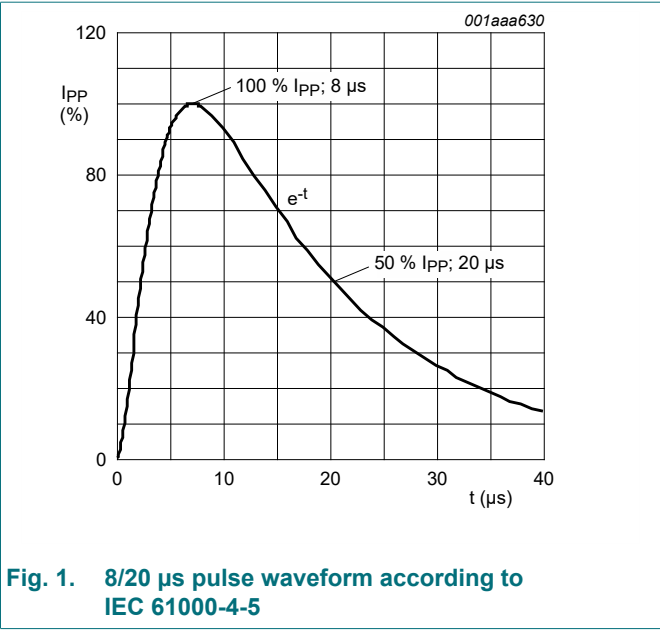
[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1] [2]	-	160	W
I _{PPM}	rated peak pulse current		[1] [2]	-	5	A
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximum ratings						
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[3] [2]	-	30	kV
		IEC 61000-4-2; air discharge		-	15	kV
		MIL-STD-883; human body model (HBM)	[3] [2]	-	10	kV

- [1] Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5.
[2] Measured across either pins 1 and 3 or pins 2 and 3.
[3] Device stressed with ten non-repetitive ESD pulses.

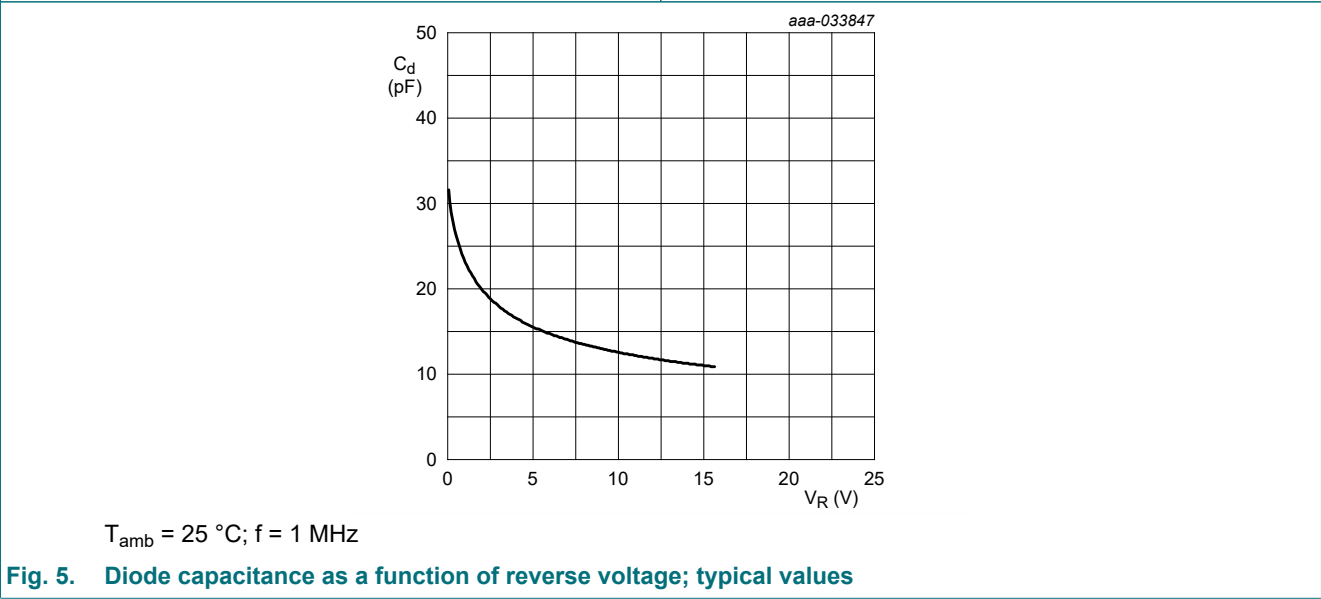
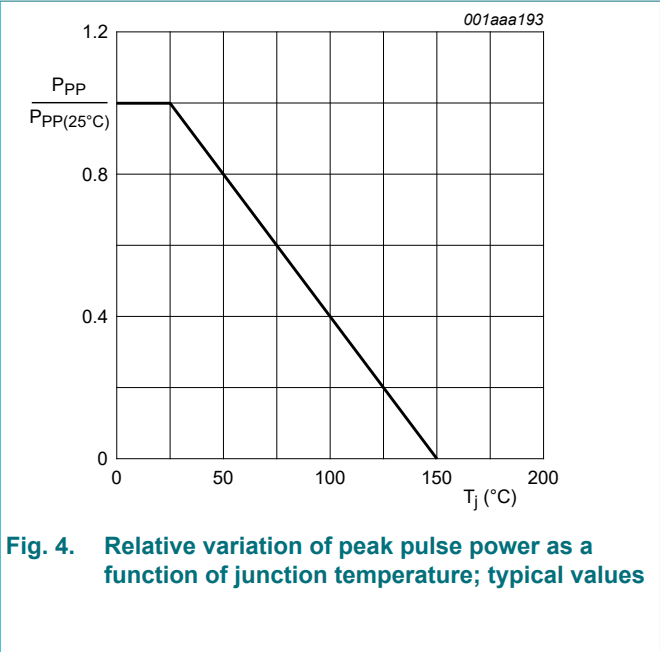
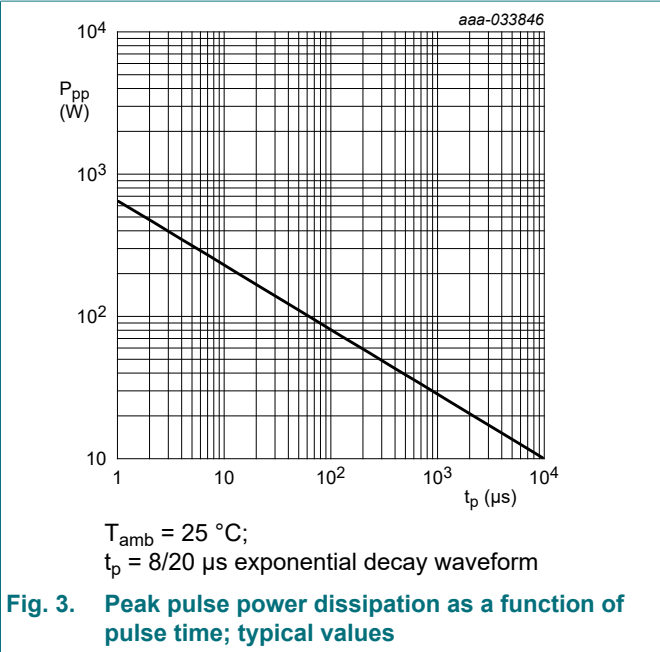


9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{RWM}	reverse standoff voltage	T _j = 25 °C	[1]	-	-	15	V
V _{BR}	breakdown voltage	I _R = 5 mA; T _j = 25 °C	[1]	17.6	18	18.4	V
I _{RM}	reverse leakage current	V _{RWM} = 15 V; T _j = 25 °C	[1]	-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _j = 25 °C	[1]	-	32	70	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; T _j = 25 °C	[2] [1]	-	-	23	V
		I _{PPM} = 5 A; T _j = 25 °C	[2] [1]	-	-	40	V
R _{diff}	differential resistance	I _R = 1 mA; T _j = 25 °C	[1]	-	-	225	Ω

- [1] Measured across either pins 1 and 3 or pins 2 and 3.
[2] Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5.



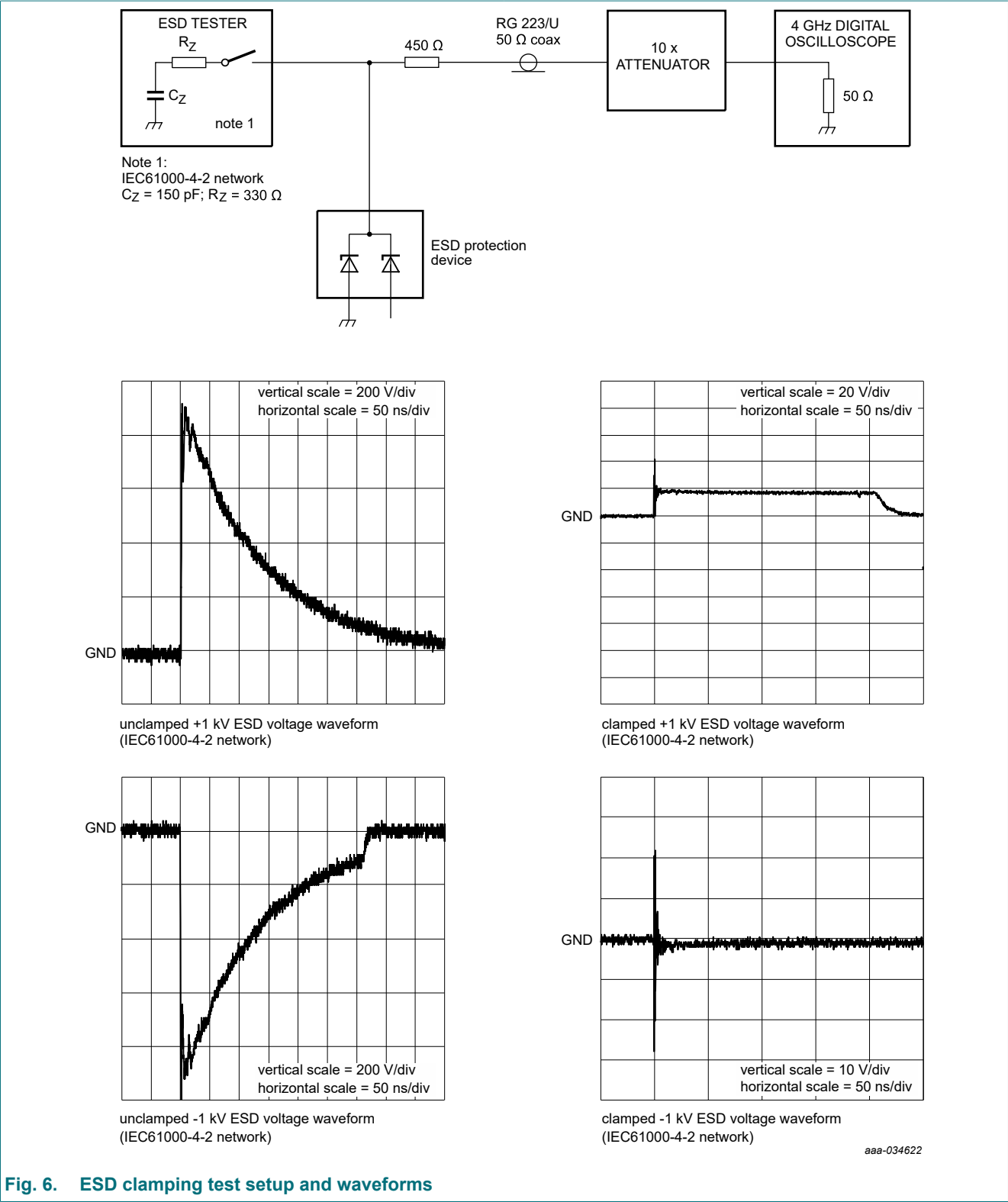


Fig. 6. ESD clamping test setup and waveforms

10. Application information

The device can protect up to two lines against damage caused by unidirectional ElectroStatic Discharge (ESD) and surge pulses. The device can protect lines whose signal polarities are below ground.

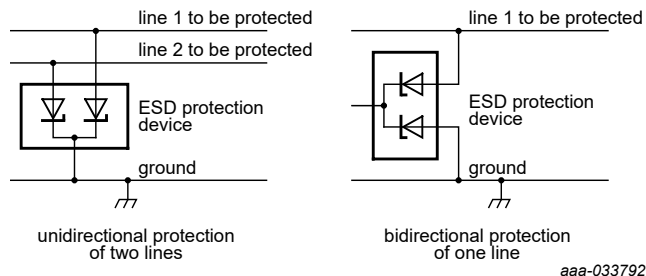


Fig. 7. Typical application: ESD protection of data lines

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Package outline

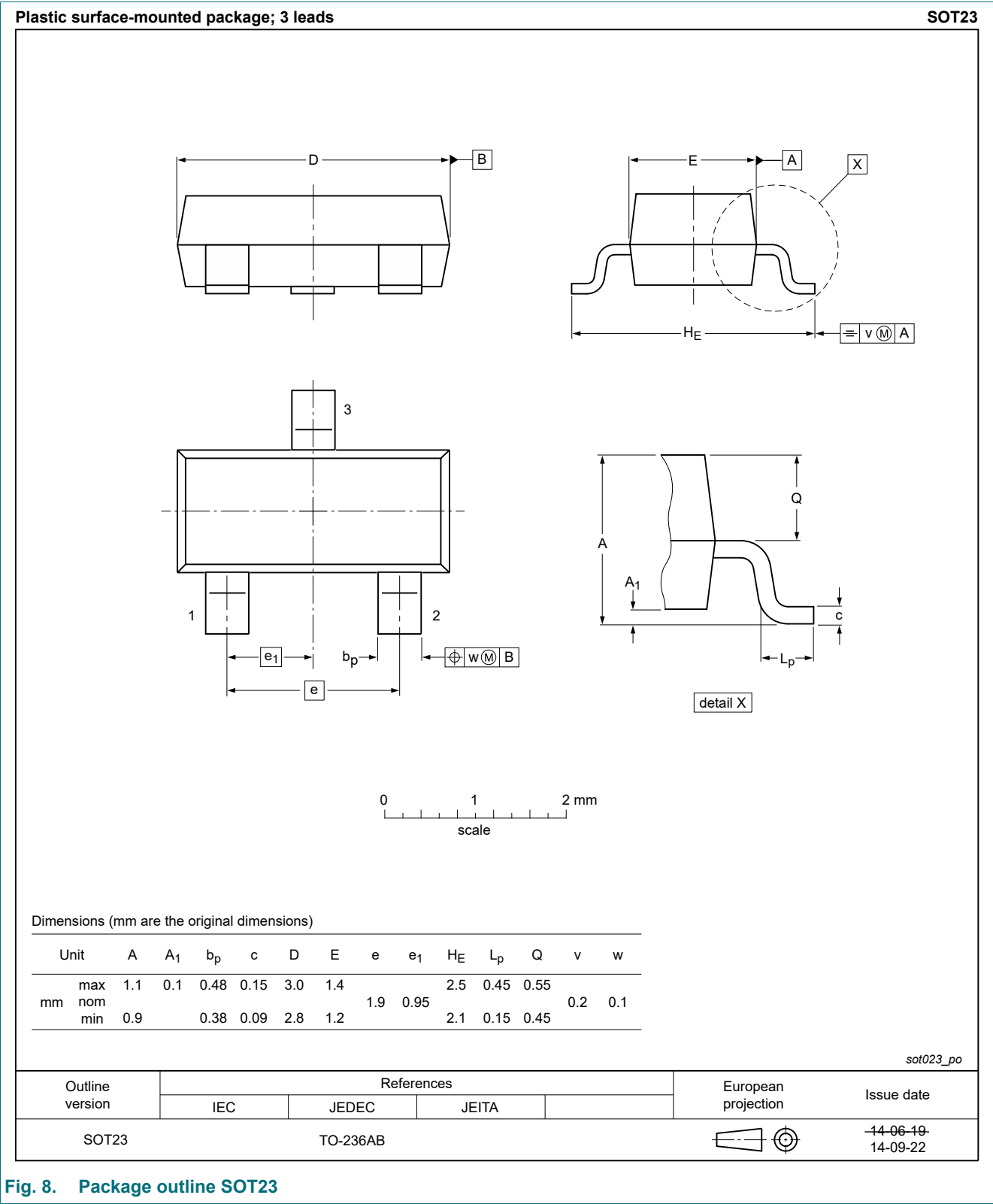
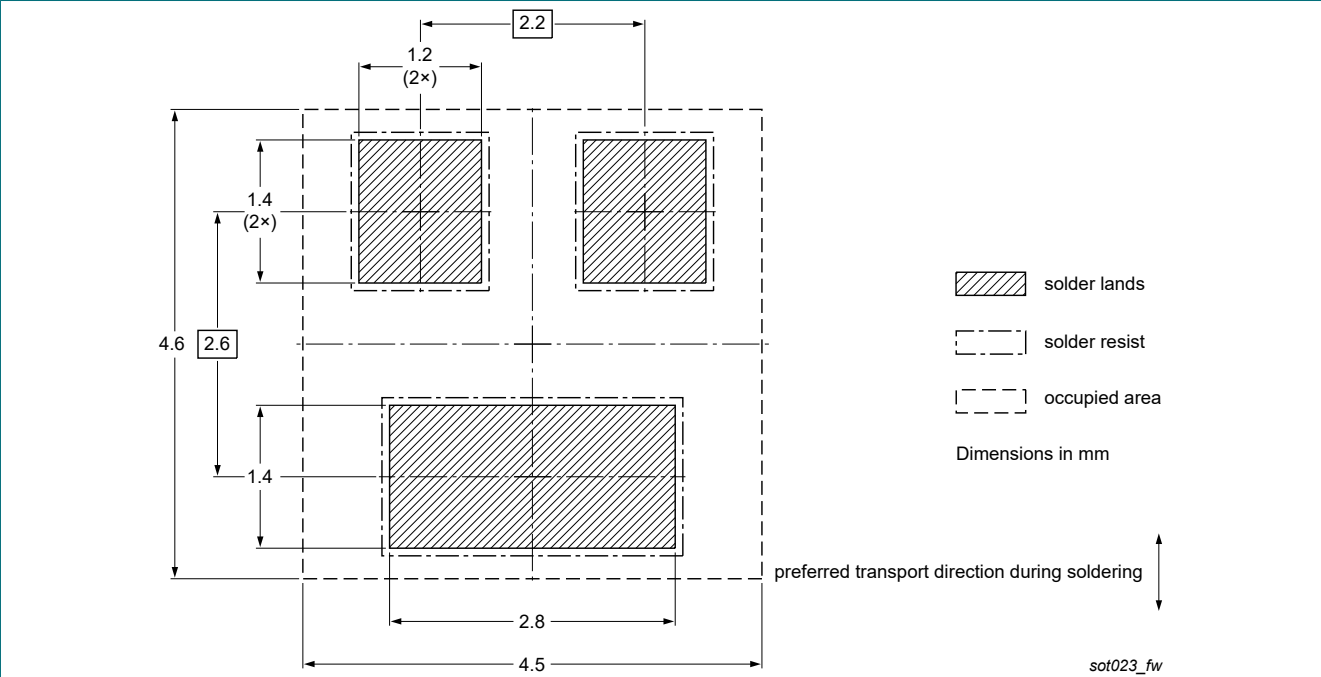
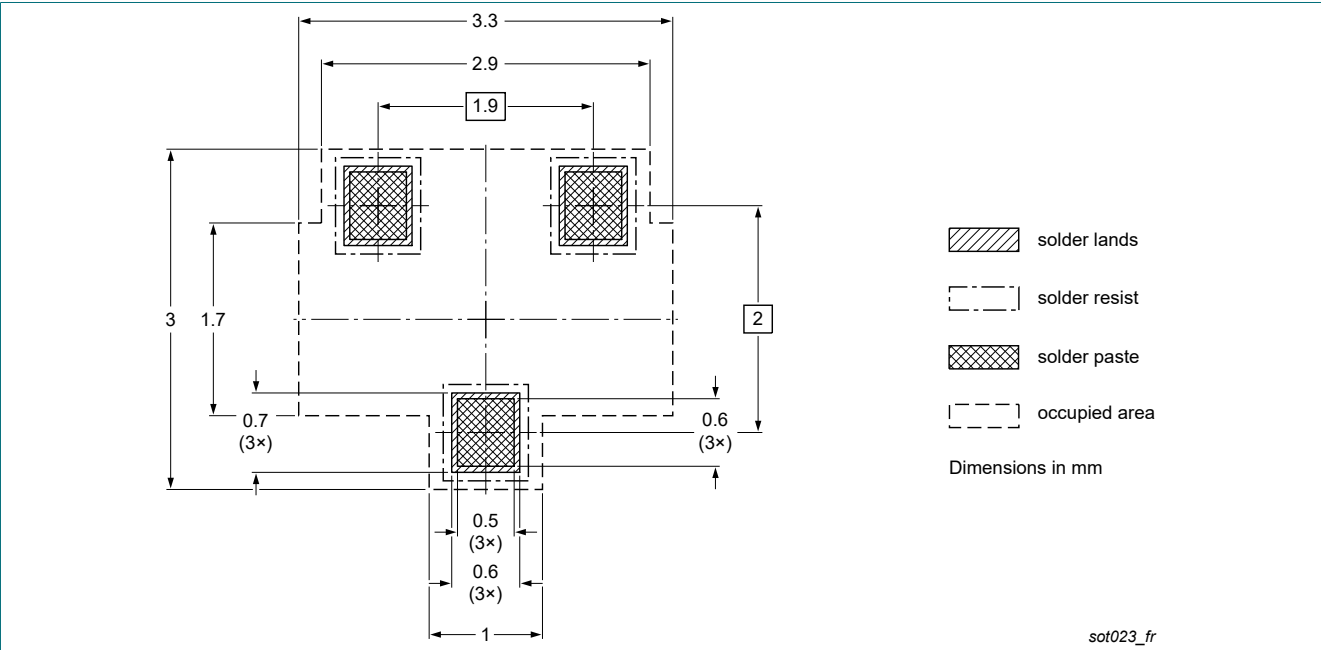


Fig. 8. Package outline SOT23

12. Soldering



13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD15VS2UAT v.2	20230413	Product data sheet	-	PESDxS2UAT series v.1
Modifications:	- The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia - Legal texts have been adapted to the new company name where appropriate - Family data sheet reduced to single type data sheet - Product changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
PESDxS2UAT series v.1	20040218	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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