

# PESD2CANFD27VT-Q

## **ESD** protection for in-vehicle networks

12 April 2023

**Product data sheet** 

## 1. General description

ESD protection device in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package, designed to protect two automotive in-vehicle network bus lines from the damage caused by ElectroStatic discharge (ESD) and other transients.

## 2. Features and benefits

- Reverse stand-off voltage: V<sub>RWM</sub> = 27 V
- Low clamping voltage: V<sub>CL</sub>= 33 V at I<sub>PP</sub> = 1 A
- ESD protection up to 20 kV (IEC 61000-4-2)
- Low capacitance: C<sub>d</sub> = 6 pF
- ESD protection up to 20 kV (ISO 10605; C = 150 pF; R = 330 Ω)
- High temperature capability: T<sub>i</sub> = 175 °C
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

ESD protection for in-vehicle network lines in automotive environments

- CAN-FD
- CAN
- FlexRay
- SENT

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	27	V
I <sub>PPM</sub>	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1] [2]	-	-	2.5	Α
$V_{CL}$	clamping voltage	I <sub>PPM</sub> = 1 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[3] [2]	-	33	44	V

- [1] According to IEC 61000-4-5.
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.



## 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	]3	
2	K2	cathode (diode 2)		к1   Ы Ы
3	cc	common cathode	SOT23	CC K2 006aaa155

## 6. Ordering information

### **Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
PESD2CANFD27VT-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

## 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
PESD2CANFD27VT-Q	%GM

[1] % = placeholder for manufacturing site code

## 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1] [2]	-	2.5	А
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C
ESD maximum	ratings					
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2; contact discharge	[2] [3]	-	20	kV
	voltage	ISO 10605; contact discharge; C = 330 pF, R = 330 $\Omega$	[2] [3]	-	17	kV
		ISO 10605; contact discharge; C = 150 pF, R = 330 $\Omega$	[2] [3]	-	20	kV

- [1] According to IEC 61000-4-5.
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device stressed with ten non-repetitive ESD pulses.

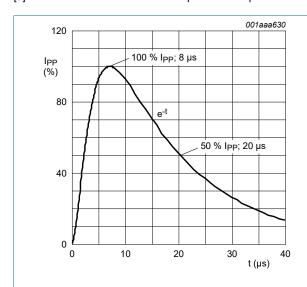


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

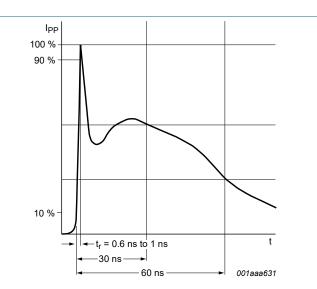


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

## 9. Characteristics

#### **Table 6. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	27	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 10 mA; T <sub>amb</sub> = 25 °C	[1]	28	-	38	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 27 V; T <sub>amb</sub> = 25 °C	[1]	-	1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 2.5 V; T <sub>amb</sub> = 25 °C	[1]	-	5.2	6	pF
		f = 1 MHz; V <sub>R</sub> = -2.5 V; T <sub>amb</sub> = 25 °C	[1]	-	5.2	6	pF
$\Delta C_d/C_d$	diode capacitance	f = 1 MHz; V <sub>R</sub> = 2.5 V; T <sub>amb</sub> = 25 °C	[2]	-	0.5	-	%
	matching	f = 1 MHz; V <sub>R</sub> = -2.5 V; T <sub>amb</sub> = 25 °C	[2]	-	0.5	-	%
V <sub>CL</sub>	clamping voltage	I <sub>PPM</sub> = 1 A; t <sub>p</sub> = 8/20 μs; T <sub>amb</sub> = 25 °C	[3] [1]	-	33	44	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C	[4] [1]	-	0.8	-	Ω

- [1] Measured from pin 1 or 2 to pin 3.
- $\Delta C_d$  is the difference of the capacitance measured between pin 1 and pin 3 and the capacitance measured between pin 2 and pin 3.
- Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5.
- [4] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008

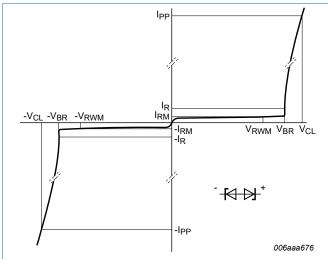
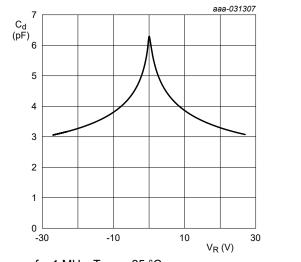
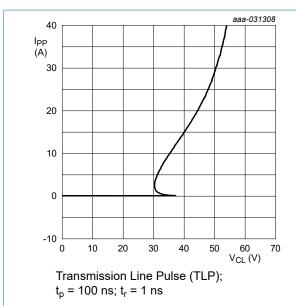


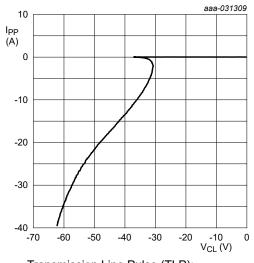
Fig. 3. V-I characteristics for a bidirectional ESD protection diode



 $f = 1 MHz; T_{amb} = 25 °C$ 

Fig. 4. Diode capacitance as a function of reverse voltage; typical values





Transmission Line Pulse (TLP);

 $t_p = 100 \text{ ns}; t_r = 1 \text{ ns}$ 

Fig. 5. Positive clamping voltage (TLP); typical values

Fig. 6. Negative clamping voltage (TLP); typical values

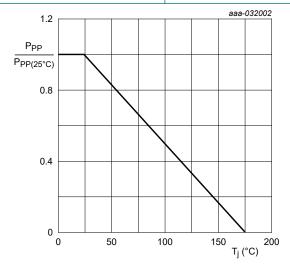
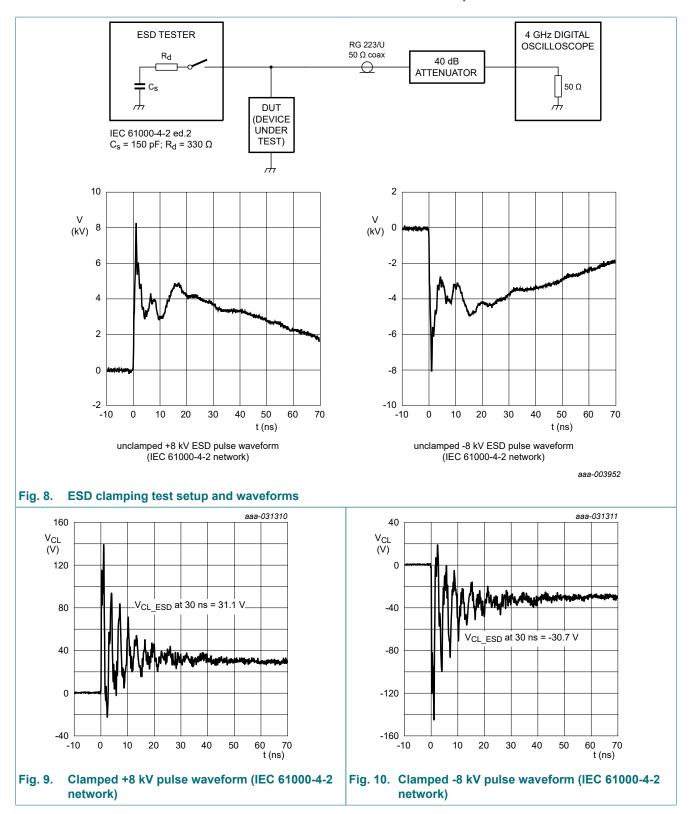


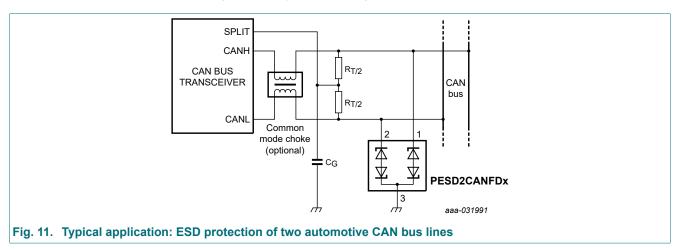
Fig. 7. Relative variation of peak pulse power as a function of junction temperature; typical values



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## 10. Application information

The device is designed for the protection of two automotive in-vehicle bus lines, e.g. CAN (FD), from the damage caused by ESD and surge pulses.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

## 11. Test information

### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

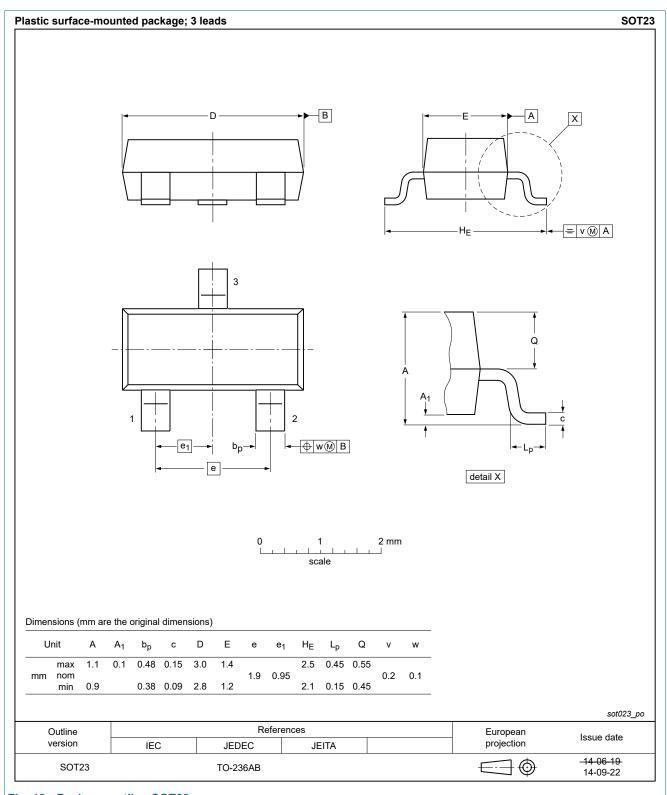
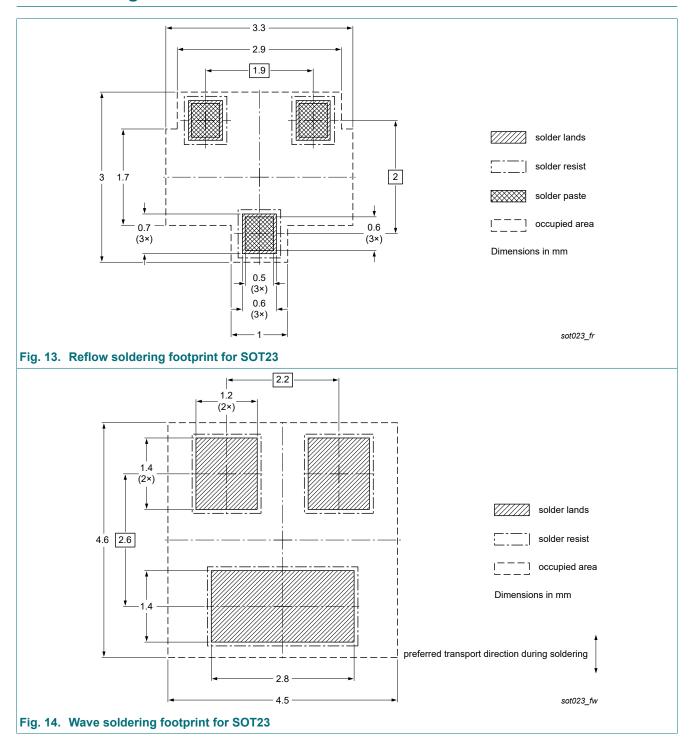


Fig. 12. Package outline SOT23

## 13. Soldering



## 14. Revision history

### **Table 7. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD2CANFD27VT-Q	20230412	Product data sheet	-	-
v.1				

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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