# XC7SET14

# Inverting Schmitt trigger Rev. 3 — 18 December 2023

**Product data sheet** 

### 1. General description

XC7SET14 is a high-speed Si-gate CMOS device. It provides an inverting buffer function with Schmitt trigger action. This device is capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

#### 2. Features and benefits

- Symmetrical output impedance
- High noise immunity
- · Low power dissipation
- TTL input levels
- · Balanced propagation delays
- · ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Specified from -40 °C to +125 °C

### 3. Applications

- · Wave and pulse shapers
- Astable multivibrators
- Monostable multivibrators

# 4. Ordering information

#### **Table 1. Ordering information**

Type number	nber Package						
	Temperature range	Name	Description	Version			
XC7SET14GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1			
XC7SET14GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753			

## 5. Marking

#### Table 2. Marking codes

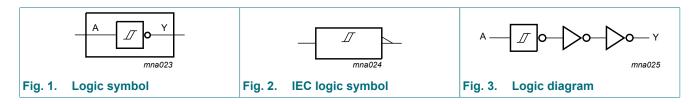
Type number	Marking code [1]
XC7SET14GW	gF
XC7SET14GV	g14

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.



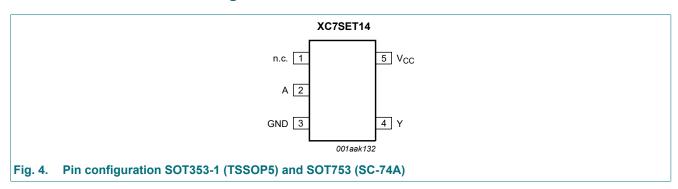
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# 6. Functional diagram



# 7. Pinning information

### 7.1. Pinning



### 7.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
n.c.	1	not connected
A	2	data input
GND	3	ground (0 V)
Υ	4	data output
V <sub>CC</sub>	5	supply voltage

# 8. Functional description

#### **Table 4. Function table**

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$ 

Input	Output
A	Υ
L	Н
Н	L

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# 9. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+7.0	V
VI	input voltage		-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < -0.5 V	-20	-	mA
I <sub>OK</sub>	output clamping current	$V_O < -0.5 \text{ V or } V_O > V_{CC} + 0.5 \text{ V}$ [1]	-	±20	mA
Io	output current	$-0.5 \text{ V} < \text{V}_{\text{O}} < \text{V}_{\text{CC}} + 0.5 \text{ V}$	-	±25	mA
I <sub>CC</sub>	supply current		-	75	mA
$I_{GND}$	ground current		-75	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$ [2]	-	250	mW

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

# 10. Recommended operating conditions

#### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage		0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	°C

<sup>[2]</sup> For SOT353-1 (TSSOP5) package: P<sub>tot</sub> derates linearly with 3.3 mW/K above 74 °C. For SOT753 (SC-74A) package: P<sub>tot</sub> derates linearly with 3.8 mW/K above 85 °C.

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### 11. Static characteristics

#### **Table 7. Static characteristics**

Voltages are referenced to GND (ground = 0 V).

Symbol	nbol Parameter Conditions			25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{T+} \text{ or } V_{T-}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I <sub>O</sub> = -50 μA	4.4	4.5	-	4.4	-	4.4	-	V
		I <sub>O</sub> = -8.0 mA	3.94	-	-	3.8	-	3.70	-	V
$V_{OL}$	LOW-level	$V_{I} = V_{T+} \text{ or } V_{T-}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I <sub>O</sub> = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 8.0 mA	-	-	0.36	-	0.44	-	0.55	V
l <sub>l</sub>	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μΑ
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	10	-	40	μΑ
ΔI <sub>CC</sub>	additional supply current	per input pin; $V_I$ = 3.4 V; other inputs at $V_{CC}$ or GND; $I_O$ = 0 A; $V_{CC}$ = 5.5 V	-	-	1.35	-	1.5	-	1.5	mA
Cı	input capacitance		-	1.5	10	-	10	-	10	pF

### 12. Transfer characteristics

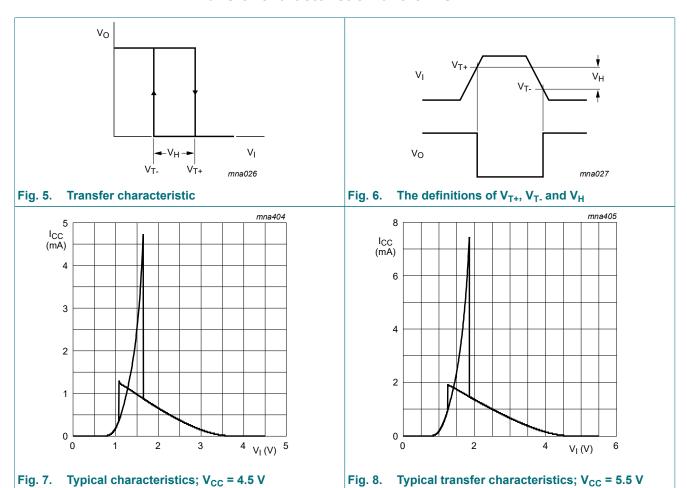
#### **Table 8. Transfer characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V). See Fig. 5 and Fig. 6.

Symbol	Parameter	Conditions	Conditions 25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit	
			Min	Тур	Max	Min	Max	Min	Max	
V <sub>T+</sub>	positive-going threshold	V <sub>CC</sub> = 4.5 V	-	-	2.0	-	2.0	-	2.0	V
voltage	voltage	V <sub>CC</sub> = 5.5 V	-	-	2.0	-	2.0	-	2.0	V
V <sub>T-</sub>	negative-going threshold	V <sub>CC</sub> = 4.5 V	0.5	-	-	0.5	-	0.5	-	V
	voltage	V <sub>CC</sub> = 5.5 V	0.6	-	-	0.6	-	0.6	-	V
V <sub>H</sub>	hysteresis voltage	V <sub>CC</sub> = 4.5 V	0.4	-	1.4	0.4	1.4	0.35	1.4	V
		V <sub>CC</sub> = 5.5 V	0.4	-	1.6	0.4	1.6	0.35	1.6	V

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#### 12.1. Transfer characteristic waveforms



# 13. Dynamic characteristics

**Table 9. Dynamic characteristics** 

GND = 0 V. For waveform see Fig. 9. For test circuit see Fig. 10.

Symbol	Parameter	Conditions		neter Conditions 25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max		
t <sub>pd</sub>	propagation delay	A to Y; [1] [2] V <sub>CC</sub> = 4.5 V to 5.5 V									
		C <sub>L</sub> = 15 pF	-	4.1	7.0	1.0	8.0	1.0	9.0	ns	
		C <sub>L</sub> = 50 pF	-	5.9	8.5	1.0	10.0	1.0	11.0	ns	
C <sub>PD</sub>	power dissipation capacitance	per buffer; [3] $V_I = GND$ to $V_{CC}$	-	13	-	-	-	-	-	pF	

- $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ . Typical values are measured at  $V_{CC}$  = 5.0 V.  $C_{PD}$  is used to determine the dynamic power dissipation  $P_D$  (µW).  $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$  where:  $f_i$  = input frequency in MHz;

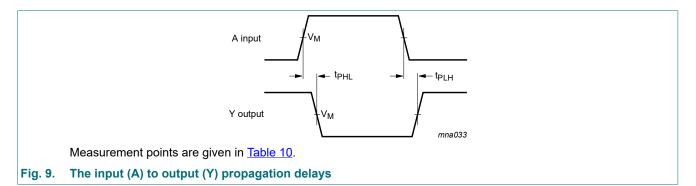
f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V.

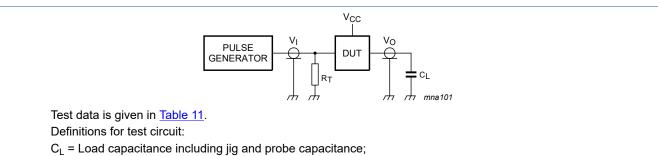
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#### 13.1. Waveform and test circuit



**Table 10. Measurement points** 

Input	Output	
VI	V <sub>M</sub>	V <sub>M</sub>
GND to 3.0 V	1.5 V	0.5 × V <sub>CC</sub>



 $R_T$  = Termination resistance should be equal to output impedance  $Z_0$  of the pulse generator.

Fig. 10. Test circuit for measuring switching times

Table 11. Test data

Input		Load	Test
V <sub>I</sub>	t <sub>r</sub> , t <sub>f</sub>	C <sub>L</sub>	
3.0 V	≤ 3.0 ns	15 pF, 50 pF	t <sub>PLH</sub> , t <sub>PHL</sub>

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# 14. Application information

The slow input rise and fall times cause additional power dissipation, which can be calculated using the following formula:

 $P_{add} = f_i \times (t_r \times \Delta I_{CC(AV)} + t_f \times \Delta I_{CC(AV)}) \times V_{CC}$  where:

 $P_{add}$  = additional power dissipation ( $\mu$ W);

 $f_i$  = input frequency (MHz);

 $t_r$  = input rise time (ns); 10 % to 90 %;

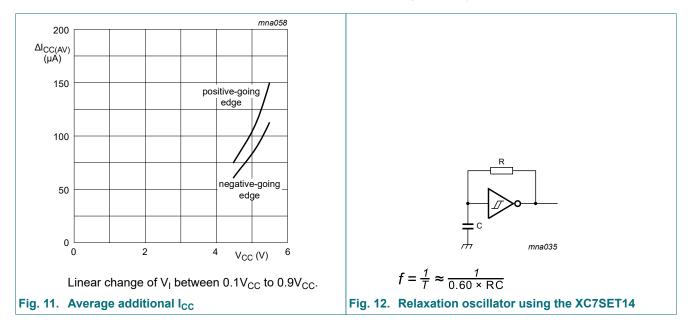
 $t_f$  = input fall time (ns); 90 % to 10 %;

 $\Delta I_{CC(AV)}$  = average additional supply current ( $\mu A$ ).

Average additional I<sub>CC</sub> differs with positive or negative input transitions, as shown in Fig. 11.

For XC7SET14 used in relaxation oscillator circuit, see Fig. 12.

Note to the application information: All values given are typical unless otherwise specified.



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# 15. Package outline

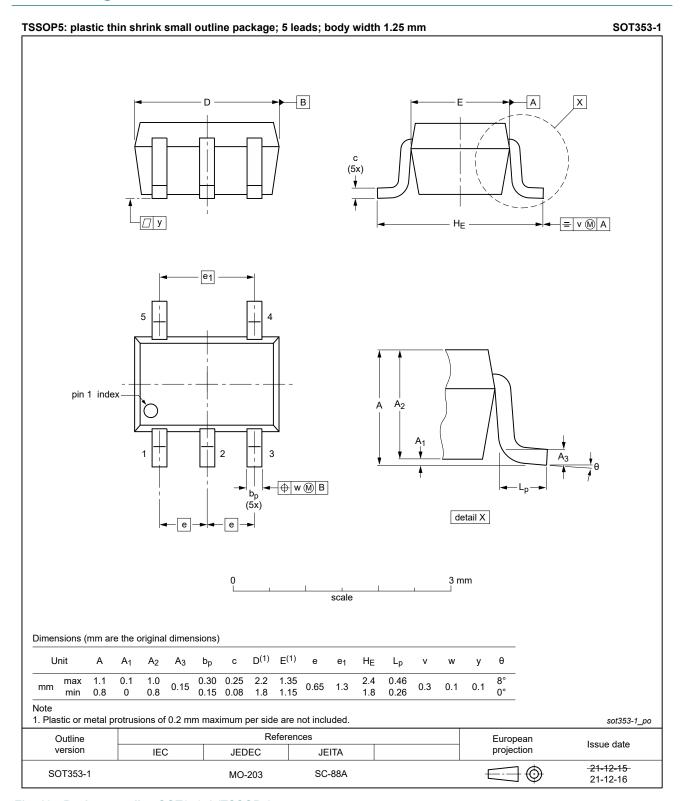


Fig. 13. Package outline SOT353-1 (TSSOP5)

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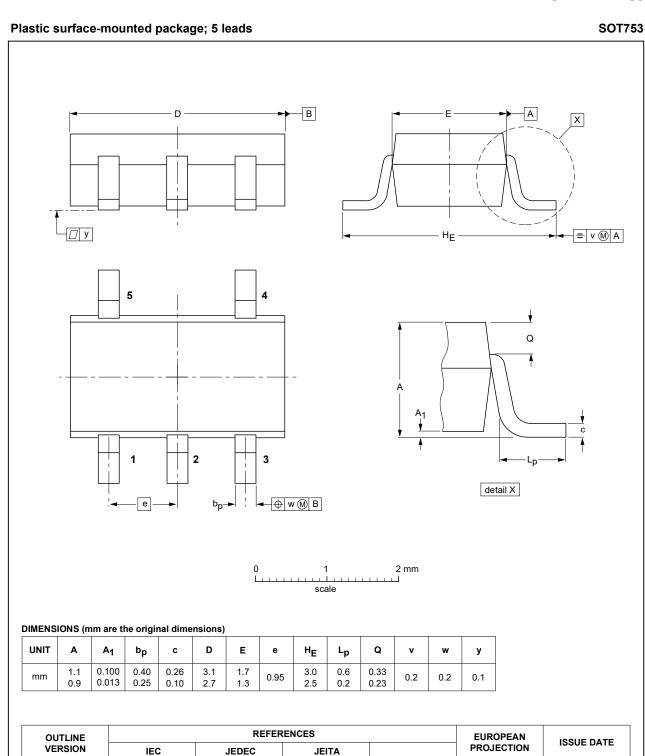


Fig. 14. Package outline SOT753 (SC-74A)

SOT753

SC-74A

02-04-16

06-03-16

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### 16. Abbreviations

#### **Table 12. Abbreviations**

Acronym	Description	
CDM	Charged Device Model	
CMOS	mplementary Metal-Oxide Semiconductor	
DUT	Device Under Test	
ESD	ElectroStatic Discharge	
HBM	Human Body Model	
TTL	Transistor-Transistor Logic	

# 17. Revision history

#### Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
XC7SET14 v.3	20231218	Product data sheet	-	XC7SET14 v.2	
Modifications:	<u>Section 2</u> : ESD specification updated according to the latest JEDEC standard.				
XC7SET14 v.2	20220114	Product data sheet	-	XC7SET14 v.1	
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Section 9: Derating values for P<sub>tot</sub> total power dissipation updated.</li> <li>Fig. 13: Package outline drawing SOT353-1 (TSSOP5) has changed.</li> </ul>				
XC7SET14 v.1	20090831	Product data sheet	-	-	

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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