



PBL4002Y

40 V, 500 mA PNP loadswitch transistor

1 January 2023

Product data sheet

1. General description

PNP low V_{CEsat} transistor and NPN Resistor-Equipped Transistor (RET) in one very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low V_{CEsat} transistor and resistor-equipped transistor in one package
- Low threshold voltage (<1 V) compared to MOSFET
- Low drive power required
- Space-saving solution
- Reduction of component count

3. Applications

- Supply line switches
- Battery charger switches
- High-side switches for LEDs, drivers and backlights
- Portable equipment

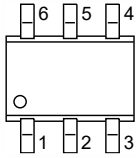
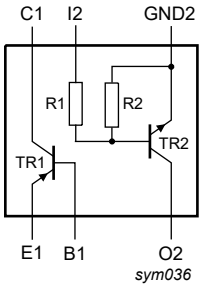
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
TR1: PNP low V_{CEsat} transistor						
V_{CEO}	collector-emitter voltage	open base	-	-	-40	V
I_{Clim}	limiting collector current		-	-	-500	mA
R_{CEsat}	collector-emitter saturation resistance	$I_C = -500$ mA; $I_B = -50$ mA; $T_{amb} = 25$ °C; pulsed; $t_p \leq 300$ μ s; $\delta_{factor} \leq 0.02$	-	440	700	m Ω
TR2: NPN resistor-equipped transistor						
V_{CEO}	collector-emitter voltage	open base	-	-	50	V
I_O	output current		-	-	100	mA
R1	bias resistor 1 (input)		3.3	4.7	6.1	k Ω
R2/R1	bias resistor ratio		0.8	1	1.2	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	GND1	GND (emitter) TR1	 TSSOP6 (SOT363)	 <i>sym036</i>
2	B1	base TR1		
3	O2	output (collector) TR2		
4	GND2	GND (emitter) TR2		
5	I2	input (base) TR2		
6	C1	collector TR1		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBLS4002Y	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBLS4002Y	S2%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
TR1: PNP low V _{CEsat} transistor						
V _{CBO}	collector-base voltage	open emitter		-	-40	V
V _{CEO}	collector-emitter voltage	open base		-	-40	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _{Clim}	limiting collector current			-	-500	mA
I _{CM}	peak collector current	t _p ≤ 1 ms; single pulse		-	-1	mA
I _B	base current			-	-50	mA
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
TR2: NPN resistor-equipped transistor						
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V _{EBO}	emitter-base voltage	open collector		-	10	V
V _I	input voltage	input voltage TR2 positive		-	30	V
		input voltage TR2 negative		-	-10	V
I _O	output current			-	100	mA
I _{CM}	peak collector current	t _p ≤ 1 ms; single pulse		-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Per device						
P _{tot}	total power dissipation			-	300	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per device							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W

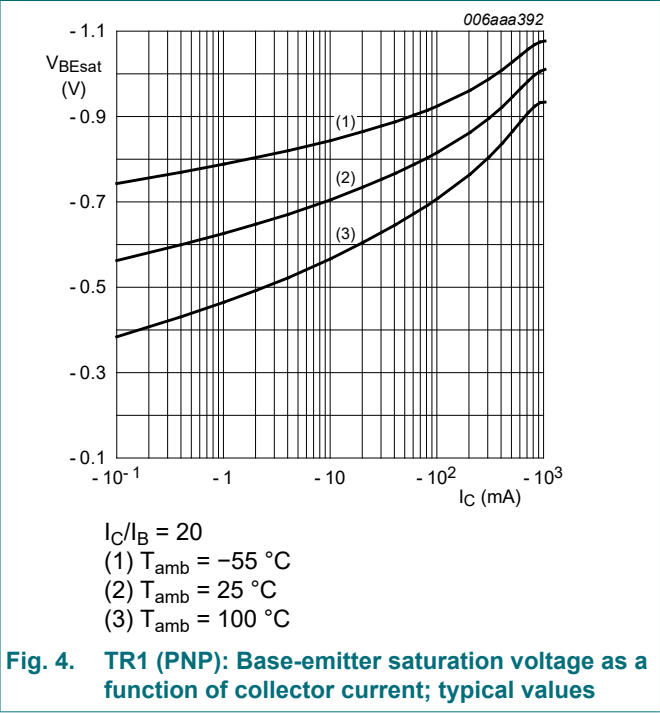
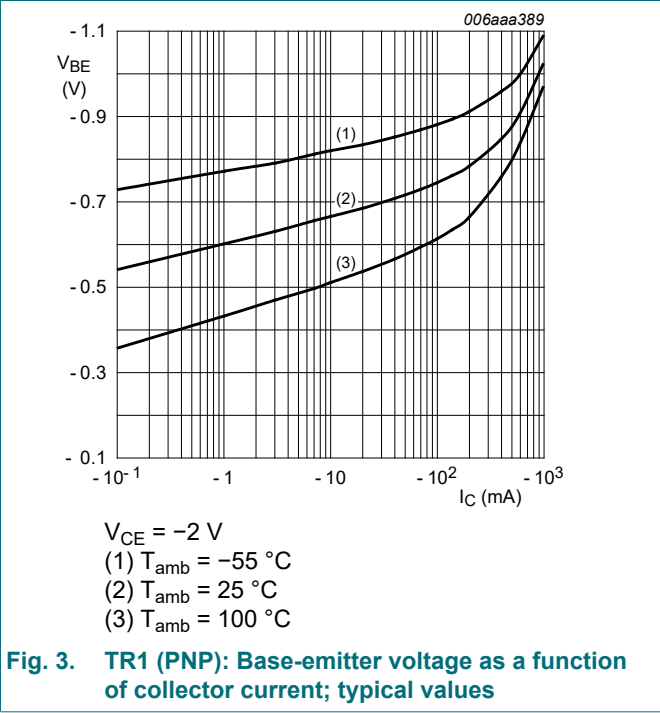
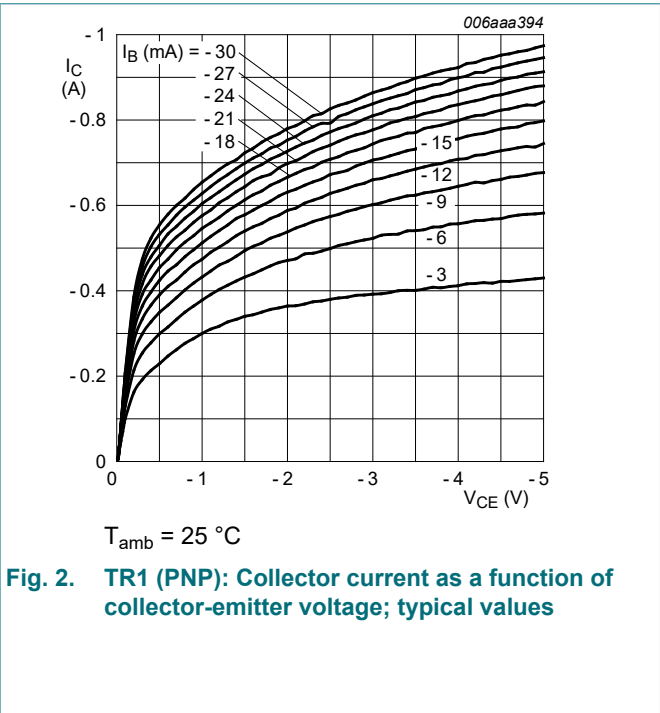
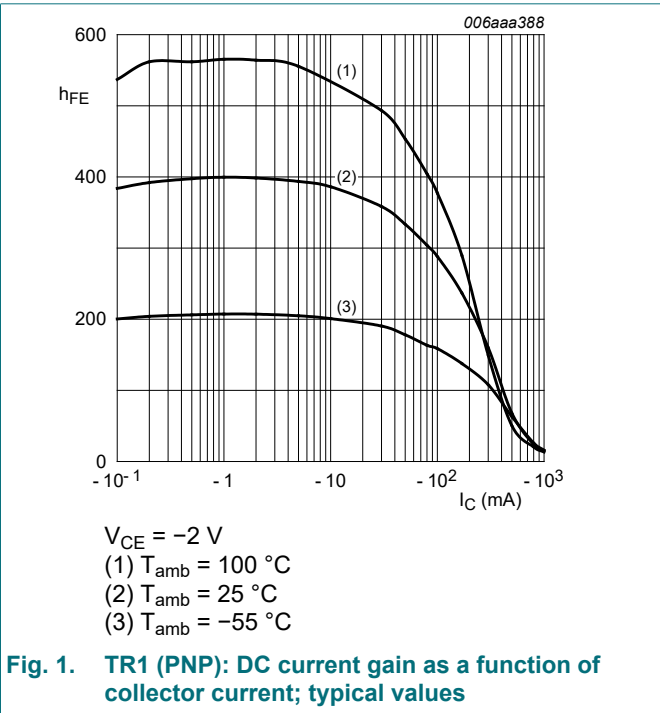
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
TR1: PNP low V_{CEsat} transistor							
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = -100 \mu A$; $I_E = 0 A$; $T_{amb} = 25^\circ C$		-40	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = -10 mA$; $I_B = 0 A$; $T_{amb} = 25^\circ C$		-40	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	$I_C = 0 A$; $I_E = 100 \mu A$; $T_{amb} = 25^\circ C$		-6	-	-	V
I_{CBO}	collector-base cut-off current	$V_{CB} = -40 V$; $I_E = 0 A$; $T_{amb} = 25^\circ C$		-	-	-100	nA
		$V_{CB} = -40 V$; $I_E = 0 A$; $T_{amb} = 150^\circ C$		-	-	-50	μA
I_{EBO}	emitter-base cut-off current	$V_{EB} = -5 V$; $I_C = 0 mA$; $T_{amb} = 25^\circ C$		-	-	-100	nA
h_{FE}	DC current gain	$V_{CE} = -2 V$; $I_C = -10 mA$; pulsed; $T_{amb} = 25^\circ C$		200	-	-	
		$V_{CE} = -2 V$; $I_C = -100 mA$; pulsed; $t_p \leq 300 \mu s$; $\delta \leq 0.02$; $T_{amb} = 25^\circ C$		150	-	-	
		$V_{CE} = -2 V$; $I_C = -500 mA$; pulsed; $t_p \leq 300 \mu s$; $\delta \leq 0.02$; $T_{amb} = 25^\circ C$		40	-	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -10 mA$; $I_B = -0.5 mA$; $T_{amb} = 25^\circ C$		-	-	-50	mV
		$I_C = -100 mA$; $I_B = -5 mA$; $T_{amb} = 25^\circ C$		-	-	-130	mV
		$I_C = -200 mA$; $I_B = -10 mA$; $T_{amb} = 25^\circ C$		-	-	-200	mV
		$I_C = -500 mA$; $I_B = -50 mA$; pulsed; $t_p \leq 300 \mu s$; $\delta \leq 0.02 \%$; $T_{amb} = 25^\circ C$		-	-	-350	mV
R_{CEsat}	collector-emitter saturation resistance	$I_C = -500 mA$; $I_B = -50 mA$; $T_{amb} = 25^\circ C$; pulsed; $t_p \leq 300 \mu s$; $\delta_{factor} \leq 0.02$		-	440	700	m Ω
V_{BEsat}	base-emitter saturation voltage			-	-	-1.2	V
V_{BEon}	base-emitter turn-on voltage	$V_{CE} = -2 V$; $I_C = -100 mA$; $T_{amb} = 25^\circ C$; pulsed; $t_p \leq 300 \mu s$; $\delta_{factor} \leq 0.02$		-	-	-1.1	V
C_c	collector capacitance	$V_{CB} = -10 V$; $I_E = 0 A$; $i_e = 0 A$; $f = 1 MHz$; $T_{amb} = 25^\circ C$		-	-	10	pF
f_T	transition frequency	$V_{CE} = -5 V$; $I_C = -100 mA$; $f = 100 MHz$; $T_{amb} = 25^\circ C$		100	300	-	MHz
TR2: NPN resistor-equipped transistor							
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100 \mu A$; $I_E = 0 A$; $T_{amb} = 25^\circ C$		50	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 10 mA$; $I_B = 0 A$; $T_{amb} = 25^\circ C$		50	-	-	V
I_{CBO}	collector-base cut-off current	$V_{CB} = 50 V$; $I_E = 0 A$; $T_{amb} = 25^\circ C$		-	-	100	nA
I_{CEO}	collector-emitter cut-off current	$V_{CE} = 50 V$; $I_B = 0 A$; $T_{amb} = 25^\circ C$		-	-	1	μA
		$V_{CE} = 50 V$; $I_B = 0 A$; $T_{amb} = 150^\circ C$		-	-	50	μA
I_{EBO}	emitter-base cut-off current	$V_{EB} = 5 V$; $I_C = 0 A$; $T_{amb} = 25^\circ C$		-	-	900	μA
h_{FE}	DC current gain	$V_{CE} = 5 V$; $I_C = 10 mA$; $T_{amb} = 25^\circ C$		30	-	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 10 mA$; $I_B = 0.5 mA$; $T_{amb} = 25^\circ C$		-	-	150	mV

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{I(off)}$	off-state input voltage	$V_{CE} = 5\text{ V}$; $I_C = 100\text{ }\mu\text{A}$; $T_{amb} = 25\text{ }^\circ\text{C}$	-	1.1	0.5	V
$V_{I(on)}$	on-state input voltage	$V_{CE} = 0.3\text{ V}$; $I_C = 20\text{ mA}$; $T_{amb} = 25\text{ }^\circ\text{C}$	2.5	1.9	-	V
R1	bias resistor 1 (input)		3.3	4.7	6.1	k Ω
R2/R1	bias resistor ratio		0.8	1	1.2	
C_c	collector capacitance	$V_{CB} = 10\text{ V}$; $I_E = 0\text{ A}$; $i_e = 0\text{ A}$; $f = 1\text{ MHz}$; $T_{amb} = 25\text{ }^\circ\text{C}$	-	-	2.5	pF



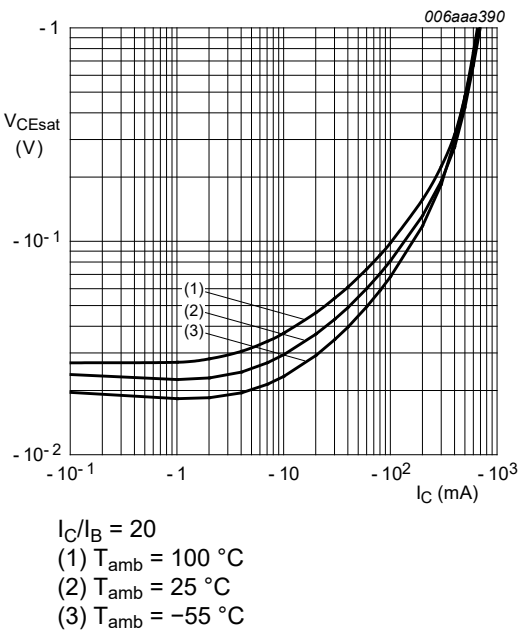


Fig. 5. TR1 (PNP): Collector-emitter saturation voltage as a function of collector current; typical values

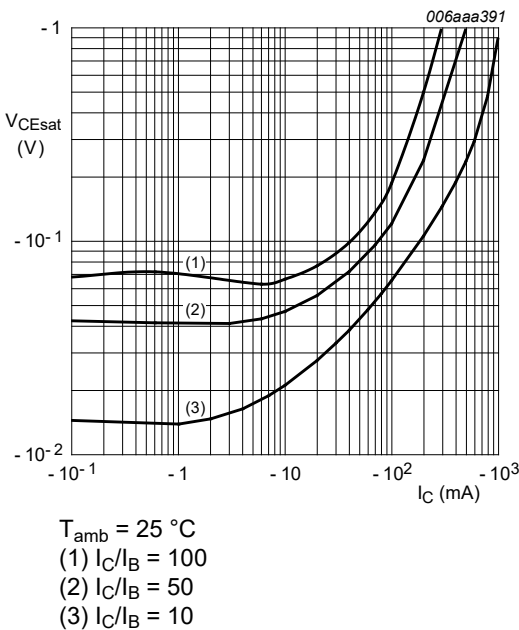


Fig. 6. TR1 (PNP): Collector-emitter saturation voltage as a function of collector current; typical values

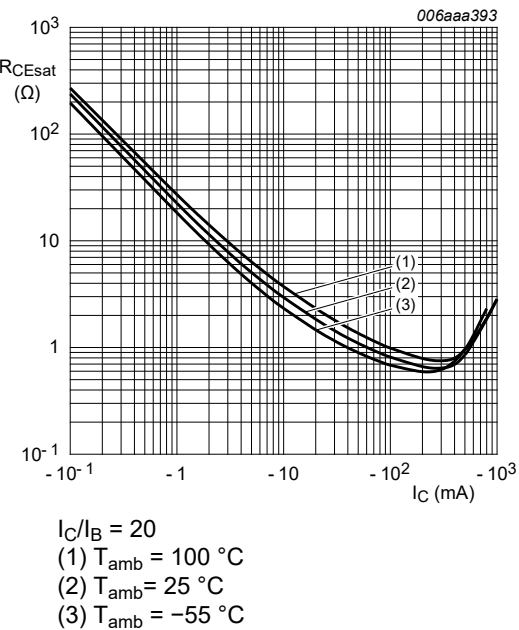


Fig. 7. TR1 (PNP): Collector-emitter saturation resistance as a function of collector current; typical values

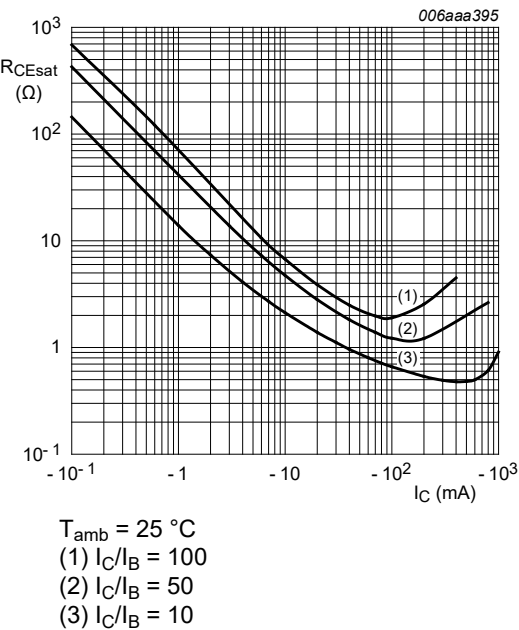


Fig. 8. TR1 (PNP): Collector-emitter saturation resistance as a function of collector current; typical values

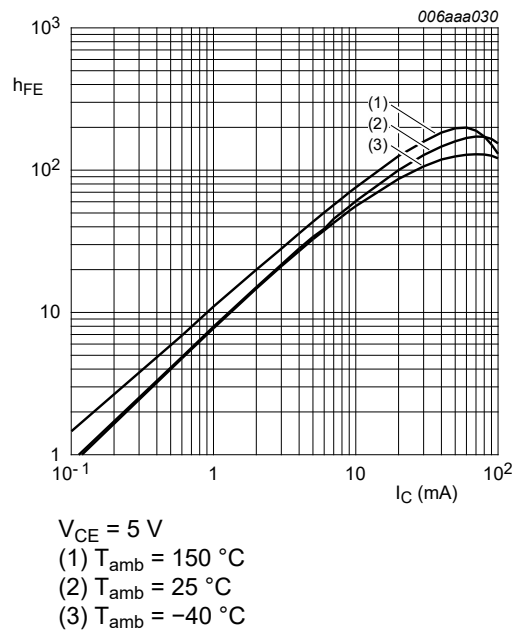


Fig. 9. TR2 (NPN): DC current gain as a function of collector current; typical values

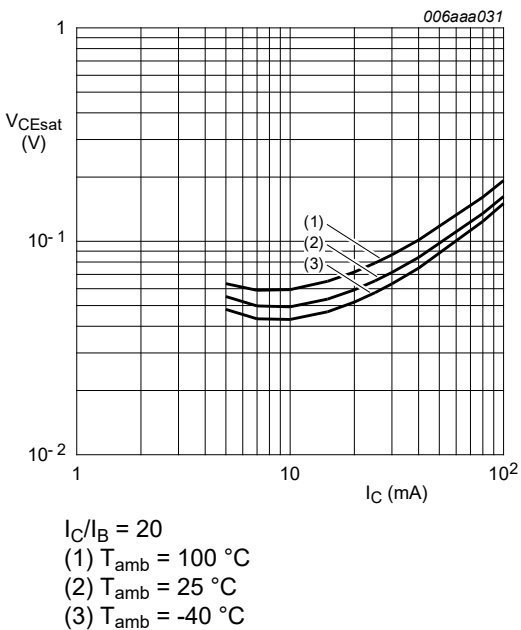


Fig. 10. TR2 (NPN): Collector-emitter saturation voltage as a function of collector current; typical values

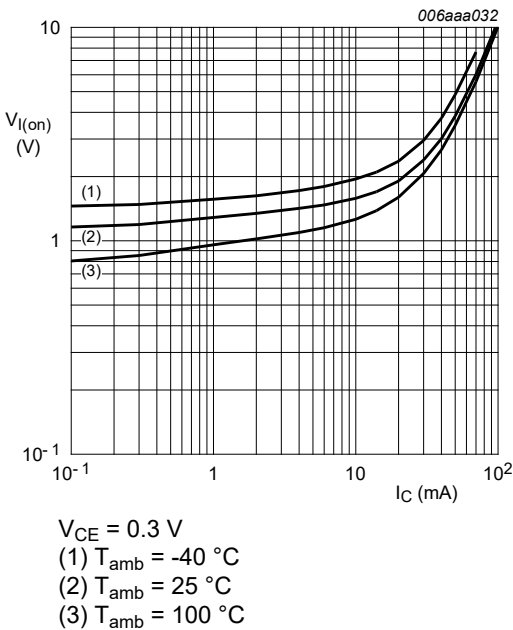


Fig. 11. TR2 (NPN): On-state input voltage as a function of collector current; typical values

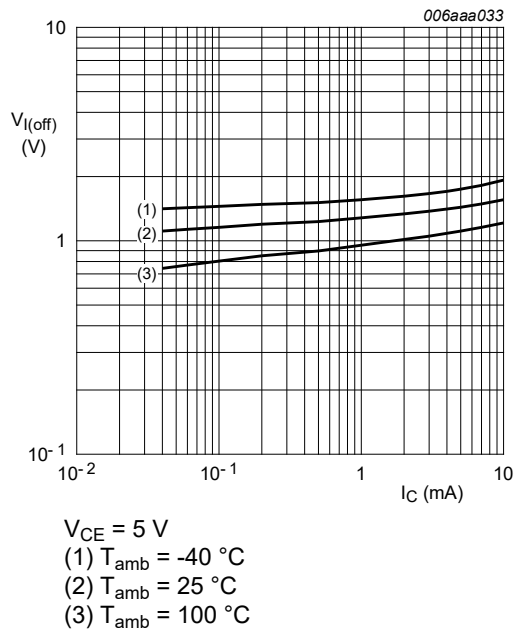


Fig. 12. TR2 (NPN): Off-state input voltage as a function of collector current; typical values

11. Test information

Resistor calculation

- Calculation of bias resistor 1 (R1)

$$R_1 = \frac{V(I_2) - V(I_1)}{I_2 - I_1}$$

- Calculation of bias resistor ratio (R2/R1)

$$\frac{R_2}{R_1} = \frac{V(I_3)}{R_1 \cdot I_3} - 1$$

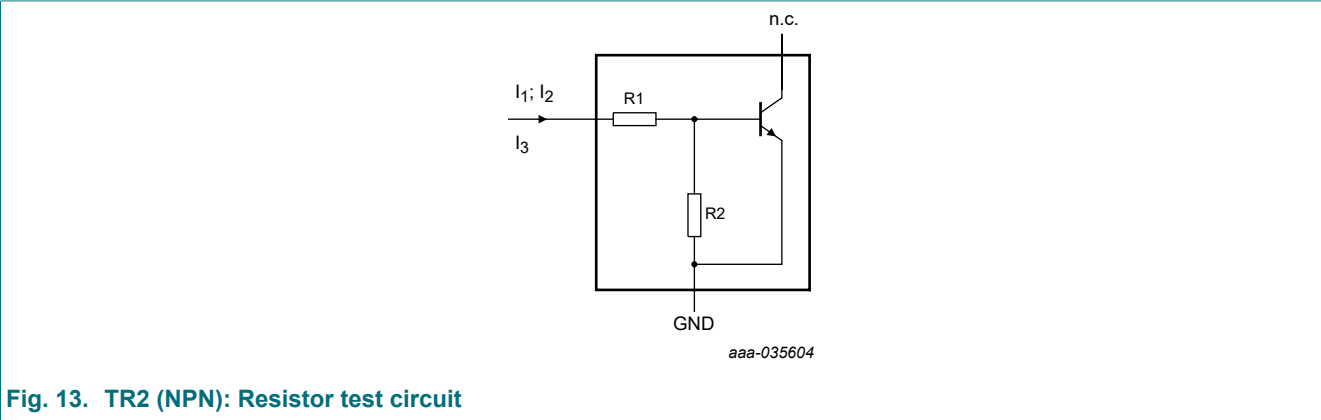


Fig. 13. TR2 (NPN): Resistor test circuit

Resistor test conditions

Table 8. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions		
			I ₁	I ₂	I ₃
Per transistor, for the PNP with negative polarity					
PBLS4002Y	4.7	4.7	750 μA	950 μA	850 μA

12. Package outline

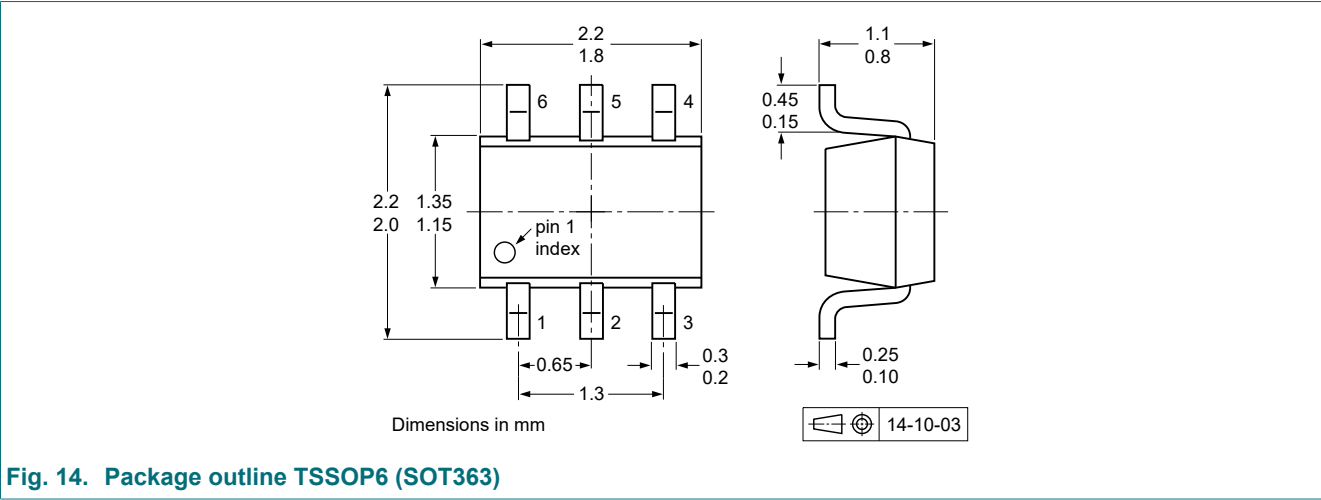


Fig. 14. Package outline TSSOP6 (SOT363)

13. Soldering

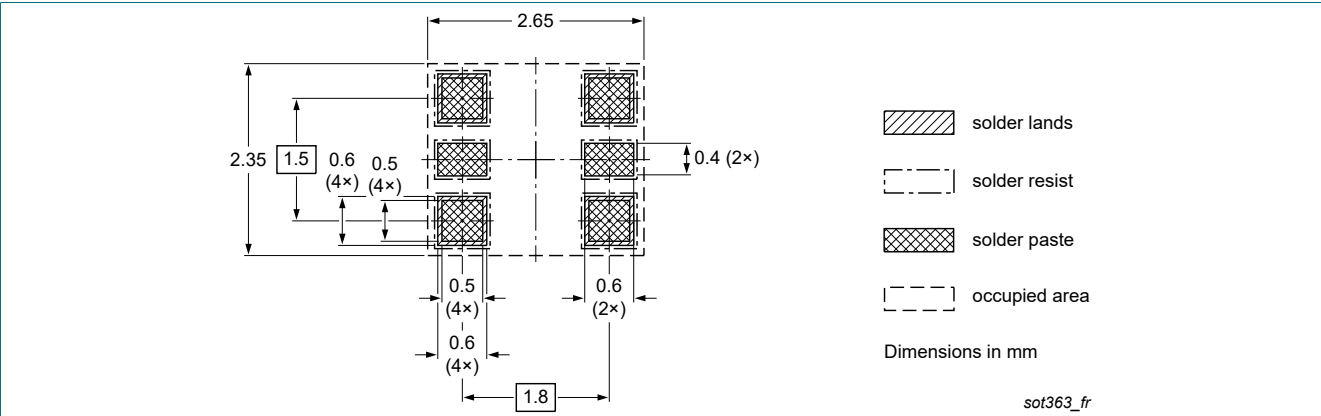


Fig. 15. Reflow soldering footprint for TSSOP6 (SOT363)

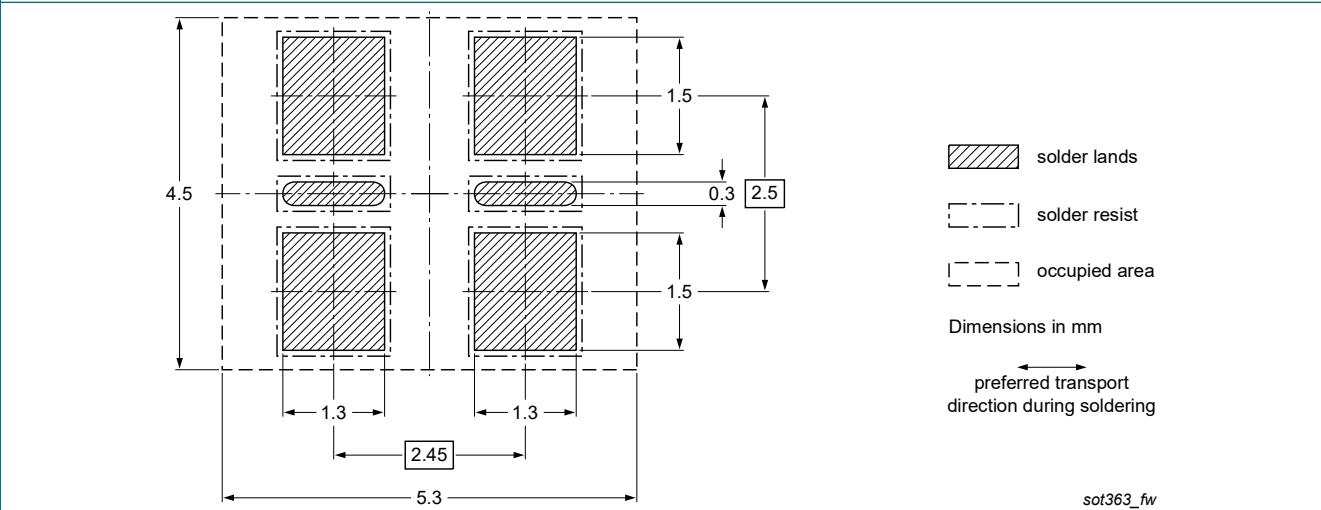


Fig. 16. Wave soldering footprint for TSSOP6 (SOT363)

14. Revision history

Table 9. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBLS4002Y v.5	20230101	Product data sheet	-	PBLS4002Y v.4
Modifications:	• Product changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
PBLS4002Y v.4	20220427	Product data sheet	-	PBLS4002Y_PBLS4002V_3
PBLS4002Y_PBLS4002V_3	20090212	Product data sheet	-	PBLS4002Y_PBLS4002V_2
PBLS4002Y_PBLS4002V_2	20050719	Product data sheet	-	PBLS4002Y_PBLS4002V_1
PBLS4002Y_PBLS4002V_1	20041206	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Contents

1. General description..... 1

2. Features and benefits..... 1

3. Applications..... 1

4. Quick reference data..... 1

5. Pinning information.....2

6. Ordering information.....2

7. Marking.....2

8. Limiting values..... 3

9. Thermal characteristics..... 3

10. Characteristics..... 4

11. Test information..... 8

12. Package outline..... 8

13. Soldering..... 9

14. Revision history.....10

15. Legal information.....11

© Nexperia B.V. 2023. All rights reserved

For more information, please visit: <http://www.nexperia.com>

For sales office addresses, please send an email to: salesaddresses@nexperia.com

Date of release: 1 January 2023

单击下面可查看定价，库存，交付和生命周期等信息

[>>Nexperia\(安世\)](#)