Low-power buffer/line driver; 3-state Rev. 2 — 25 January 2019

1. General description

The 74AUP1G125-Q100 provides a single non-inverting buffer/line driver with 3-state output. The 3-state output is controlled by the output enable input (\overline{OE}). A HIGH level at pin \overline{OE} causes the output to assume a high-impedance OFF-state. This device has the input-disable feature, which allows floating input signals. The inputs are disabled when the output enable input \overline{OE}) is HIGH.

Schmitt-trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V. This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing a damaging backflow current through the device when it is powered down.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - MIL-STD-883, method 3015 Class 3A. Exceeds 5000 V
 - HBM JESD22-A114F Class 3A. Exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)
- Low static power consumption; $I_{CC} = 0.9 \,\mu A$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- Input-disable feature allows floating input conditions
- IOFF circuitry provides partial Power-down mode operation

ne<mark>x</mark>peria

3. Ordering information

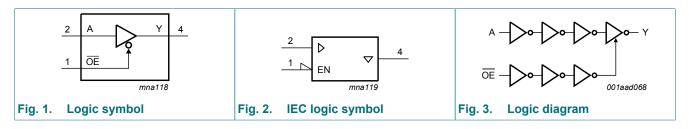
Type number	Package			
	Temperature range	Name	Description	Version
74AUP1G125GW-Q100	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
74AUP1G125GM-Q100	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886
74AUP1G125GS-Q100	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202

4. Marking

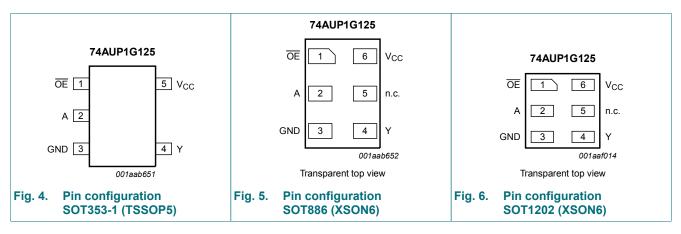
Table 2. Marking					
Type number	Marking code [1]				
74AUP1G125GW-Q100	рМ				
74AUP1G125GM-Q100	рМ				
74AUP1G125GS-Q100	рМ				

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information



6.1. Pinning

6.2. Pin description

Symbol	Pin	Pin			
	TSSOP5	XSON6			
OE	1	1	output enable input		
A	2	2	data input		
GND	3	3	ground (0 V)		
Y	4	4	data output		
n.c.	-	5	not connected		
V _{CC}	5	6	supply voltage		

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level; X = Don't care; Z = high-impedance OFF-state.

Input OE	nput C			
OE	Α	Y		
L	L	L		
L	Н	Н		
Н	X	Z		

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V ₁ < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage	Active mode [1]	-0.5	V _{CC} + 0.5	V
		Power-down mode [1]	-0.5	+4.6	V
I _O	output current	$V_{O} = 0 V$ to V_{CC}	-	±20	mA
I _{CC}	supply current		-	+50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C [2]	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For TSSOP5 packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K.

For XSON6 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	bol Parameter Conditions		Min	Мах	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V_{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 0.8 V to 3.6 V	0	200	ns/V

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	25 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	$I_{\rm O}$ = -20 µA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
l _l	input leakage current	V_1 = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.1	μA
I _{OZ}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.1	μA
I _{OFF}	power-off leakage current	V_1 or V_0 = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.2	μA
∆I _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μA
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
ΔI _{CC}	additional supply current	data input; V _I = V _{CC} - 0.6 V; I _O = 0 A; V _{CC} = 3.3 V	[1] -	-	40	μA
			[1] -	-	110	μA
		all inputs; V_I = GND to 3.6 V; \overline{OE} = V _{CC} ; V _{CC} = 0.8 V to 3.6 V	[2] -	-	1	μA

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
CI	input capacitance	V_{CC} = 0 V to 3.6 V; V _I = GND or V _{CC}	-	0.9	-	pF
Co	output capacitance	output enabled; V_0 = GND; V_{CC} = 0 V	-	1.7	-	pF
		output disabled; V _{CC} = 0 V to 3.6 V; V _O = GND or V _{CC}	-	1.5	-	pF
T _{amb} = -4	40 °C to +85 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	$0.70 \times V_{CC}$	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output	V _I = V _{IH} or V _{IL}				
	voltage	$I_{\rm O}$ = -20 µA; $V_{\rm CC}$ = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V
		$I_0 = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V
V _{OL}	LOW-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V
l	input leakage current	V_1 = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.5	μA
l _{oz}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.5	μA
I _{OFF}	power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.5	μA
Δl _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.6	μA
I _{CC}	supply current	V_1 = GND or V_{CC} ; I_0 = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.9	μA
ΔI _{CC}	additional supply current	data input; V _I = V _{CC} - 0.6 V; I _O = 0 A; [1 V_{CC} = 3.3 V	1 -	-	50	μA
		\overline{OE} input; V _I = V _{CC} - 0.6 V; I _O = 0 A; [1 V _{CC} = 3.3 V	1 -	-	120	μA
		all inputs; $V_I = GND$ to 3.6 V; [2 $\overline{OE} = V_{CC}$; $V_{CC} = 0.8$ V to 3.6 V	- 1	-	1	μA

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	-40 °C to +125 °C	·				
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.75 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.25 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	$I_{\rm O}$ = -20 µA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I_0 = -2.3 mA; V_{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.30	-	-	V
-	LOW-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
l _l	input leakage current	V_1 = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.75	μA
I _{OZ}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.75	μA
I _{OFF}	power-off leakage current	V_1 or V_0 = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.75	μA
ΔI _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA
I _{CC}	supply current	V_I = GND or V_{CC} ; I_O = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	1.4	μA
ΔI _{CC}	additional supply current	data input; V _I = V _{CC} - 0.6 V; I _O = 0 A; [1 V_{CC} = 3.3 V] -	-	75	μA
			1 -	-	180	μA
		all inputs; V_1 = GND to 3.6 V; [2 $\overline{OE} = V_{CC}$; $V_{CC} = 0.8$ V to 3.6 V	2] -	-	1	μA

[1] [2]

One input at V_{CC} - 0.6 V, other input at V_{CC} or GND. To show I_{CC} remains very low when the input-disable feature is enabled.

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9

Symbol	Parameter	Conditions	Т	_{amb} = 25 °	°C	T _{an} -40 °C te	_{1b} = o +85 °C	T _{ar} -40 °C to	_{nb} = o +125 °C	Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	1
C _L = 5 p	F			· · · · · ·						
t _{pd}	· · ·	A to Y; see <u>Fig. 7</u> [2]								
	delay	V _{CC} = 0.8 V	-	20.6	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.8	5.5	10.5	2.5	11.7	2.5	12.9	ns
		V _{CC} = 1.4 V to 1.6 V	2.2	3.9	6.1	2.0	7.3	2.0	8.1	ns
		V _{CC} = 1.65 V to 1.95 V	1.9	3.2	4.8	1.7	6.1	1.7	6.7	ns
		V _{CC} = 2.3 V to 2.7 V	1.6	2.6	3.6	1.4	4.3	1.4	4.9	ns
		V _{CC} = 3.0 V to 3.6 V	1.4	2.4	3.1	1.2	3.9	1.2	4.4	ns
t _{en}	enable time	OE to Y; see Fig. 8 [3]								
		V _{CC} = 0.8 V	-	69.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.1	6.1	11.8	2.9	13.9	2.9	15.4	ns
		V _{CC} = 1.4 V to 1.6 V	2.5	4.2	6.6	2.3	7.7	2.3	8.3	ns
		V _{CC} = 1.65 V to 1.95 V	2.1	3.4	5.1	2.0	6.2	2.0	6.8	ns
		V _{CC} = 2.3 V to 2.7 V	1.8	2.6	3.7	1.7	4.5	1.7	5.0	ns
		V _{CC} = 3.0 V to 3.6 V	1.7	2.4	3.1	1.7	3.5	1.7	3.9	ns
t _{dis} disa	disable time	OE to Y; see Fig. 8 [4]								
		V _{CC} = 0.8 V	-	14.3	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.7	4.3	6.5	2.7	7.3	2.7	8.2	ns
		V _{CC} = 1.4 V to 1.6 V	2.1	3.2	4.4	2.1	5.1	2.1	5.7	ns
		V _{CC} = 1.65 V to 1.95 V	2.0	3.0	4.3	2.0	5.0	2.0	5.7	ns
		V _{CC} = 2.3 V to 2.7 V	1.4	2.2	2.9	1.4	3.3	1.4	4.1	ns
		V _{CC} = 3.0 V to 3.6 V	1.7	2.5	3.2	1.7	3.4	1.7	3.9	ns
C _L = 10	pF			· · · · · ·		-			-	
t _{pd}		A to Y; see <u>Fig. 7</u> [2]								
	delay	V _{CC} = 0.8 V	-	24.0	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.2	6.4	12.3	3.0	13.8	3.0	15.2	ns
		V _{CC} = 1.4 V to 1.6 V	2.1	4.5	7.3	1.9	8.5	1.9	9.4	ns
		V _{CC} = 1.65 V to 1.95 V	1.9	3.8	5.5	1.7	6.8	1.7	7.6	ns
		V _{CC} = 2.3 V to 2.7 V	2.1	3.2	4.2	1.6	5.3	1.6	5.9	ns
		V _{CC} = 3.0 V to 3.6 V	1.8	3.0	3.8	1.6	4.6	1.6	5.2	ns
t _{en}	enable time	OE to Y; see Fig. 8 [3]								
		V _{CC} = 0.8 V	-	73.7	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.6	6.9	13.5	3.4	15.8	3.4	17.5	ns
		V _{CC} = 1.4 V to 1.6 V	2.3	4.8	7.7	2.2	8.6	2.2	9.4	ns
		V _{CC} = 1.65 V to 1.95 V	2.0	3.9	5.8	1.9	6.8	1.9	7.4	ns
		V _{CC} = 2.3 V to 2.7 V	1.8	3.2	4.3	1.7	5.3	1.7	5.9	ns
		V _{CC} = 3.0 V to 3.6 V	1.7	3.0	3.9	1.7	4.3	1.7	4.8	ns

8 / 18

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions	T,	_{amb} = 25	°C	T _{ar} -40 °C t	_{nb} = o +85 °C	T _{amb} = -40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Мах	Min	Max	1
t _{dis}	disable time	OE to Y; see Fig. 8 [4]								
		V _{CC} = 0.8 V	-	32.7	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.4	5.4	7.9	3.4	8.8	3.4	9.9	ns
		V _{CC} = 1.4 V to 1.6 V	2.2	4.1	5.5	2.2	6.2	2.2	7.1	ns
		V _{CC} = 1.65 V to 1.95 V	2.2	4.2	5.6	1.9	6.3	1.9	7.1	ns
		V _{CC} = 2.3 V to 2.7 V	1.7	3.0	3.8	1.7	4.5	1.7	5.1	ns
		V _{CC} = 3.0 V to 3.6 V	2.1	3.8	4.8	1.7	5.0	1.7	5.6	ns
C _L = 15	pF					1		1		1
t _{pd}	propagation	A to Y; see Fig. 7 [2]								
	delay	V _{CC} = 0.8 V	-	27.4	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.6	7.2	14.1	3.3	15.8	3.3	17.5	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	5.1	8.1	2.5	9.8	2.5	10.9	ns
		V _{CC} = 1.65 V to 1.95 V	2.2	4.3	6.3	2.0	7.9	2.0	8.8	ns
		V _{CC} = 2.3 V to 2.7 V	2.0	3.7	4.9	1.8	6.0	1.8	6.7	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	3.5	4.4	1.8	5.4	1.8	6.1	ns
t _{en}	enable time	OE to Y; see Fig. 8 [3]								<u> </u>
		V _{CC} = 0.8 V	-	77.5	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.0	7.7	15.2	3.7	17.6	3.7	19.6	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	5.3	8.4	2.5	9.8	2.5	10.7	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	4.4	6.5	2.1	7.7	2.1	8.5	ns
		V _{CC} = 2.3 V to 2.7 V	2.1	3.6	5.0	2.0	6.1	2.0	6.8	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	3.5	4.5	1.9	4.9	1.9	5.5	ns
t _{dis}	disable time	OE to Y; see Fig. 8 [4]								
		V _{CC} = 0.8 V	-	60.8	-	_	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.3	6.5	9.2	3.7	10.3	3.7	11.6	ns
		V _{CC} = 1.4 V to 1.6 V	3.0	5.0	6.5	2.5	7.4	2.5	8.4	ns
		V _{CC} = 1.65 V to 1.95 V	3.0	5.3	6.6	2.1	7.4	2.1	8.9	ns
		V _{CC} = 2.3 V to 2.7 V	2.1	3.8	4.9	2.0	5.1	2.0	6.4	ns
		V _{CC} = 3.0 V to 3.6 V	2.9	5.0	6.2	1.9	6.6	1.9	7.4	ns
C _L = 30	pF						1			1
t _{pd}	propagation	A to Y; see Fig. 7 [2]								
,	delay	V _{CC} = 0.8 V	-	37.4	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.8	9.5	19.0	4.4	21.6	4.4	24.0	ns
		V _{CC} = 1.4 V to 1.6 V	4.0	6.7	10.8	3.0	13.0	3.0	14.5	ns
		V _{CC} = 1.65 V to 1.95 V	2.9	5.6	8.4	2.6	10.3	2.6	11.5	ns
		V _{CC} = 2.3 V to 2.7 V	2.7	4.8	6.3	2.5	7.8	2.5	8.7	ns
		V _{CC} = 3.0 V to 3.6 V	2.7	4.6	5.8	2.5	7.5	2.5	8.3	ns

Low-power buffer/line driver; 3-state

Symbol	Parameter	Conditions	T,	_{amb} = 25	°C	T _{an} -40 °C te	_{nb} = o +85 °C	T _{amb} = -40 °C to +125 °C		Unit
			Min	Typ [1]	Мах	Min	Max	Min	Max	
t _{en}	enable time	OE to Y; see Fig. 8 [3]								
		V _{CC} = 0.8 V	-	88.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	5.2	9.9	19.8	4.8	22.8	4.8	25.3	ns
		V _{CC} = 1.4 V to 1.6 V	4.0	6.8	10.8	3.1	12.6	3.1	14.1	ns
		V _{CC} = 1.65 V to 1.95 V	3.0	5.6	8.5	2.8	10.2	2.8	11.3	ns
		V _{CC} = 2.3 V to 2.7 V	2.7	4.8	6.5	2.6	7.8	2.6	8.8	ns
		V _{CC} = 3.0 V to 3.6 V	2.7	4.6	6.0	2.6	6.9	2.6	7.7	ns
t _{dis}	disable time	OE to Y; see Fig. 8 [4]								
		V _{CC} = 0.8 V	-	49.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	6.0	9.9	13.3	4.8	14.8	4.8	16.5	ns
		V _{CC} = 1.4 V to 1.6 V	4.4	7.7	9.6	3.1	10.7	3.1	12.1	ns
		V _{CC} = 1.65 V to 1.95 V	5.1	8.7	11.1	2.8	12.4	2.8	13.8	ns
		V_{CC} = 2.3 V to 2.7 V	3.6	6.2	7.4	2.6	8.6	2.6	9.6	ns
		V _{CC} = 3.0 V to 3.6 V	5.2	8.7	10.5	2.6	10.8	2.6	13.1	ns
T _{amb} = 2	5 °C									
C _{PD}	dissipation	f = 1 MHz; [5] $ V_I = GND \text{ to } V_{CC}; $ output enabled								
		V _{CC} = 0.8 V	-	2.7	-	-	-	-	-	pF
		V _{CC} = 1.1 V to 1.3 V	-	2.8	-	-	-	-	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.9	-	-	-	-	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	3.0	-	-	-	-	-	pF
		V_{CC} = 2.3 V to 2.7 V	-	3.6	-	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.2	-	-	-	-	-	pF

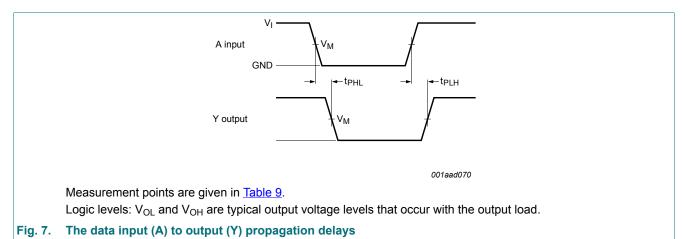
- All typical values are measured at nominal V_{CC} . [1]
- t_{pd} is the same as t_{PLH} and t_{PHL} , t_{en} is the same as t_{PZH} and t_{PZL} . [2]
- [3]
- [4] [5] f_i = input frequency in MHz;
 - f_0 = output frequency in MHz;

 C_{L} = output load capacitance in pF;

- V_{CC} = supply voltage in V;
- N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$ = sum of the outputs.

Low-power buffer/line driver; 3-state





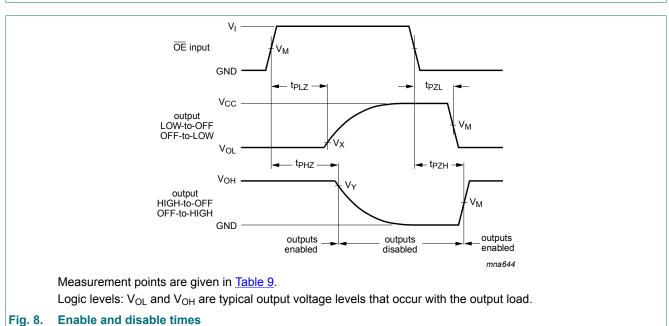


Table 9. Measurement points

Supply voltage	Input			Output		
V _{cc}	V _M	VI	t _r = t _f	V _M	V _X	V _Y
0.8 V to 1.6 V	$0.5 \times V_{CC}$	V _{CC}	≤ 3.0 ns	0.5 × V _{CC}	V _{OL} + 0.1 V	V _{OH} - 0.1 V
1.65 V to 2.7 V	$0.5 \times V_{CC}$	V _{CC}	≤ 3.0 ns	0.5 × V _{CC}	V _{OL} + 0.15 V	V _{OH} - 0.15 V
3.0 V to 3.6 V	$0.5 \times V_{CC}$	V _{CC}	≤ 3.0 ns	0.5 × V _{CC}	V _{OL} + 0.3 V	V _{OH} - 0.3 V

Low-power buffer/line driver; 3-state

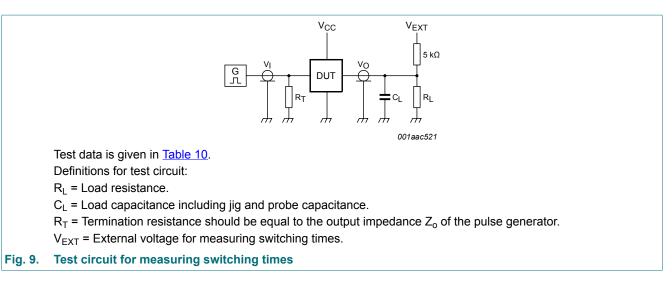


Table 10. Test data

Supply voltage	Load	V _{EXT}			
V _{CC}	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V _{CC}

[1] For measuring enable and disable times $R_L = 5 k\Omega$.

For measuring propagation delays, setup and hold times and pulse width R_L = 1 M Ω .

74AUP1G125_Q100

Low-power buffer/line driver; 3-state

12. Package outline

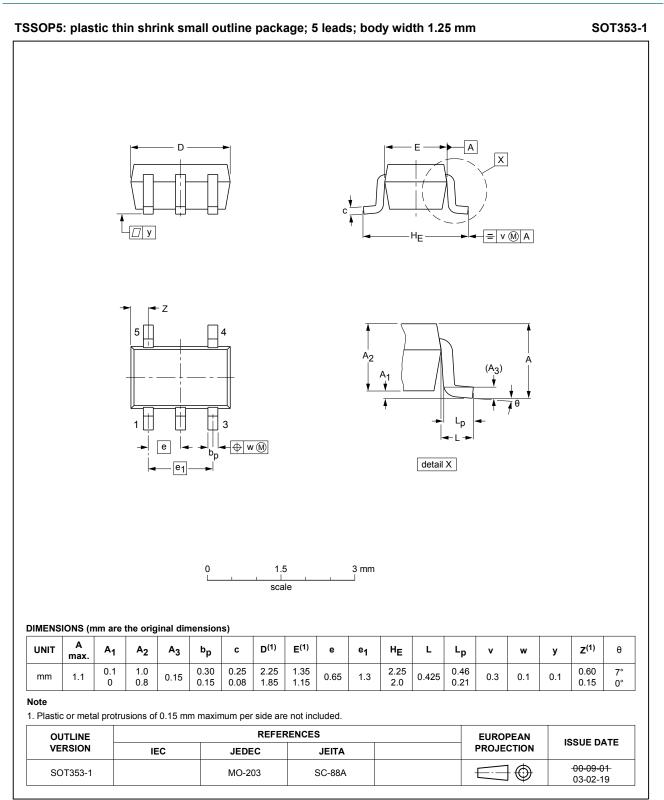


Fig. 10. Package outline SOT353-1 (TSSOP5)

74AUP1G125_Q100

Low-power buffer/line driver; 3-state

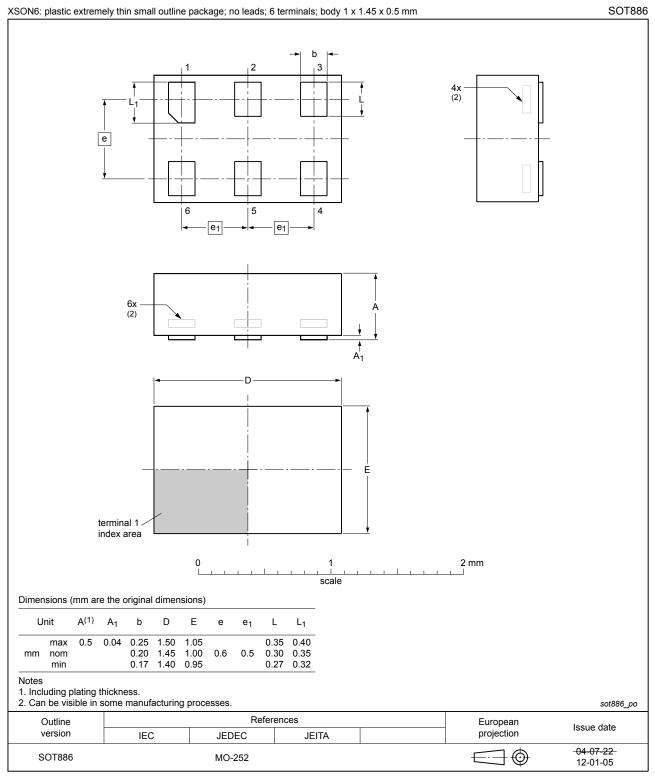


Fig. 11. Package outline SOT886 (XSON6)

SOT1202

Low-power buffer/line driver; 3-state

XSON6: extremely thin small outline package; no leads;	
6 terminals; body 1.0 x 1.0 x 0.35 mm	

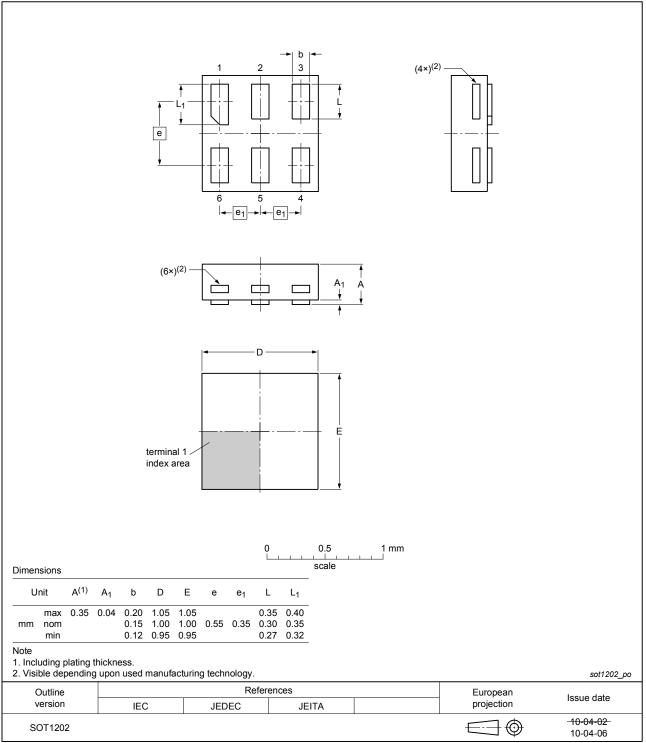


Fig. 12. Package outline SOT1202 (XSON6)

13. Abbreviations

Acronym	Description
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MIL	Military
MM	Machine Model

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AUP1G125_Q100 v.2	20190125	Preliminary data sheet	-	74AUP1G125_Q100 v.1	
Modifications:	of Nexperia. Legal texts have a second sec	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type numbers 74AUP1G125GM-Q100 (SOT886) and 74AUP1G125GS-Q100 (SOT1202) added. 			
74AUP1G125_Q100 v.1	20130320	Product data sheet	-	-	

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use in automotive applications — This Nexperia product has been qualified for use in automotive applications. Unless otherwise agreed in writing, the product is not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or

Low-power buffer/line driver; 3-state

equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <u>http://www.nexperia.com/profile/terms</u>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Contents

1. General description	1
2. Features and benefits	1
3. Ordering information	2
4. Marking	2
5. Functional diagram	2
6. Pinning information	3
6.1. Pinning	3
6.2. Pin description	3
7. Functional description	3
8. Limiting values	4
9. Recommended operating conditions	4
10. Static characteristics	5
11. Dynamic characteristics	8
11.1. Waveforms and test circuit	11
12. Package outline	13
13. Abbreviations	16
14. Revision history	16
15. Legal information	17

© Nexperia B.V. 2019. All rights reserved

For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 25 January 2019



单击下面可查看定价,库存,交付和生命周期等信息

>>Nexperia(安世)