Product data sheet

1 General description

The 74LV04AT is a hex inverter with TTL inputs.

Designed to operate over a V_{CC} range from 4.5 V to 5.5 V, the inputs are TTL compatible, which allows the device to be used to translate from 3.3 V to 5 V.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device is fully specified for partial Power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2 Features and benefits

- · Direct interface with TTL levels
- Supply voltage range from 4.5 V to 5.5 V
- Typical t_{pd} of 3.3 ns at 5 V
- Typical $V_{OL(p)}$ < 0.8 V at V_{CC} = 5 V, T_{amb} = 25 °C
- Typical $V_{OH(v)} > 2.3 \text{ V}$ at $V_{CC} = 5 \text{ V}$, $T_{amb} = 25 ^{\circ}\text{C}$
- Supports mixed-mode voltage operation on all ports
- I_{OFF} circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3 kV
 - MM JESD22-A115-A exceeds 150 V
 - CDM JESD22-C101E exceeds 2 kV
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

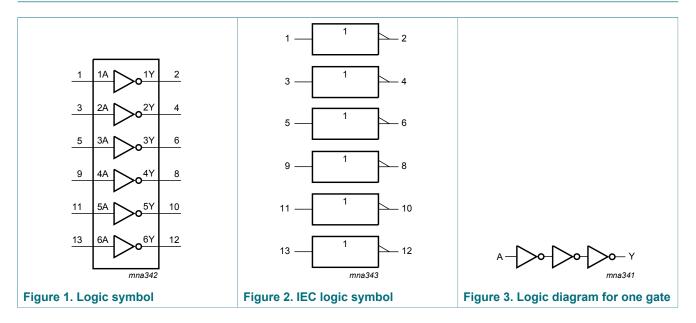
3 Ordering information

Table 1. Ordering information

Type number	Package						
	Temperature range	Name	Description	Version			
74LV04ATPW	-40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1			

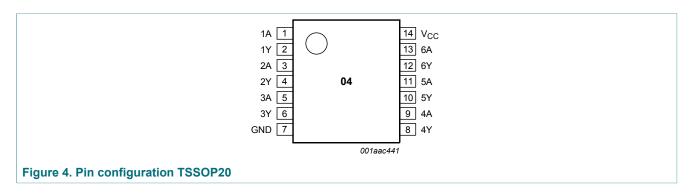


4 Functional diagram



5 Pinning information

5.1 Pinning



5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
1A, 2A, 3A, 4A, 5A, 6A	1, 3, 5, 9, 11, 13	data input
1Y, 2Y, 3Y, 4Y, 5Y, 6Y	2, 4, 6, 8, 10, 12	data output
GND	7	ground (0 V)
V _{CC}	14	supply voltage

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Functional description

Table 3. Function table [1]

Input nA	Output nY
L	Н
Н	L

H = HIGH voltage level; L = LOW voltage level

Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CC}	supply voltage			-0.5	+7.0	V
VI	input voltage		[1]	-0.5	+7.0	V
Vo	output voltage	active mode	[2] [3]	-0.5	V _{CC} + 0.5	V
		power-down or 3-state mode	[2]	-0.5	+7.0	V
I _{IK}	input clamping current	V _I < 0 V		-20	-	mA
I _{OK}	output clamping current	V _O < 0 V		-50	-	mA
Io	output current	$V_O = 0 V \text{ to } V_{CC}$		-	±35	mA
Icc	supply current			-	70	mA
I _{GND}	ground current			-70	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T_{amb} = -40 °C to +125 °C	[4]	-	500	mW

The minimum input voltage ratings may be exceeded if the input current ratings are observed.

Recommended operating conditions 8

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		4.5	5.5	V
VI	input voltage		0	5.5	V
V _O	output voltage	active mode	0	V _{CC}	V
		power-down or 3-state mode	0	5.5	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	-	20	ns/V

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^[2] [3] [4] The output voltage ratings may be exceeded if the output current ratings are observed.

This value is limited to 7.0 V maximum.

For TSSOP14 packages: above 60 °C derate linearly with 5.5 mW/K.

9 Static characteristics

Table 6. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	meter Conditions		25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2	-	-	2	-	2	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	-	8.0	-	0.8	-	0.8	V
V _{OH}	HIGH-level	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = -50 μA	4.4	-	-	4.4	-	4.4	-	V
	Vollago	I _O = -12 mA	3.8	-	-	3.8	-	3.8	-	V
V_{OL}	LOW-level	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = 50 μA	-	-	0.1	-	0.1	-	0.1	V
	Vollago	I _O = 12mA	-	-	0.55	-	0.55	-	0.55	V
I _{OFF}	power-off leakage current	V_1 or V_O = GND to 5.5 V; V_{CC} = 0 V	-	-	0.5	-	5	-	5	μA
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 0 \text{ V to } 5.5 \text{ V}$	-	-	±0.1	-	±1	-	±1	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	2	-	20	-	20	μΑ
ΔI _{CC}	additional supply current	per input pin; $V_I = 3.4 \text{ V}$; other pins at V_{CC} or GND; $I_O = 0 \text{ A}$; $V_{CC} = 5.5 \text{ V}$	-	-	1.35	-	1.5	-	1.5	mA

10 Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V. For test circuit see Figure 6.

Symbol	Parameter	eter Conditions 25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit	
			Min	Typ ^[1]	Max	Min	Max	Min	Max	
t _{pd}	propagation	nA to nY; see Figure 5 [2]								
	delay	V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	3.3	6.3	1	7.3	1	8.1	ns
		C _L = 50 pF	-	5.1	7.7	1	8.8	1	10.4	ns
Cı	input capacitance	$V_I = V_{CC}$ or GND; $V_{CC} = 5 V$	-	2	6	-	6	-	6	pF
СО	output capacitance	$V_O = V_{CC}$ or GND; $V_{CC} = 5 \text{ V}$	-	5	-	-	-	-	-	pF
C _{PD}	power dissipation capacitance	per buffer; C_L = 50 pF; [3] f = 10 MHz; V_I = GND to V_{CC}	-	9	-	-	-	-	-	pF

Typical values are measured at T_{amb} = 25 °C and V_{CC} = 5 V.

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

 f_o = output frequency in MHz;

C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\sum (C_L \times V_{CC}^2 \times f_0)$ = sum of the outputs.

Table 8. Noise characteristics

GND = 0 V. For test circuit see Figure 6.

Symbol Parameter		Conditions	T,	T _{amb} = 25 °C		
			Min	Тур	Max	
V _{CC} = 5	V; C _L = 50 pF			,		
$V_{OL(p)}$	LOW-level output voltage (peak)		-	0.2	0.8	V
$V_{OL(v)}$	LOW-level output voltage (valley)		-0.8	-0.3	-	V
$V_{OH(v)}$	HIGH-level output voltage (valley)		-	4.5	-	V
$V_{IH(AC)}$	AC HIGH-level input voltage (dynamic)		2	-	-	V
V _{IL(AC)}	AC LOW-level input voltage (dynamic)		-	-	0.8	V

t_{pd} is the same as t_{PLH} and t_{PHL}. C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

10.1 Waveforms and test circuit

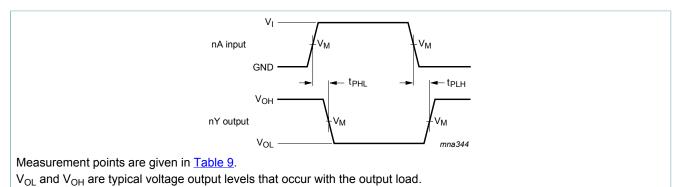
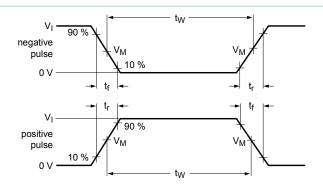
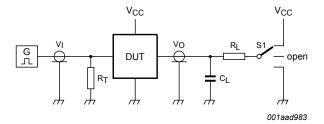


Figure 5. Propagation delay input (An) to output (Yn)

Table 9. Measurement points

Input	Output		
V _M	V _M	V _X	V _Y
1.5 V	0.5V _{CC}	V _{OL} + 0.3 V	V _{OH} - 0.3 V





Test data is given in Table 10.

Definitions test circuit:

 R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator

 C_L = Load capacitance including jig and probe capacitance

R_L = Load resistance

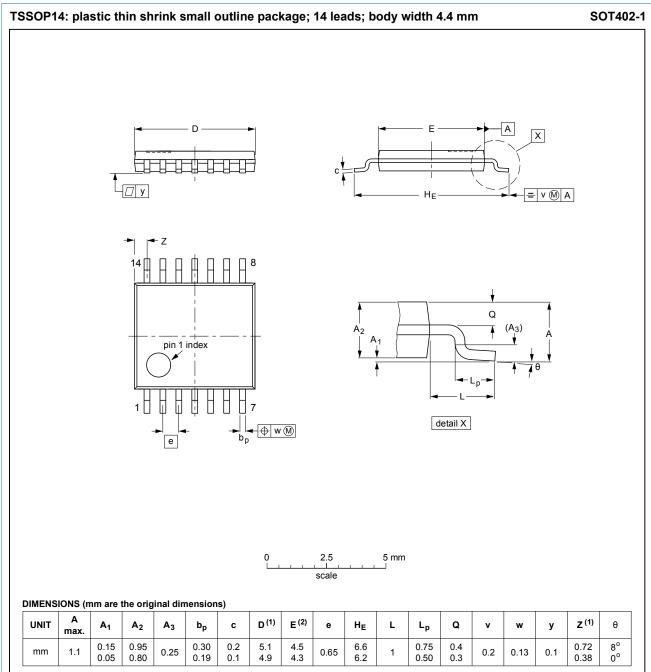
S1 = Test selection switch

Figure 6. Test circuit for measuring switching times

Table 10. Test data

Input		Load		S1 position		
VI	t _r , t _f	C _L R _L		t _{PHL} , t _{PLH} t _{PZH} , t _{PHZ} t _{PZL} , t _{PL}		t _{PZL} , t _{PLZ}
GND to 3.0 V	3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}

11 Package outline



Notes

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFERENCES				ICCUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT402-1		MO-153				-99-12-27 03-02-18	

Figure 7. Package outline SOT402-1 (TSSOP14)

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12 Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charge Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

13 Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74LV04AT v.1	20170518	Product data sheet	-	-

14 Legal information

14.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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