**Product data sheet** 

## 1. General description

NPN/NPN matched double transistor in a SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package. The transistors are fully isolated internally.

PNP/PNP complement: BMC857DS

### 2. Features and benefits

- · Current gain matching
- Base-emitter voltage matching
- Drop-in replacement for standard double transistors
- AEC-Q101 qualified

## 3. Applications

- Current mirror
- · Differential amplifier

### 4. Quick reference data

### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	45	V
I <sub>C</sub>	collector current			-	-	100	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C		200	290	450	
Per device			•		•	·	
h <sub>FE1</sub> /h <sub>FE2</sub>	DC current gain matching	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C	[1]	0.9	1	-	
V <sub>BE1</sub> -V <sub>BE2</sub>	base-emitter voltage matching		[2]	-	-	2	mV

- [1] The smaller of the two values is taken as the numerator.
- [2] The smaller of the two values is subtracted from the larger value.



### NPN/NPN matched double transistor

## 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1		C1 B2 E2
2	B1	base TR1	<u> </u>	
3	C2	collector TR2		(TR1) TR2)
4	E2	emitter TR2		
5	B2	base TR2	SC-74; TSOP6 (SOT457)	
6	C1	collector TR1		sym020

## 6. Ordering information

### **Table 3. Ordering information**

Type number	Package						
	Name	Description	Version				
BCM847DS	SC-74; TSOP6	plastic, surface-mounted package (SC-74; TSOP6); 6 leads	SOT457				

### 7. Marking

### Table 4. Marking codes

Type number	Marking code
BCM847DS	R6

# 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transiste	or		•			
V <sub>CBO</sub>	collector-base voltage	open emitter		-	50	V
$V_{CEO}$	collector-emitter voltage	open base		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1] [2]	-	250	mW
Per device			•	·		
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1] [2]	-	380	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Reflow soldering is the only recommended soldering method.

BCM847DS

### NPN/NPN matched double transistor

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	500	K/W
Per device							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	328	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Reflow soldering is the only recommended soldering method.

### 10. Characteristics

### **Table 7. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	15	nA
	current	$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A}; T_j = 150 ^{\circ}\text{C}$		-	-	5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	<sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 10  \mu\text{A}; T_{amb} = 25 ^{\circ}\text{C}$		-	250	-	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C		200	290	450	
V <sub>CEsat</sub>	collector-emitter	$I_C$ = 10 mA; $I_B$ = 0.5 mA; $T_{amb}$ = 25 °C		-	50	200	mV
	saturation voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA; T <sub>amb</sub> = 25 °C		-	200	400	mV
V <sub>BEsat</sub>	base-emitter saturation	$I_C$ = 10 mA; $I_B$ = 0.5 mA; $T_{amb}$ = 25 °C	[1]	-	760	-	mV
	voltage	$I_C$ = 100 mA; $I_B$ = 5 mA; $T_{amb}$ = 25 °C	[1]	-	910	-	mV
$V_{BE}$	base-emitter voltage	$V_{CE} = 5 \text{ V}; I_{C} = 2 \text{ mA}; T_{amb} = 25 \text{ °C}$	[2]	610	660	710	mV
		$V_{CE} = 5 \text{ V}; I_{C} = 10 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	[2]	-	-	770	mV
C <sub>c</sub>	collector capacitance	$V_{CB}$ = 10 V; $I_{E}$ = 0 A; $i_{e}$ = 0 A; f = 1 MHz; $T_{amb}$ = 25 °C		-	-	1.5	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_{C} = 0 \text{ A}; i_{c} = 0 \text{ A};$ f = 1 MHz; $T_{amb} = 25 \text{ °C}$		-	11	-	pF
f <sub>T</sub>	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 10 \text{ mA}; f = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$		100	250	-	MHz
NF	noise figure	$V_{CE}$ = 5 V; $I_{C}$ = 0.2 mA; $R_{S}$ = 2 k $\Omega$ ; f = 10 Hz to 15.7 kHz; $T_{amb}$ = 25 °C		-	2.8	-	dB
		$V_{CE}$ = 5 V; $I_{C}$ = 0.2 mA; $R_{S}$ = 2 k $\Omega$ ; f = 1 kHz; B = 200 Hz; $T_{amb}$ = 25 °C		-	3.3	-	dB
Per device						,	
h <sub>FE1</sub> /h <sub>FE2</sub>	DC current gain matching	$V_{CE} = 5 \text{ V}; I_{C} = 2 \text{ mA}; T_{amb} = 25 \text{ °C}$	[3]	0.9	1	-	
V <sub>BE1</sub> -V <sub>BE2</sub>	base-emitter voltage matching		[4]	-	-	2	mV

- [1]  $V_{BEsat}$  decreases by about 1.7 mV/K with increasing temperature.
- [2] V<sub>BE</sub> decreases by about 2 mV/K with increasing temperature.
- [3] The smaller of the two values is taken as the numerator.
- [4] The smaller of the two values is subtracted from the larger value.

BCM847DS

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### NPN/NPN matched double transistor

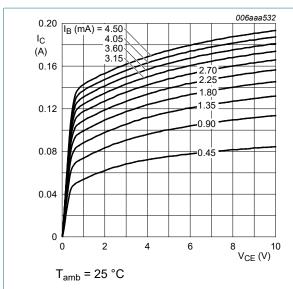
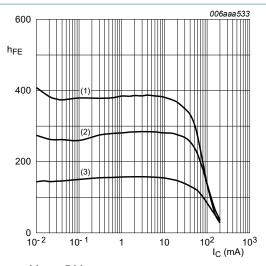


Fig. 1. Collector current as a function of collectoremitter voltage; typical values



V<sub>CE</sub> = 5 V (1) T<sub>amb</sub> = 100 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = -55 °C

Fig. 2. DC current gain as a function of collector current; typical values

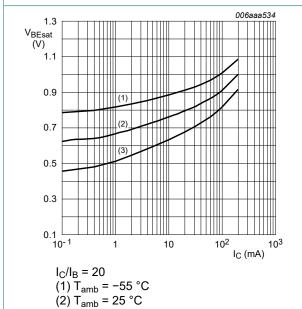
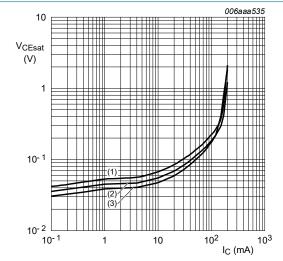


Fig. 3. Base-emitter saturation voltage as a function of Fig. 4. collector current; typical values

(3)  $T_{amb} = 100 \, ^{\circ}C$ 



 $I_{\rm C}/I_{\rm B} = 20$ (1)  $T_{amb}$  = 100 °C (2)  $T_{amb} = 25 \, ^{\circ}C$ (3)  $T_{amb} = -55 \, ^{\circ}C$ 

Collector-emitter saturation voltage as a function of collector current; typical values

### NPN/NPN matched double transistor

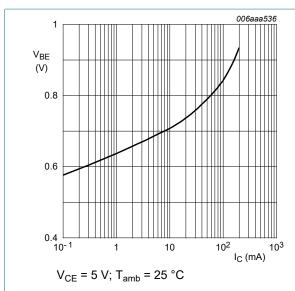


Fig. 5. Base-emitter voltage as a function of collector current; typical values

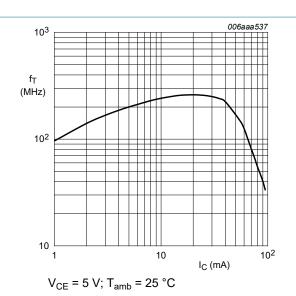


Fig. 6. Transition frequency as a function of collector current; typical values

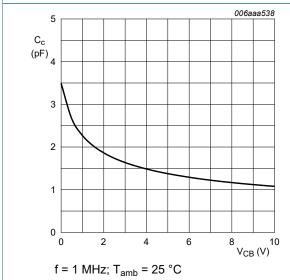
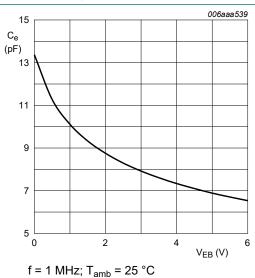
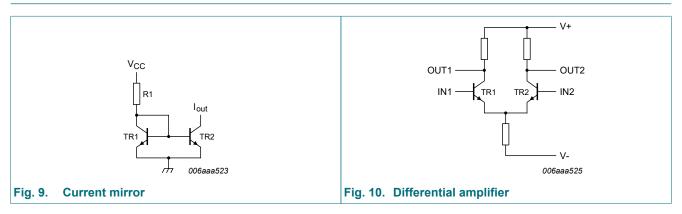


Fig. 7. Collector capacitance as a function of collectorbase voltage; typical values



8. Emitter capacitance as a function of emitterbase voltage; typical values

## 11. Application information



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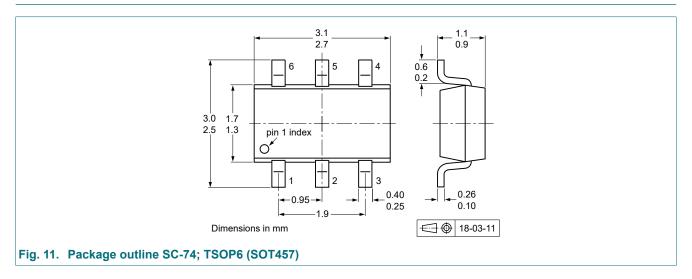
### NPN/NPN matched double transistor

### 12. Test information

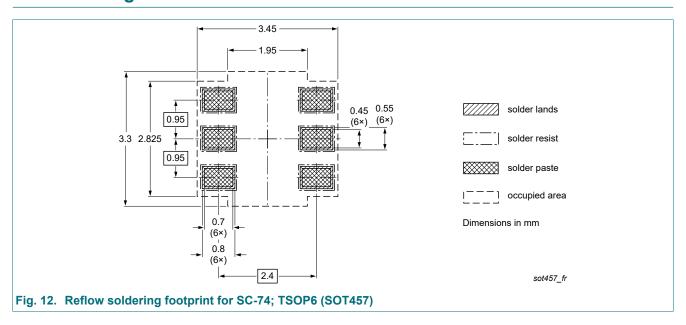
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

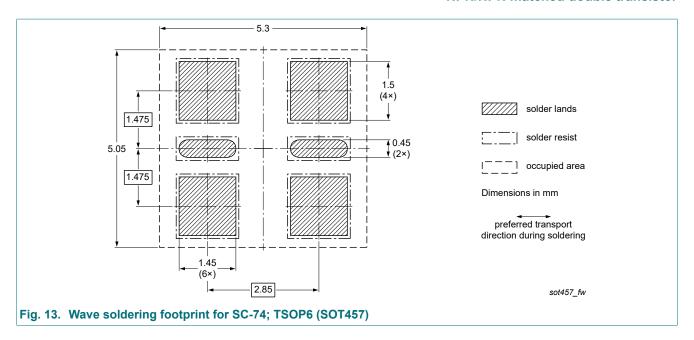
# 13. Package outline



### 14. Soldering



### NPN/NPN matched double transistor



### **NPN/NPN** matched double transistor

# 15. Revision history

### Table 8. Revision history

Table 6. Revision misto	ı y			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCM847DS v.7	20221227	Product data sheet	-	BCM847BV_BS_DS_6
Modifications:	<ul><li>Family data sheet sp</li><li>Packing information</li></ul>	olitted to single type data removed.	sheets.	
BCM847BV_BS_DS_6		Product data sheet	-	BCM847BV_BS_DS_5
BCM847BV_BS_DS_5		Product data sheet Product data sheet	-	BCM847BS_DS_4
BCM847BS_DS_4		Product data sheet	-	BCM847BS_DS_3
BCM847BS_DS_3		Product data sheet	-	BCM847BS_2
BCM847BS_2		Product data sheet	-	BCM847BS_1
BCM847BS_1		Product data sheet	-	-

#### NPN/NPN matched double transistor

### 16. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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### **NPN/NPN** matched double transistor

## **Contents**

1.	General description	. 1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	. 1
5.	Pinning information	. 2
6.	Ordering information	. 2
7.	Marking	. 2
8.	Limiting values	2
9.	Thermal characteristics	3
10.	Characteristics	. 3
11.	Application information	. 5
12.	Test information	. 6
13.	Package outline	6
14.	Soldering	6
15.	Revision history	.8
16.	Legal information	.9

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