Product data sheet

1. General description

PNP general-purpose transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low current (max. 100 mA)
- Low voltage (max. 45 V)
- AEC-Q101 qualified

3. Applications

· General purpose switching and amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-45	V
I _C	collector current		-	-	-100	mA
h _{FE}	DC current gain	$V_{CE} = -5 \text{ V}; I_{C} = -2 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	120	-	260	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	E	emitter		C
3	С	collector		В—
			1 2	
			SOT23	·

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BCW69	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23



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7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BCW69	H1%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-50	V
V _{CEO}	collector-emitter voltage	open base		-	-45	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-100	mA
I _{CM}	peak collector current			-	-200	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

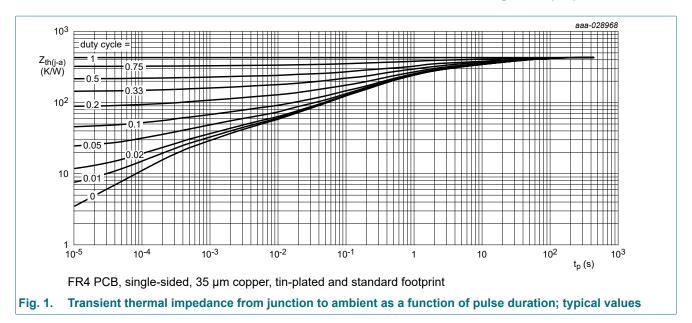
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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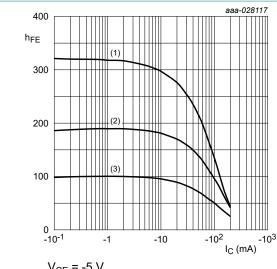
10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	M	lin	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -20 V; I _E = 0 A; T _{amb} = 25 °C	-		-	-100	nA
	current	V _{CB} = -20 V; I _E = 0 A; T _{amb} = 100 °C	-		-	-10	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-		-	-100	nA
h _{FE}	DC current gain	V_{CE} = -5 V; I_{C} = -10 μ A; T_{amb} = 25 °C	-		90	-	
		V_{CE} = -5 V; I_{C} = -2 mA; T_{amb} = 25 °C	1:	20	-	260	
V _{CEsat}	collector-emitter saturation voltage	I_C = -10 mA; I_B = -0.5 mA; T_{amb} = 25 °C	-		-80	-300	mV
		I_C = -50 mA; I_B = -2.5 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-		-150	-	mV
V _{BEsat}	base-emitter saturation voltage	I_C = -10 mA; I_B = -0.5 mA; T_{amb} = 25 °C	-		-720	-	mV
		I_C = -50 mA; I_B = -2.5 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-		-810	-	mV
V_{BE}	base-emitter voltage	V_{CE} = -5 V; I_{C} = -2 mA; T_{amb} = 25 °C	-6	600	-	-750	mV
C _c	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A};$ f = 1 MHz; $T_{amb} = 25 \text{ °C}$	-		4.5	-	pF
f _T	transition frequency	V_{CE} = -5 V; I_{C} = -10 mA; f = 100 MHz; T_{amb} = 25 °C	10	00	-	-	MHz
NF	noise figure	V_{CE} = -5 V; I_{C} = -200 μ A; R_{S} = 2 $k\Omega$; f = 1 k Hz; B = 200 Hz; T_{amb} = 25 $^{\circ}$ C	-		-	10	dB

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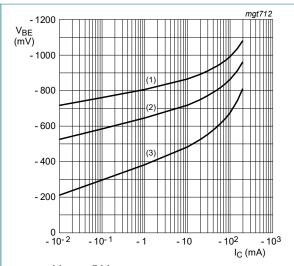


 V_{CE} = -5 V $(1) T_{amb} = 150 °C$

 $(2) T_{amb} = 25 °C$

(3) $T_{amb} = -55 \, ^{\circ}C$

Fig. 2. DC current gain as a function of collector current; typical values

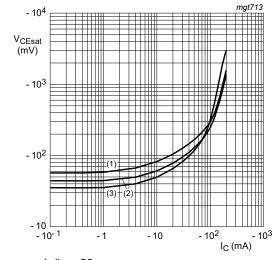


 $V_{CE} = -5 V$ (1) $T_{amb} = -55 °C$

 $(2) T_{amb} = 25 °C$

(3) $T_{amb} = 150 \, ^{\circ}C$

Base-emitter voltage as a function of collector Fig. 3. current; typical values



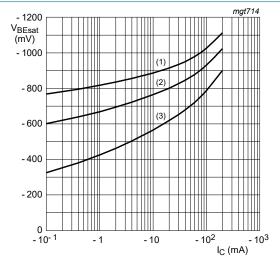
 $I_{\rm C}/I_{\rm B}=20$

(1) $T_{amb} = 150 \, ^{\circ}C$

(2) T_{amb} = 25 °C

(3) T_{amb} = -55 °C

Collector-emitter saturation voltage as a Fig. 4. function of collector current; typical values



 $I_{\rm C}/I_{\rm B} = 20$

(1) $T_{amb} = -55 \, ^{\circ}C$

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) T_{amb} = 150 °C

Fig. 5. Base-emitter saturation voltage as a function of collector current; typical values

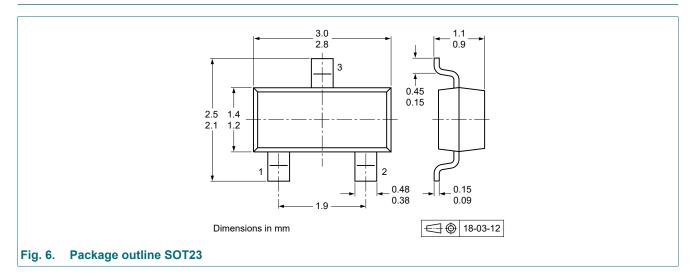
11. Test information

Quality information

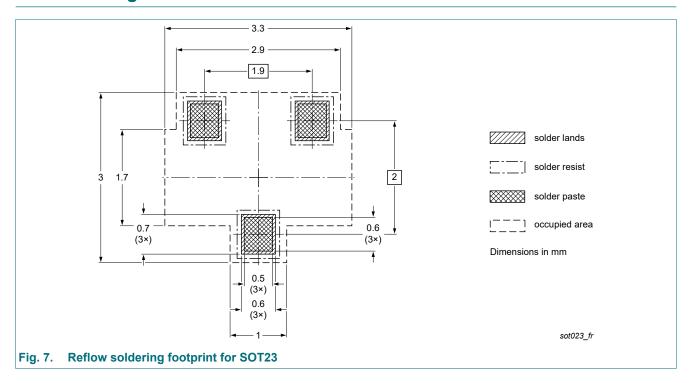
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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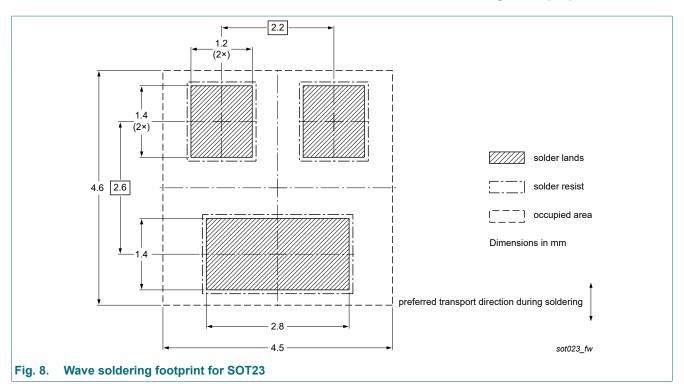
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history

Table 6: Itevicion inc				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCW69 v.3	20241030	Product data sheet	-	BCW69_BCW70 v.2
Modifications:	The format of the Nexperia.	neet splitted to single type dat this data sheet has been rede ve been adapted to the new o dded.	esigned to comply with	
BCW69_BCW70 v.2	20040206	Product data sheet	-	BCW69_BCW70 v.1
BCW69_BCW70 v.1	19990419	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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