

# PMBTA06-Q

NPN general purpose transistor 5 July 2021

### 1. General description

NPN general-purpose transistor encapsulated in a small SOT23 Surface-Mounted Device (SMD) plastic package.

PNP complement: PMBTA56-Q

### 2. Features and benefits

- High current (max. 500 mA)
- Low voltage (max. 80 V)
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

• General purpose switching and amplification in e.g. telephony and professional communication equipment.

### 4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	80	V
I <sub>C</sub>	collector current			-	-	500	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = 25 °C		100	-	-	



## 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	C
2	E	emitter		J
3	С	collector		в-К
			1 2 SOT23	sym021

## 6. Ordering information

### Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMBTA06-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		

## 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
PMBTA06-Q	%1G

[1] % = placeholder for manufacturing site code

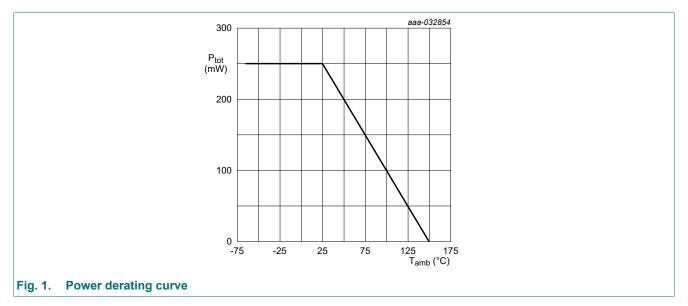
## 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	80	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	4	V
I <sub>C</sub>	collector current			-	500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	1	А
I <sub>BM</sub>	peak base current	-		-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

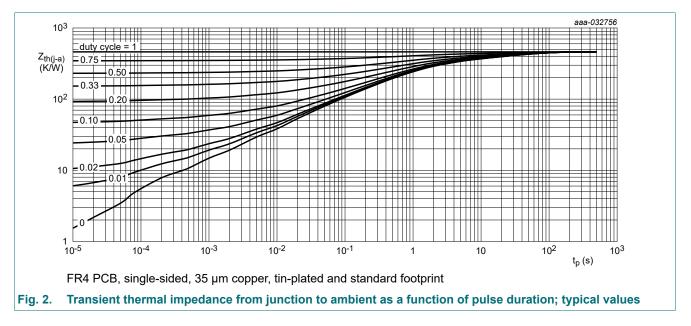
[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.



## 9. Thermal characteristics

Table 6. Therma	al characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

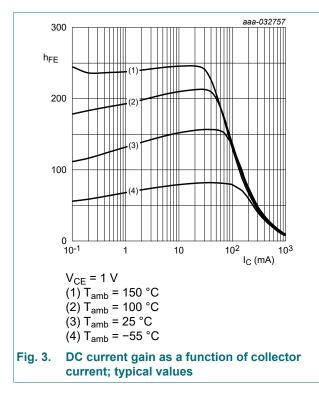


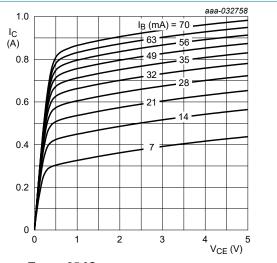
### **10. Characteristics**

### **Table 7. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	80	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	$I_{C}$ = 2 mA; $I_{B}$ = 0 A; $T_{amb}$ = 25 °C	80	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage (collector open)	I <sub>E</sub> = 0 A; I <sub>C</sub> = 100 μA; T <sub>amb</sub> = 25 °C	4	-	-	V
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 80 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	50	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = 25 °C	100	-	-	
		$V_{CE}$ = 1 V; I <sub>C</sub> = 100 mA; T <sub>amb</sub> = 25 °C	100	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 10 mA; T <sub>amb</sub> = 25 °C	-	-	0.25	V
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA; T <sub>amb</sub> = 25 °C	-	-	1.2	V
f <sub>T</sub>	transition frequency	$V_{CE} = 2 \text{ V}; \text{ I}_{C} = 10 \text{ mA}; \text{ f} = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	100	-	-	MHz



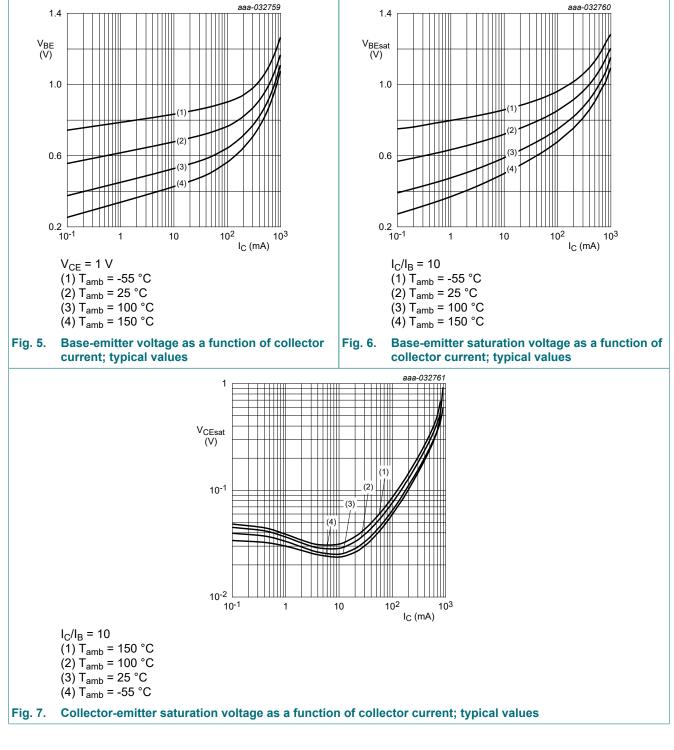






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### 11. Test information

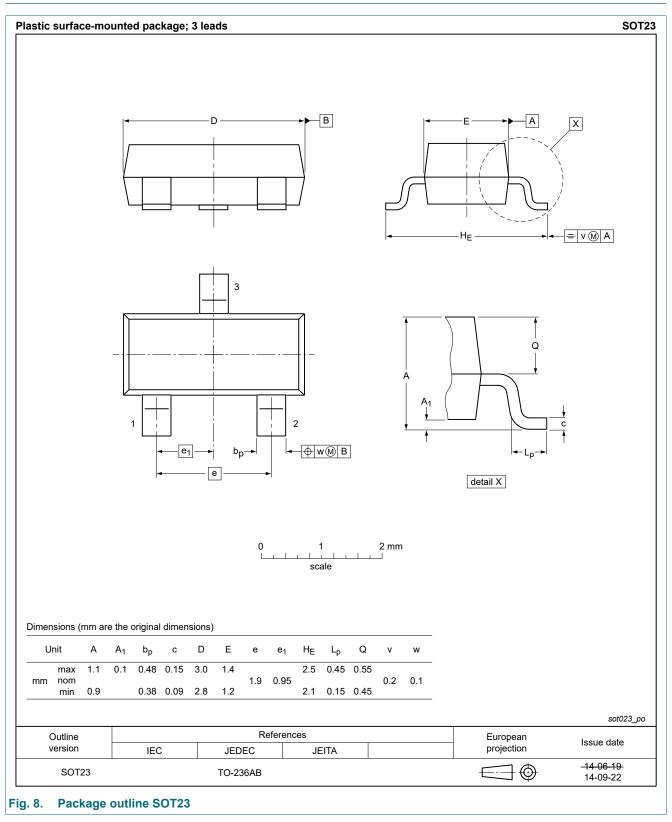
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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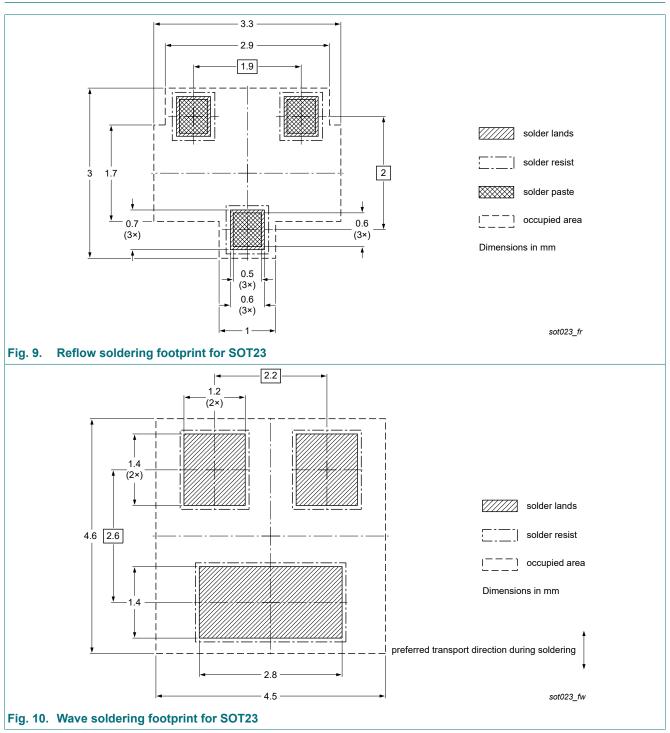
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### 12. Package outline



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## 13. Soldering



## 14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMBTA06-Q v.1	20210618	Product data sheet	-	-			

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### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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