**Product data sheet** 

## 1. General description

PNP high-voltage transistor in a SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Low current (max. 100 mA)
- High voltage (max. 100 V)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

- High-voltage general purpose
- Switching applications

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-100	V
I <sub>C</sub>	collector current		-	-	-100	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -1 V; $I_{C}$ = -10 mA; $T_{amb}$ = 25 °C	30	-	-	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	_
2	E	emitter		C
3	С	collector		В—
			12	   E   sym132
			SOT23	



### PNP high-voltage transistor

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
BSS63-Q		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		

### 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
BSS63-Q	BM%

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-110	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-100	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-6	V
I <sub>C</sub>	collector current			-	-100	mA
I <sub>CM</sub>	peak collector current			-	-100	mA
I <sub>BM</sub>	peak base current			-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

### 9. Thermal characteristics

### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

**PNP** high-voltage transistor

### 10. Characteristics

**Table 7. Characteristics** 

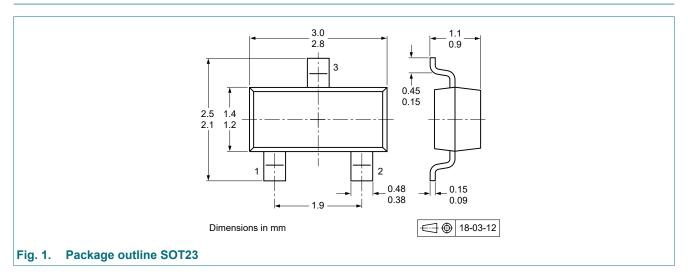
Symbol	Parameter	Conditions	N	/lin	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = -90 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-		-	-100	nA
	current	V <sub>CB</sub> = -90 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-		-	-50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -6 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-		-	-100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C	3	0	-	-	
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -25 mA; T <sub>amb</sub> = 25 °C	3	0	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C$ = -25 mA; $I_B$ = -2.5 mA; $T_{amb}$ = 25 °C	-		-	-250	mV
V <sub>BEsat</sub>	base-emitter saturation voltage		-		-	-900	mV
f <sub>T</sub>	transition frequency	$V_{CE}$ = -5 V; $I_{C}$ = -25 mA; f = 100 MHz; $T_{amb}$ = 25 °C	5	60	85	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-		3	-	pF

### 11. Test information

### **Quality information**

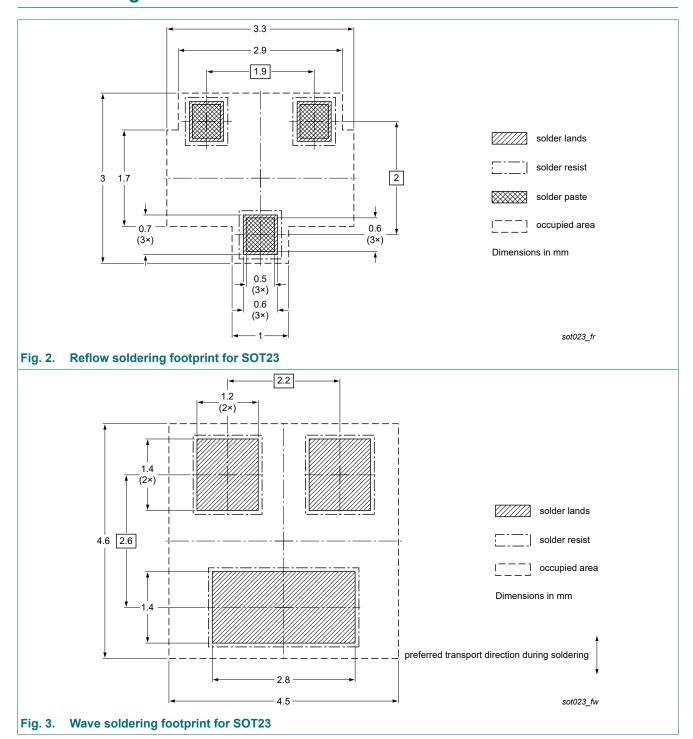
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline



### PNP high-voltage transistor

# 13. Soldering



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# 14. Revision history

### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BSS63-Q v.1	20220610	Product data sheet	-	-

### PNP high-voltage transistor

### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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