

40 V, 1 A NPN low VCEsat transistor

1 October 2024

1. General description

NPN low V_{CEsat} transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package. PNP complement: PBSS5140T

2. Features and benefits

- Low collector-emitter saturation voltage
- High current capabilities
- · Improved device reliability due to reduced heat generation

3. Applications

- · General purpose switching and muting
- LCD backlighting
- Supply line switching circuits
- · Battery driven equipment (mobile phones, video cameras and hand-held devices)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	40	V
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	2	А
R _{CEsat}	collector-emitter saturation resistance	I_C = 500 mA; I_B = 50 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	260	500	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	С
2	E	emitter		Ŭ J
3	С	collector		в
				E
			1 L 2 SOT23	sym123

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6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PBSS4140T	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		

7. Marking

Table 4. Marking codes					
Type number	Marking code[1]				
PBSS4140T	ZT%				

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	40	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	2	А
I _{BM}	peak base current			-	1	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
			[2]	-	450	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
(1) (u)	thermal resistance from	in free air	[1]	-	-	417	K/W
	junction to ambient		[2]	-	-	278	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

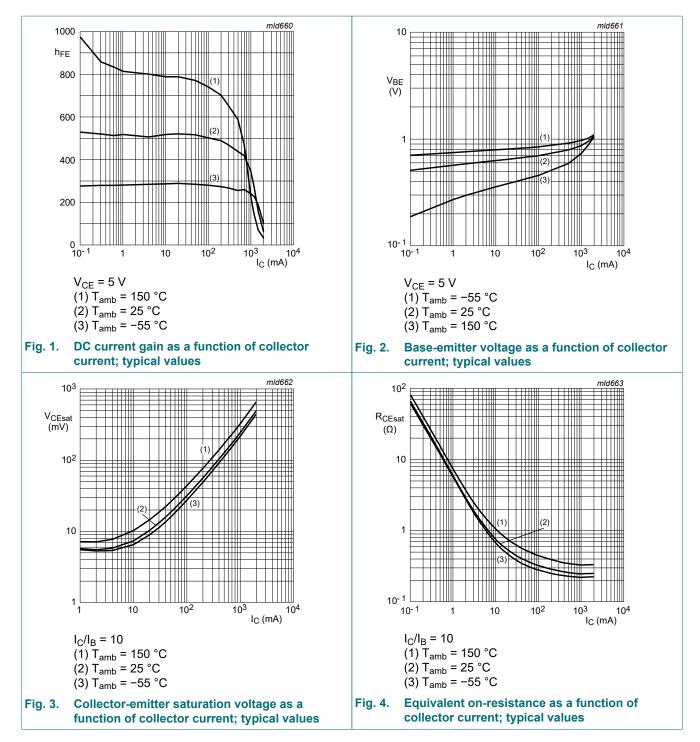
[2] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 1 cm².

40 V, 1 A NPN low VCEsat transistor

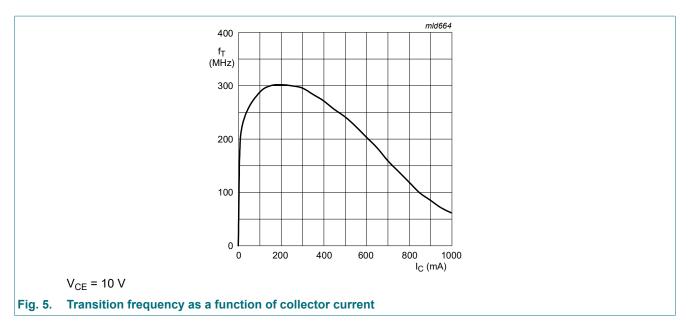
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A; T _{amb} = 25 °C	40	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _B = 0 A; T _{amb} = 25 °C	40	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I_E = 100 µA; I_C = 0 A; T_{amb} = 25 °C	5	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = 40 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
	current	V _{CB} = 40 V; I _E = 0 A; T _{amb} = 150 °C	-	-	50	μA
I _{CES}	collector-emitter cut-off current	V _{CE} = 30 V; T _{amb} = 25 °C	-	-	100	nA
I _{CEO}	collector-emitter cut-off current (base open)	I _B = 0 A; V _{CE} = 30 V; T _{amb} = 25 °C	-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V_{CE} = 5 V; I _C = 1 mA; T _{amb} = 25 °C	300	-	-	
		V_{CE} = 5 V; I _C = 500 mA; T _{amb} = 25 °C	300	-	900	
		V _{CE} = 5 V; I _C = 1 A; T _{amb} = 25 °C	200	-	-	
OLSAI	collector-emitter	I_{C} = 100 mA; I_{B} = 1 mA; T_{amb} = 25 °C	-	-	200	mV
	saturation voltage	I_{C} = 500 mA; I_{B} = 50 mA; T_{amb} = 25 °C	-	-	250	mV
		I _C = 1 A; I _B = 100 mA; T _{amb} = 25 °C	-	-	500	mV
R _{CEsat}	collector-emitter saturation resistance	I_C = 500 mA; I_B = 50 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	260	500	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = 1 A; I _B = 100 mA; T _{amb} = 25 °C	-	-	1.2	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = 5 V; I _C = 1 A; T _{amb} = 25 °C	-	-	1.1	V
f _T	transition frequency	V _{CE} = 10 V; I _C = 50 mA; f = 100 MHz; T _{amb} = 25 °C	150	-	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	10	pF

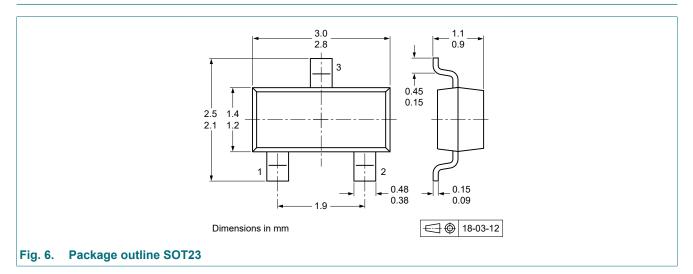
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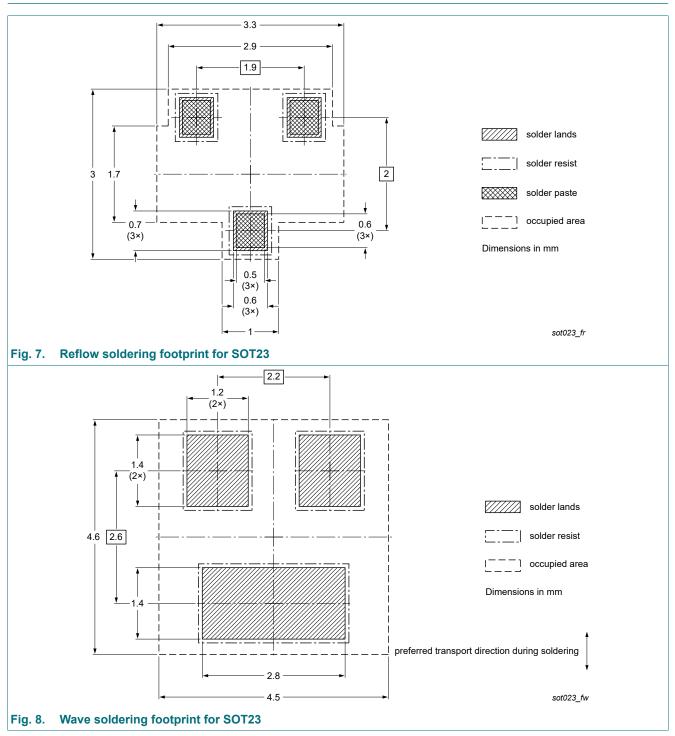


11. Package outline



40 V, 1 A NPN low VCEsat transistor

12. Soldering



13. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4140T v.4	20241001	Product data sheet	-	PBSS4140T v.3
Modifications:	Limiting values:	I _{BM} unit corrected	·	·
PBSS4140T v.3	20230401	Product data sheet	-	PBSS4140T v.2
PBSS4140T v.2	20050224	Product data sheet	-	PBSS4140T v.1
PBSS4140T v.1	20050214	Product specification	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or [1] completing a design.

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40 V, 1 A NPN low VCEsat transistor

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1 October 2024

40 V, 1 A NPN low VCEsat transistor

Contents

1.	General description	. 1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	1
5.	Pinning information	1
6.	Ordering information	2
7.	Marking	. 2
8.	Limiting values	. 2
9.	Thermal characteristics	. 2
10.	Characteristics	. 3
11.	Package outline	. 5
	Soldering	
	Revision history	
	Legal information	
	-	

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