

600 V, 0.1 A PNP high-voltage low VCEsat transistor

9 October 2024

Product data sheet

## 1. General description

PNP high-voltage low V<sub>CEsat</sub> transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- High voltage
- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability I<sub>C</sub>
- High collector current gain h<sub>FE</sub> at high I<sub>C</sub>

## 3. Applications

- Electronic ballast for fluorescent lighting
- LED driver for LED chain module
- LCD backlighting
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

## 4. Quick reference data

### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-600	V
I <sub>C</sub>	collector current		-	-	-0.1	А
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -10 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C	70	130	-	

## 5. Pinning information

#### Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	С
2	С	collector		в
3	E	emitter		□ □ □ ¯ hr
4	С	collector		Ė
			SC-73 (SOT223)	sym028



## 6. Ordering information

Table 3. Ordering information					
Type number Package					
	Name	Description	Version		
PBHV3160Z		plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	<u>SOT223</u>		

## 7. Marking

Table 4. Marking codes					
Type number	Marking code				
PBHV3160Z	HV316Z				

# 8. Limiting values

### Table 5. Limiting values

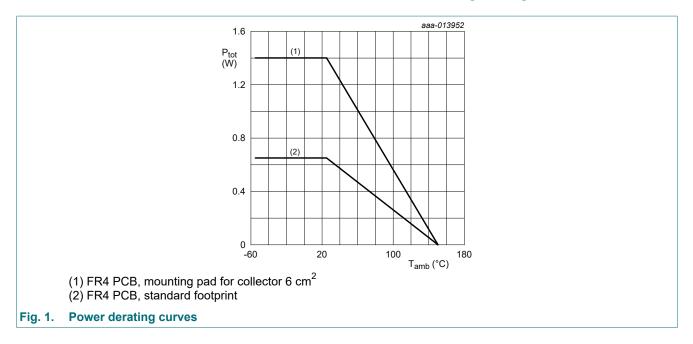
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-600	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-600	V
V <sub>CESM</sub>	collector-emitter peak voltage	V <sub>BE</sub> = 0 V		-	-600	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-6	V
l <sub>C</sub>	collector current			-	-0.1	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.4	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

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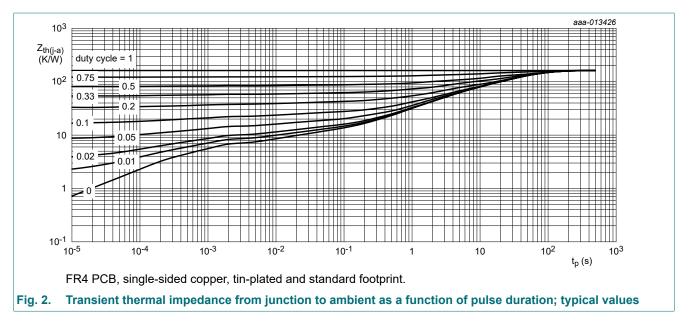


## 9. Thermal characteristics

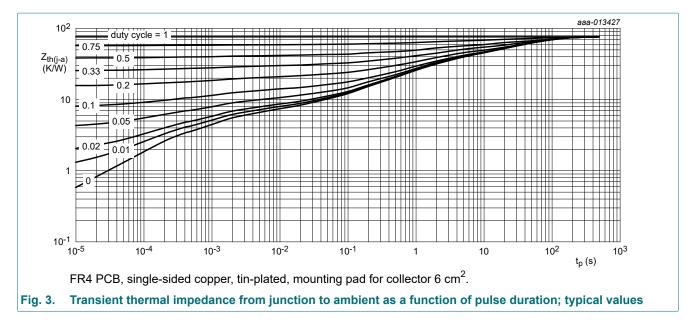
Table 6. The	ermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance from		[1]	-	-	190	K/W
	junction to ambient		[2]	-	-	89	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	20	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.



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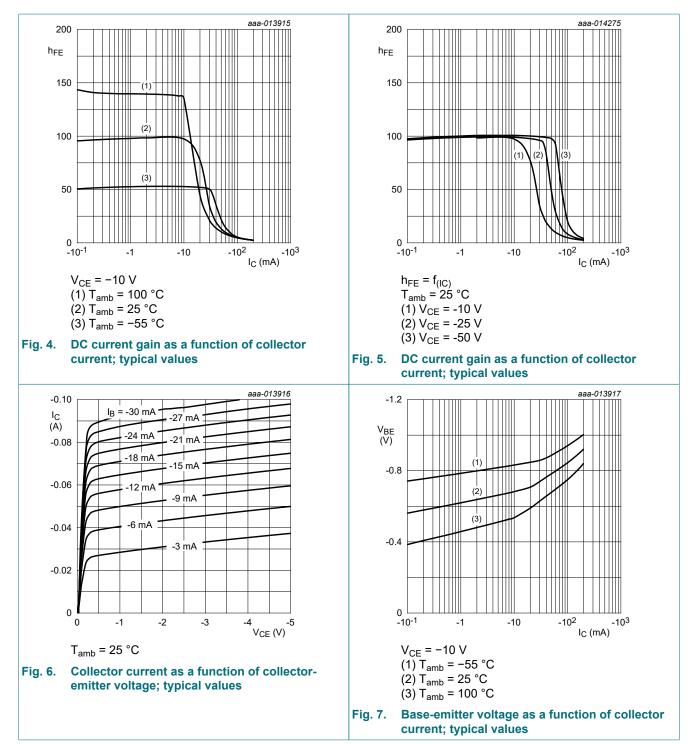


## **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = -400 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
с	current	V <sub>CB</sub> = -400 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	-10	μA
I <sub>CES</sub>	collector-emitter cut-off current	$V_{CE}$ = -400 V; $V_{BE}$ = 0 V; $T_{amb}$ = 25 °C	-	-	-100	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -10 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C	70	130	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -30 mA; I <sub>B</sub> = -6 mA; T <sub>amb</sub> = 25 °C	-	-150	-250	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{C}$ = -50 mA; $I_{B}$ = -5 mA; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	-950	mV
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -10 V; I <sub>C</sub> = -5 mA; f = 100 MHz	-	38	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	6	-	pF
C <sub>e</sub>	emitter capacitance	V <sub>EB</sub> = -0.5 V; I <sub>C</sub> = 0 A; i <sub>c</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	76	-	pF

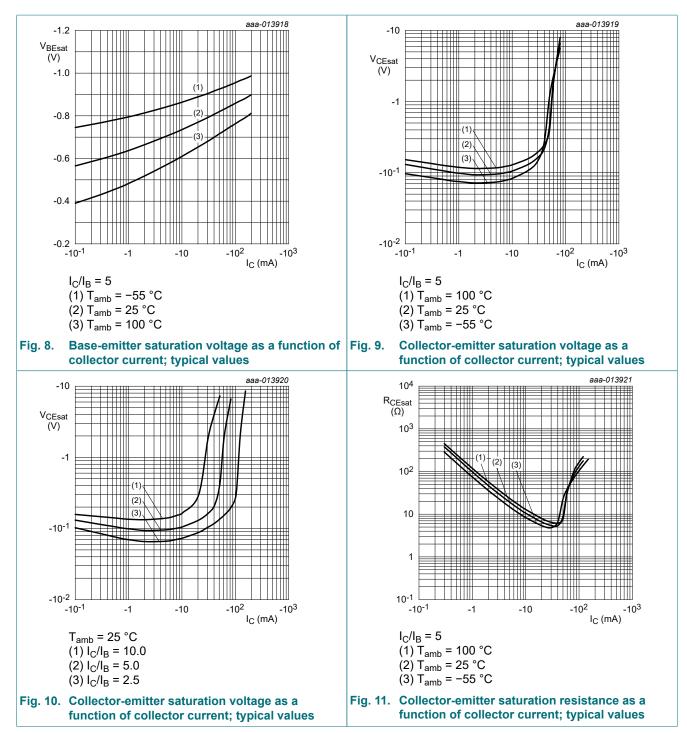
#### PBHV3160Z

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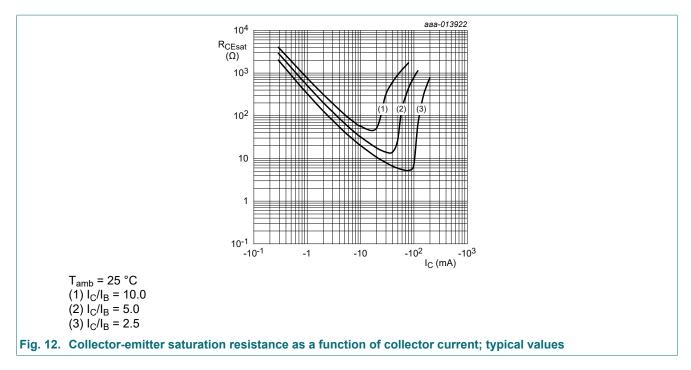


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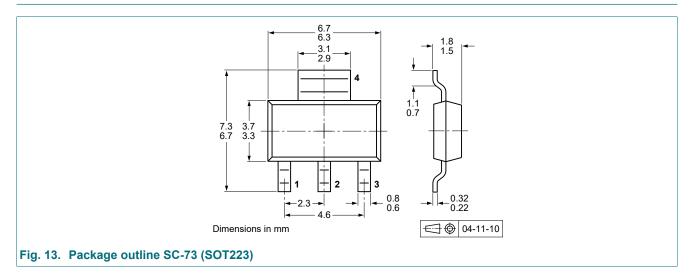
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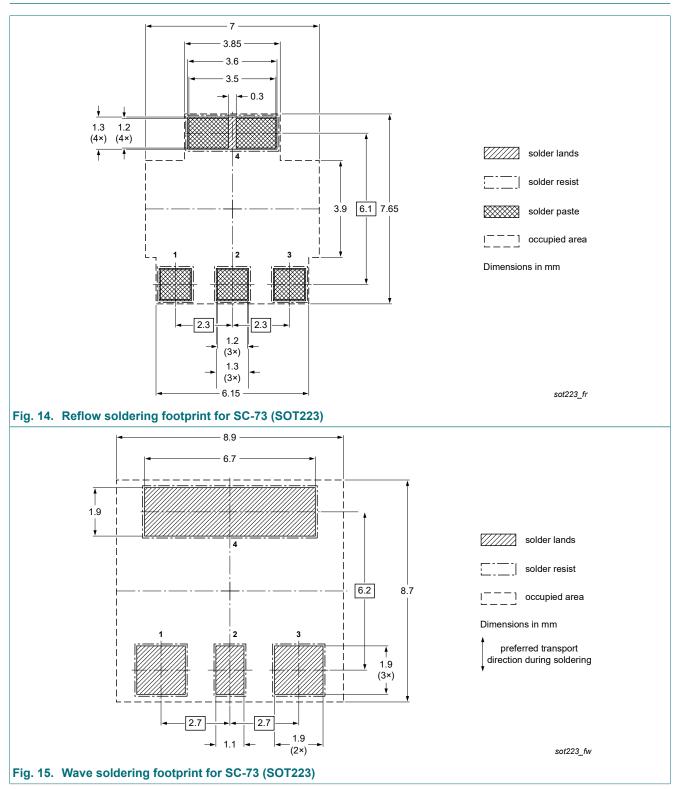


### 11. Package outline



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## 12. Soldering



### 600 V, 0.1 A PNP high-voltage low VCEsat transistor

# 13. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PBHV3160Z v.2	20241009	Product data sheet	-	PBHV3160Z v.1			
Modifications:		<ul> <li>Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).</li> </ul>					
PBHV3160Z v.1	20140818	Product data sheet	-	-			

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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