

PBSS4240T-Q

40 V; 2 A NPN low VCEsat transistor

13 May 2022

1. General description

NPN low $V_{\mbox{CEsat}}$ transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS5240T-Q

2. Features and benefits

- Low collector-emitter saturation voltage
- High current capability
- Improved device reliability due to reduced heat generation
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Supply line switching circuits
- Battery management applications
- DC/DC converter applications
- Strobe flash units
- Heavy duty battery powered equipment (motor and lamp drivers)

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	40	V
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	3	A
R _{CEsat}	collector-emitter saturation resistance	I_C = 500 mA; I_B = 50 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C		-	140	200	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	C
2	E	emitter		
3	С	collector		в
				е Б sym123

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6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PBSS4240T-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<u>SOT23</u>			

7. Marking

Table 4. Marking codes						
Type number	Marking code[1]					
PBSS4240T-Q	ZE%					

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	40	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	2	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	3	А
I _{BM}	peak base current			-	300	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
			[2]	-	480	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1]	-	-	417	K/W
	junction to ambient		[2]	-	-	260	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

10. Characteristics

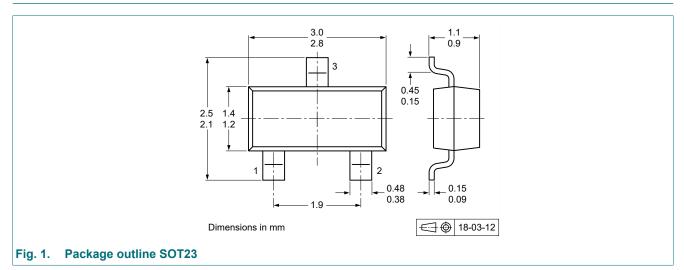
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	50	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 100 mA; T _{amb} = 25 °C	350	470	-	
		V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C	300	450	-	
		V _{CE} = 2 V; I _C = 1 A; T _{amb} = 25 °C	300	420	-	
		V _{CE} = 2 V; I _C = 2 A; T _{amb} = 25 °C	150	250	-	
V _{CEsat}	collector-emitter saturation voltage	I_{C} = 100 mA; I_{B} = 1 mA; T_{amb} = 25 °C	-	45	70	mV
		I_{C} = 500 mA; I_{B} = 50 mA; T_{amb} = 25 °C	-	70	100	mV
		I _C = 750 mA; I _B = 15 mA; T _{amb} = 25 °C	-	120	180	mV
		I_{C} = 1 A; I_{B} = 50 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	130	180	mV
		I_{C} = 2 A; I_{B} = 200 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	240	320	mV
R _{CEsat}	collector-emitter saturation resistance	I_{C} = 500 mA; I_{B} = 50 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	140	200	mΩ
V _{BEsat}	base-emitter saturation voltage	I_{C} = 2 A; I_{B} = 200 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	1.1	V
V _{BEon}	base-emitter turn-on voltage	V_{CE} = 2 V; I _C = 100 mA; T _{amb} = 25 °C	-	-	0.75	V
f _T	transition frequency	V _{CE} = 10 V; I _C = 100 mA; f = 100 MHz; T _{amb} = 25 °C	100	230	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	15	20	pF

11. Test information

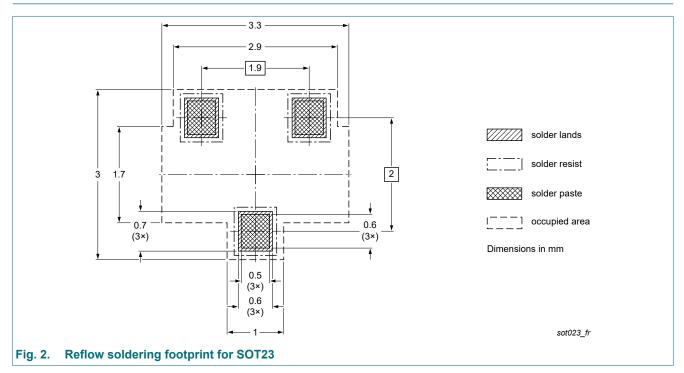
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

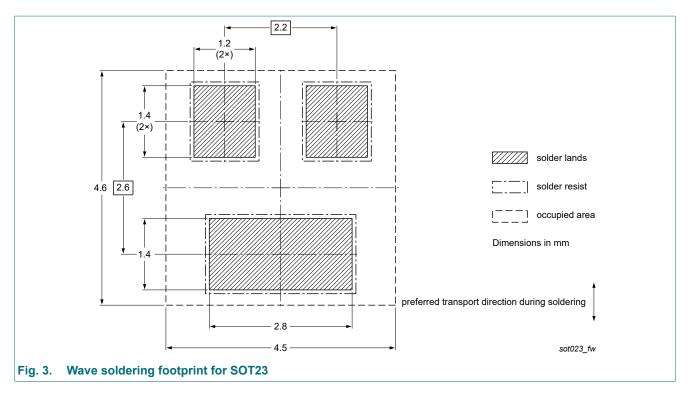


13. Soldering



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Product data sheet

14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PBSS4240T-Q v.1	20220513	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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