

# PDTC123YT-Q

NPN resistor-equipped transistor;  $R1 = 2.2 \text{ k}\Omega$ ,  $R2 = 10 \text{ k}\Omega$ 17 April 2023Product data sheet

### 1. General description

NPN Resistor-Equipped Transistor (RET) in a small SOT23 Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Built-in bias resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

- General-purpose switching and amplification
- Inverter and interface circuits
- Circuit drivers

### 4. Quick reference data

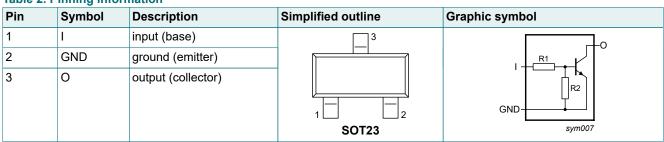
#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	50	V
I <sub>O</sub>	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	1.54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		[1]	3.6	4.5	5.5	

[1] See "Section 11: Test information" for resistor calculation and test conditions.

### 5. Pinning information

### Table 2. Pinning information





### 6. Ordering information

Table 3. Ordering information						
Type number Package						
	Name	Description	Version			
PDTC123YT-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<u>SOT23</u>			

### 7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
PDTC123YT-Q	%AL

[1] % = placeholder for manufacturing site code

### 8. Limiting values

#### Table 5. Limiting values

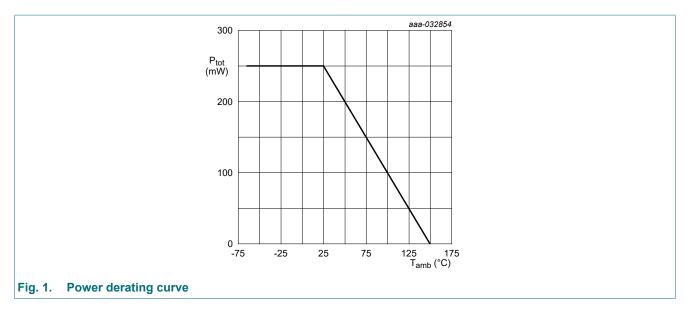
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
VI	input voltage	positive		-	12	V
		negative		-	-5	V
I <sub>O</sub>	output current			-	100	mA
I <sub>CM</sub>	peak collector current	$t_p \le 1 \text{ ms}; \text{ single pulse}$		-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.

## PDTC123YT-Q

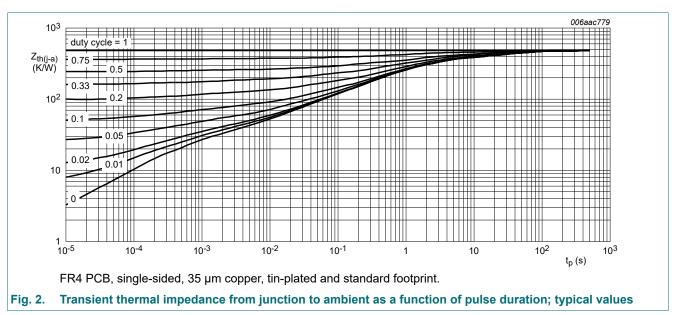
#### NPN resistor-equipped transistor; R1 = 2.2 k $\Omega$ , R2 = 10 k $\Omega$



### 9. Thermal characteristics

	al characteristics Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

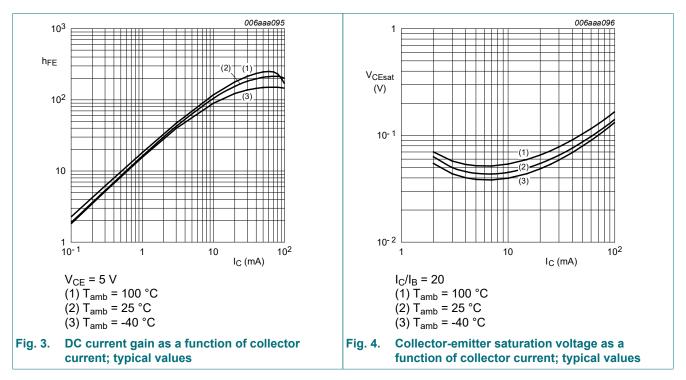
[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.



### **10. Characteristics**

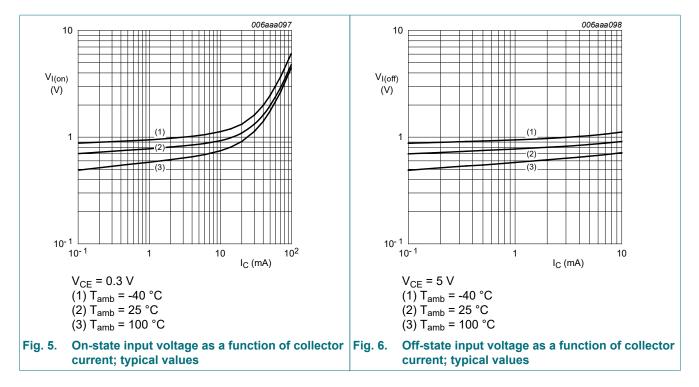
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		50	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = 2 mA; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C		50	-	-	V
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = 50 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ T}_{amb} = 25 ^{\circ}\text{C}$		-	-	100	nA
I <sub>CEO</sub> collector-emitter cut-	collector-emitter cut-off	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
	current	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; \text{ I}_{C} = 0 \text{ A}; \text{ T}_{amb} = 25 \text{ °C}$		-	-	700	μA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C		35	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA; T <sub>amb</sub> = 25 °C		-	-	150	mV
V <sub>I(off)</sub>	off-state input voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 100 μA; T <sub>amb</sub> = 25 °C		-	0.75	0.3	V
V <sub>I(on)</sub>	on-state input voltage	V <sub>CE</sub> = 300 mV; I <sub>C</sub> = 20 mA; T <sub>amb</sub> = 25 °C		2.5	1.15	-	V
R1	bias resistor 1 (input)		[1]	1.54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		[1]	3.6	4.5	5.5	
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	-	2	pF

[1] See "Section 11: Test information" for resistor calculation and test conditions.



### PDTC123YT-Q

#### NPN resistor-equipped transistor; R1 = 2.2 k $\Omega$ , R2 = 10 k $\Omega$



### **11. Test information**

#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

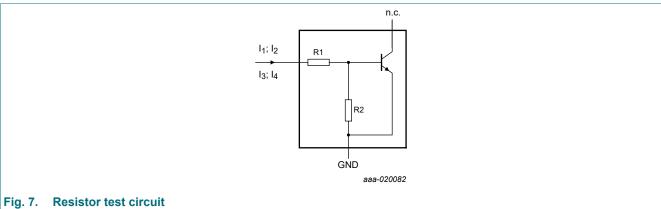
#### **Resistor calculation**

• Calculation of bias resistor 1 (R1)

$$R_{I} = \frac{V(I_{2}) - V(I_{1})}{I_{2} - I_{1}}$$

Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I4) - V(I3)}{R1 \cdot (I4 - I3)} - 1$$

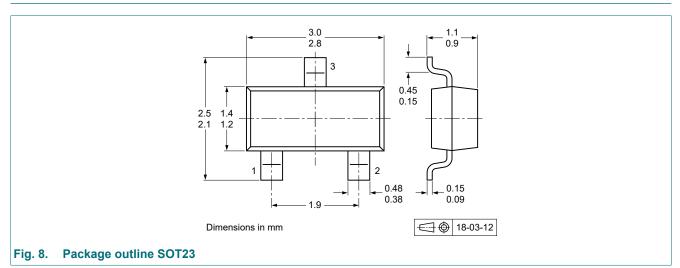


### **Resistor test conditions**

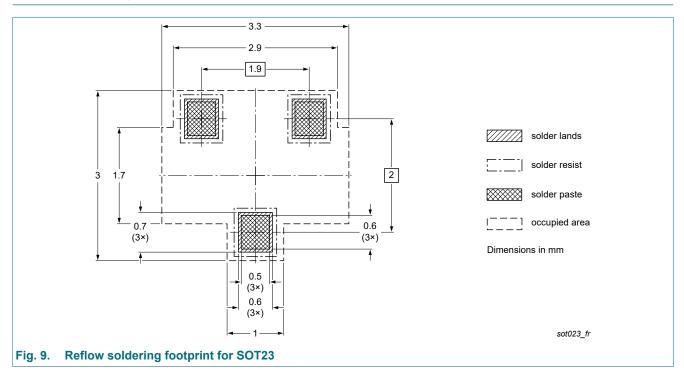
#### Table 8. Resistor test conditions

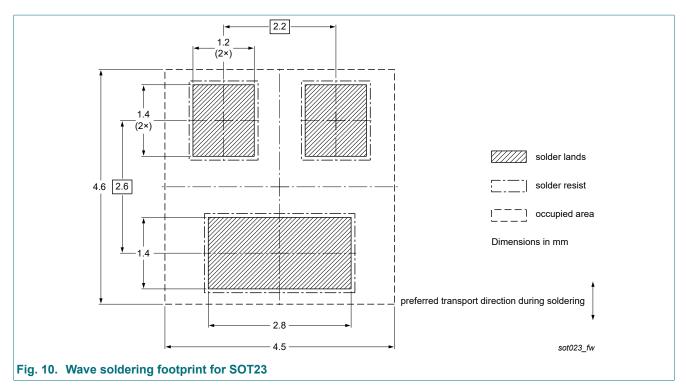
PDTC123YT-Q	R1 (kΩ)	R2 (kΩ)	Test conditions				
			I <sub>1</sub>	l <sub>2</sub>	l <sub>3</sub>	I <sub>4</sub>	
NPN	2.2	10	1300 µA	1500 µA	-350 µA	-450 µA	

### 12. Package outline



### 13. Soldering





### 14. Revision history

Table 9. Revision histo	ry			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTC123YT-Q v.1	20230417	Product data sheet	-	-

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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