



# PTVS6V3Z1UPC

## Transient Voltage Suppressor

25 August 2023

Product data sheet

### 1. General description

Transient voltage suppressor in a DFN1610-2 (SOD1610-1) ultra small and leadless Surface-Mounted Device (SMD) package designed to protect one line against high surge currents and other transients.

### 2. Features and benefits

- Unidirectional protection of one line
- Reverse standoff voltage:  $V_{RWM} = 6.3\text{ V}$
- Average Surge current for 8/20  $\mu\text{s}$  pulse:  $I_{PPM} = 140\text{ A}$  (rated) /  $I_{PP} = 160\text{ A}$  (average measured)
- Ultra low clamping voltage  $V_{CL} = 9.9\text{ V}$  typ. at 140 A

### 3. Applications

- Portable electronics
- Power supply protection
- Power management

### 4. Quick reference data

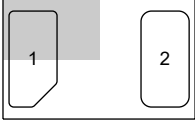

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$V_{RWM}$	reverse standoff voltage	$T_{amb} = 25\text{ }^{\circ}\text{C}$		-	-	6.3	V
$I_{PPM}$	rated peak pulse current	$t_p = 8/20\text{ }\mu\text{s}$	[1]	-	-	140	A
$V_{CL}$	clamping voltage	$I_{PPM} = 140\text{ A}$ ; $t_p = 8/20\text{ }\mu\text{s}$ ; $T_{amb} = 25\text{ }^{\circ}\text{C}$	[1]	-	9.9	12	V

[1] Device stressed with 8/20  $\mu\text{s}$  exponential decay waveform according to IEC 61000-4-5.

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	 <p>Transparent top view</p> <p><b>DFN1610-2 (SOD1610-1)</b></p>	 <p><i>sym035</i></p>
2	A	anode		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PTVS6V3Z1UPC	DFN1610-2	plastic, leadless ultra small package; 2 terminals; body 1.6 x 1 x 0.55 mm	SOD1610-1

## 7. Marking

Table 4. Marking codes

Type number	Marking code
PTVS6V3Z1UPC	6Z3

## 8. Limiting values

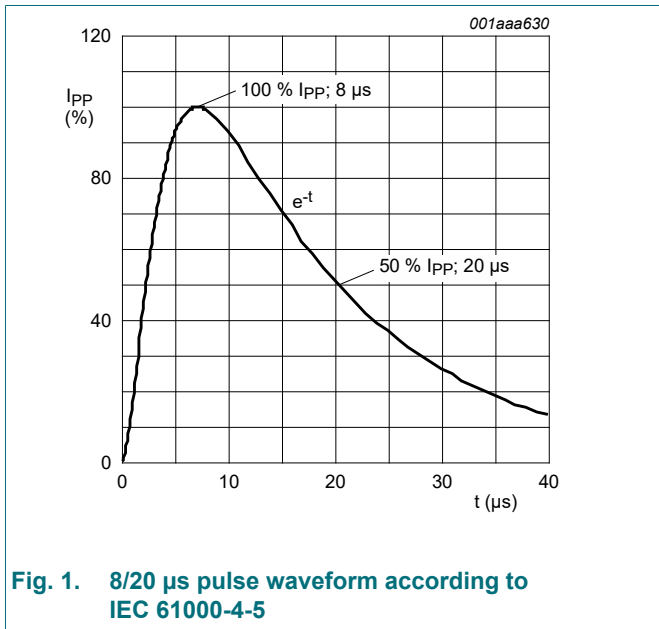
**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

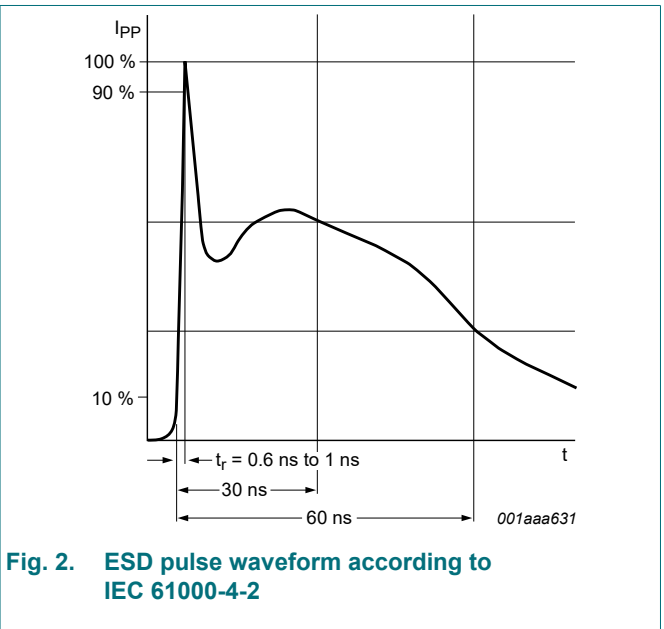
Symbol	Parameter	Conditions		Min	Max	Unit
$I_{PPM}$	rated peak pulse current	$t_p = 8/20 \mu s$	[1]	-	140	A
$T_j$	junction temperature			-	150	°C
$T_{amb}$	ambient temperature			-40	125	°C
$T_{stg}$	storage temperature			-55	150	°C
<b>ESD maximum ratings</b>						
$V_{ESD}$	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[2]	-	30	kV
		IEC 61000-4-2; air discharge	[2]	-	30	kV

[1] Device stressed with 8/20  $\mu s$  exponential decay waveform according to IEC 61000-4-5.

[2] Device stressed with ten non-repetitive ESD pulses.



**Fig. 1. 8/20  $\mu s$  pulse waveform according to IEC 61000-4-5**



**Fig. 2. ESD pulse waveform according to IEC 61000-4-2**

### 9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{RWM}$	reverse standoff voltage	$T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	6.3	V
$V_{BR}$	breakdown voltage	$I_R = 1\text{ mA}; T_{amb} = 25\text{ }^{\circ}\text{C}$	6.4	-	9	V
$I_{RM}$	reverse leakage current	$V_R = 6.3\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	1	$\mu\text{A}$
$C_d$	diode capacitance	$f = 1\text{ MHz}; V_R = 0\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	380	-	pF
$V_{CL}$	clamping voltage	$I_{PPM} = 140\text{ A}; t_p = 8/20\text{ }\mu\text{s}; T_{amb} = 25\text{ }^{\circ}\text{C}$ [1]	-	9.9	12	V

[1] Device stressed with 8/20  $\mu\text{s}$  exponential decay waveform according to IEC 61000-4-5.

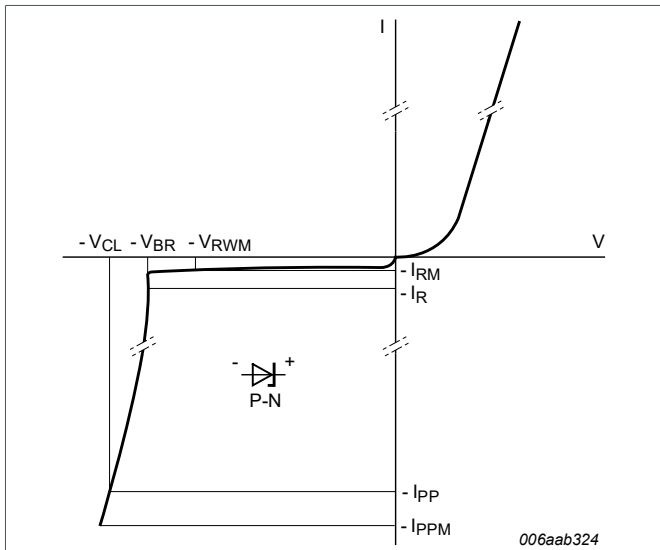


Fig. 3. V-I characteristics for a unidirectional TVS protection diode

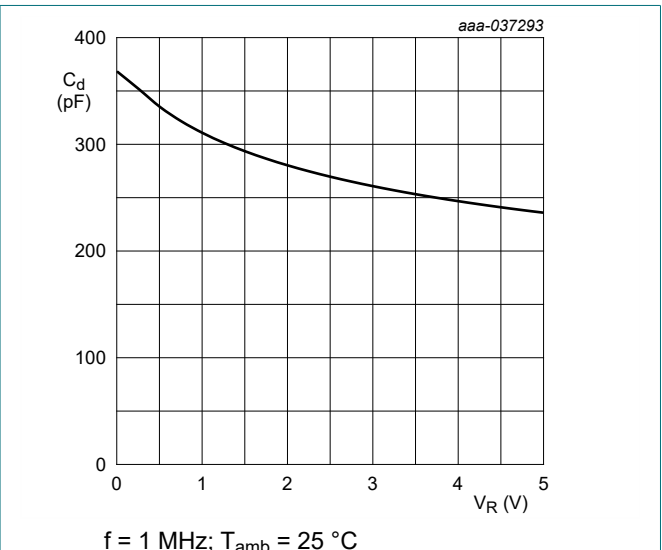


Fig. 4. Capacitance as a function of reverse voltage; typical values

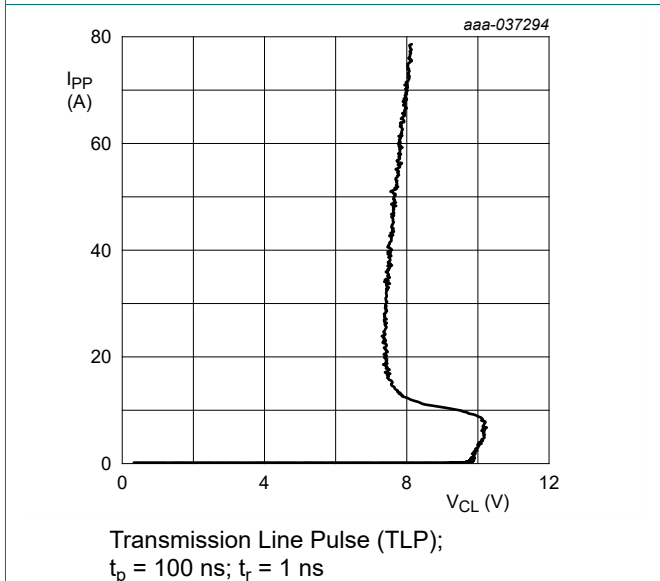


Fig. 5. Positive clamping voltage (TLP); typical values

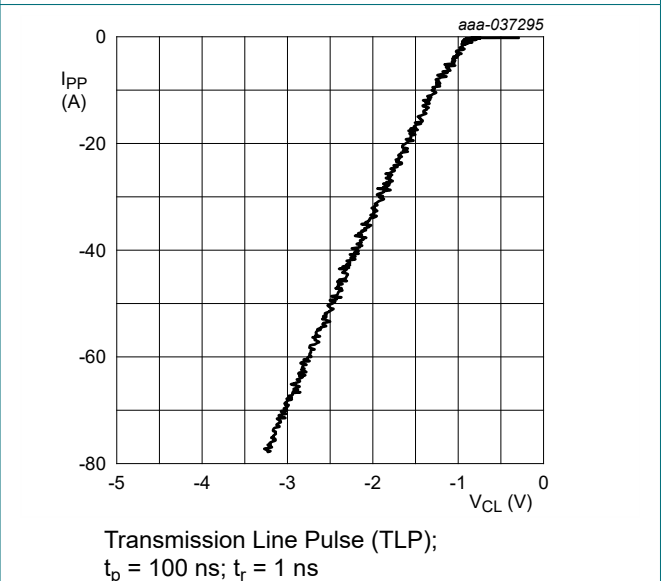
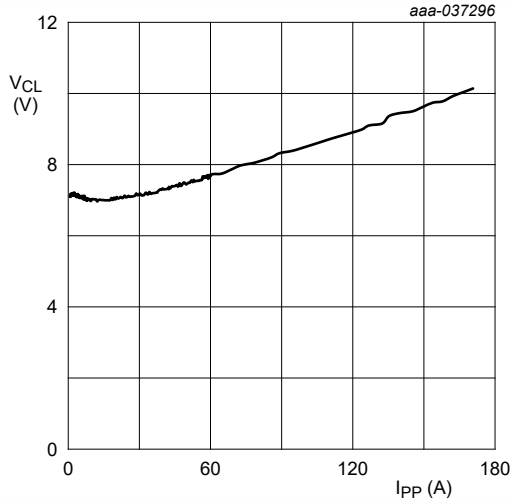
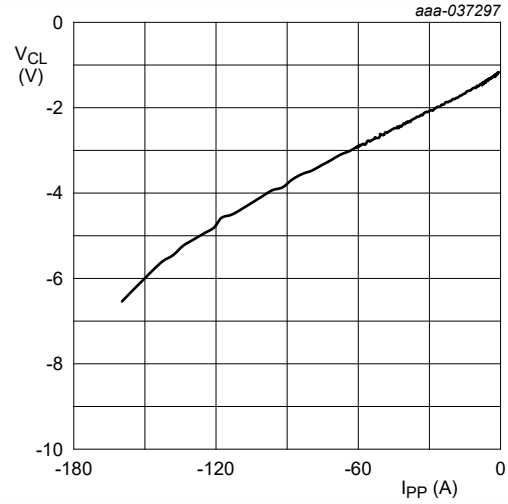


Fig. 6. Negative clamping voltage (TLP); typical values



IEC 61000-4-5;  $t_p = 8/20 \mu s$ ; positive pulse

**Fig. 7. Positive clamping voltage (8/20  $\mu s$  pulse); typical values**

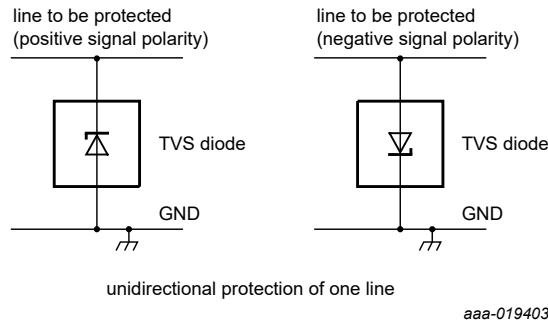


IEC 61000-4-5;  $t_p = 8/20 \mu s$ ; negative pulse

**Fig. 8. Negative clamping voltage (8/20  $\mu s$  pulse); typical values**

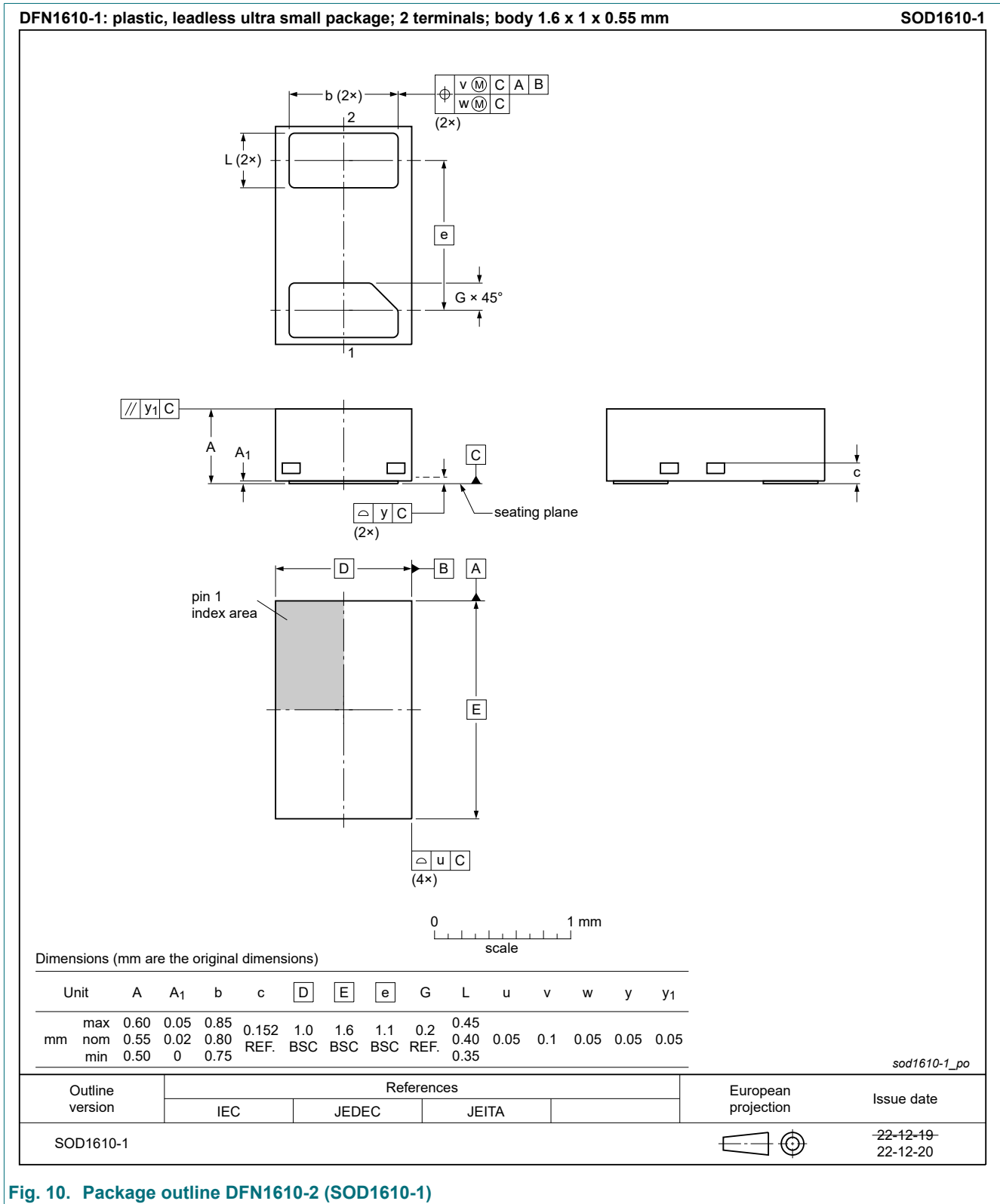
## 10. Application information

The device is designed for the protection of one unidirectional power line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.



**Fig. 9. Application diagram**

### 11. Package outline



**Fig. 10. Package outline DFN1610-2 (SOD1610-1)**

## 12. Soldering

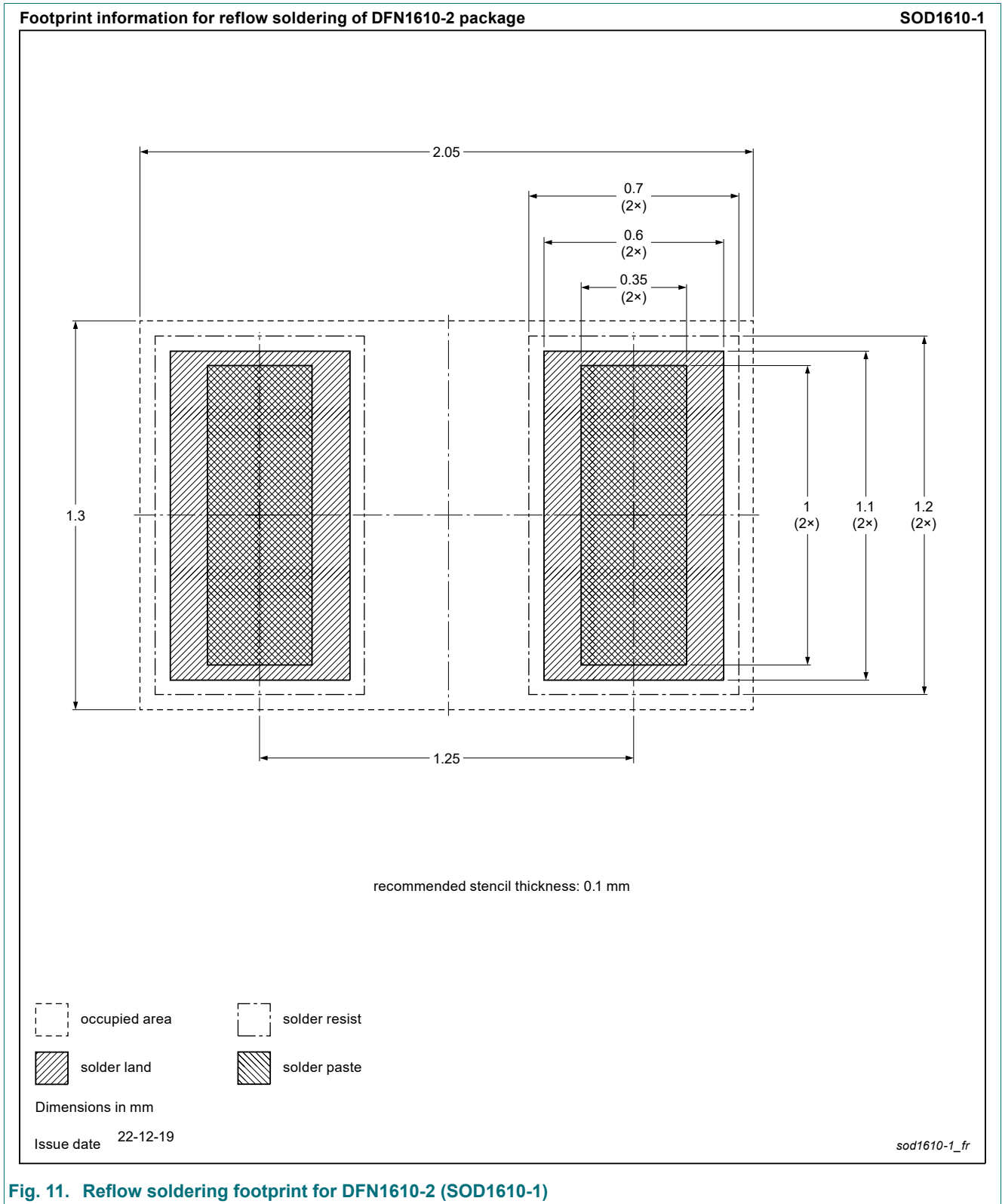


Fig. 11. Reflow soldering footprint for DFN1610-2 (SOD1610-1)

### 13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PTVS6V3Z1UPC v.1	20230825	Product data sheet	-	-



## 14. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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