

PDTC143ZT

50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 47 k Ω

14 July 2023

Product data sheet

1. General description

NPN Resistor-Equipped Transistor (RET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

PNP complement: PDTA143ZT

2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- Simplifies circuit design
- · Reduces component count
- Reduces pick and place costs
- AEC-Q101 qualified

3. Applications

- · Digital application in industrial segments
- · Cost-saving alternative for BC847 series in digital applications
- · Controlling IC inputs
- Switching loads

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	50	V
Io	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio		[1]	8	10	12	

[1] See "Section 11: Test information" for resistor calculation and test conditions.



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	3	
2	GND	ground (emitter)		R1
3	0	output (collector)		GND R2
			1	sym007

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PDTC143ZT	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PDTC143ZT	%18

[1] % = placeholder for manufacturing site code

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50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 47 k Ω

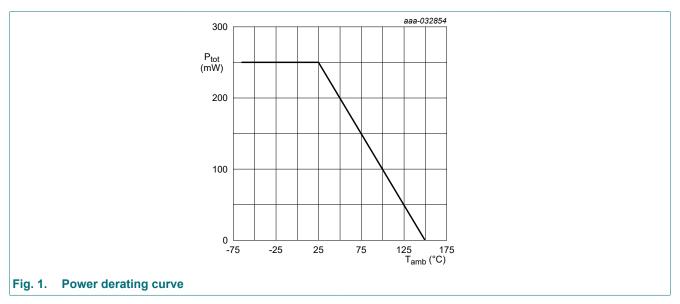
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V_{EBO}	emitter-base voltage	open collector		-	5	V
VI	input voltage			-5	30	V
Io	output current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.



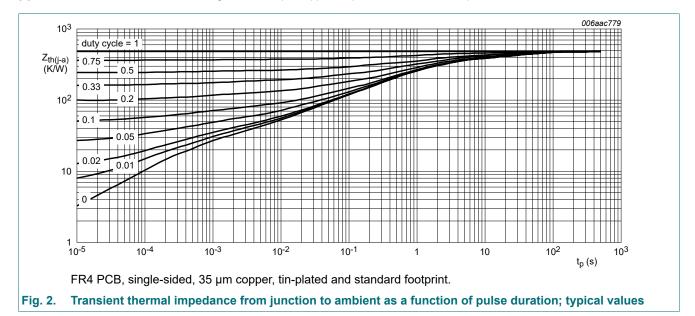
50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 47 k Ω

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
11(J-a)	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

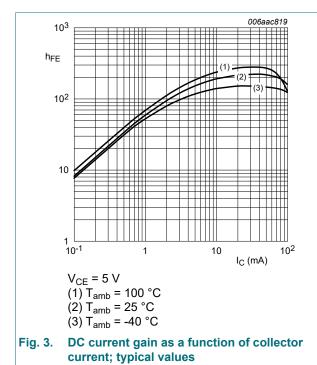


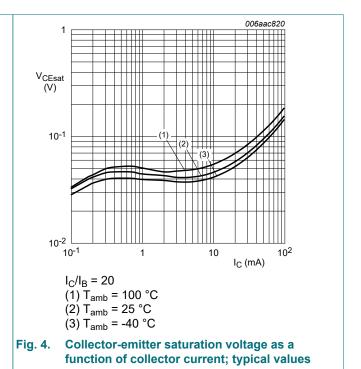
10. Characteristics

Table 7. Characteristics

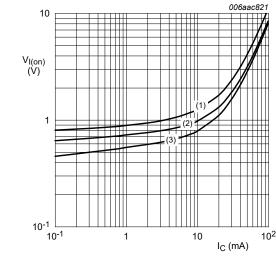
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = 100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		50	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		50	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = 50 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
I _{CEO}	collector-emitter cut-off	V _{CE} = 30 V; I _B = 0 A; T _{amb} = 25 °C		-	-	100	nA
	current	V _{CE} = 30 V; I _B = 0 A; T _j = 150 °C		-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		-	-	170	μA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 10 mA; T _{amb} = 25 °C		100	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 5 \text{ mA}; I_B = 0.25 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		-	-	100	mV
$V_{I(off)}$	off-state input voltage	V _{CE} = 5 V; I _C = 100 μA; T _{amb} = 25 °C		-	0.6	0.5	V
$V_{I(on)}$	on-state input voltage	V_{CE} = 0.3 V; I_{C} = 5 mA; T_{amb} = 25 °C		1.3	0.9	-	V
R1	bias resistor 1 (input)		[1]	3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio		[1]	8	10	12	
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_{E} = 0 \text{ A}; i_{e} = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$		-	-	2.5	pF
f _T	transition frequency	V_{CE} = 5 V; I_{C} = 10 mA; f = 100 MHz; T_{amb} = 25 °C	[2]	-	230	-	MHz

- [1] See "Section 11: Test information" for resistor calculation and test conditions.
- [2] Characteristics of built-in transistor.



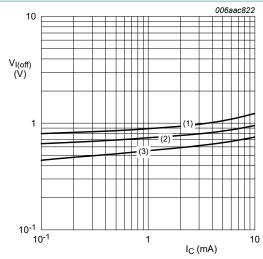


50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 47 k Ω



 $V_{CE} = 0.3 V$

(1) T_{amb} = -40 °C (2) T_{amb} = 25 °C (3) T_{amb} = 100 °C



V_{CE} = 5 V (1) T_{amb} = -40 °C (2) T_{amb} = 25 °C

(3) $T_{amb} = 100 \, ^{\circ}C$

Fig. 5. On-state input voltage as a function of collector | Fig. 6. current; typical values



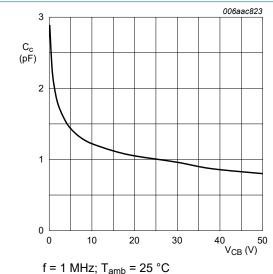
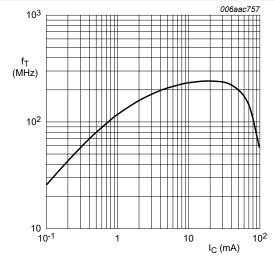


Fig. 7. Collector capacitance as a function of collector- Fig. 8. base voltage; typical values



 V_{CE} = 5 V; T_{amb} = 25 °C

Transition frequency as a function of collector current; typical values of built-in transistor

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11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

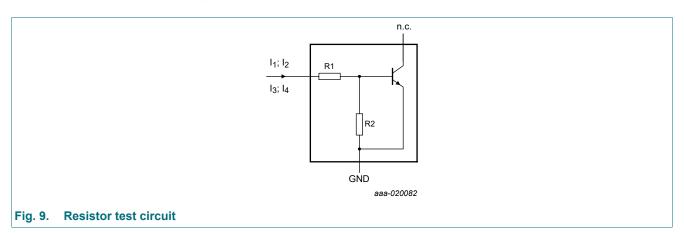
Resistor calculation

· Calculation of bias resistor 1 (R1)

$$R_{I} = \frac{V(I_{2}) - V(I_{1})}{I_{2} - I_{1}}$$

· Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I4) - V(I3)}{R1 \cdot (I4 - I3)} - 1$$



Resistor test conditions

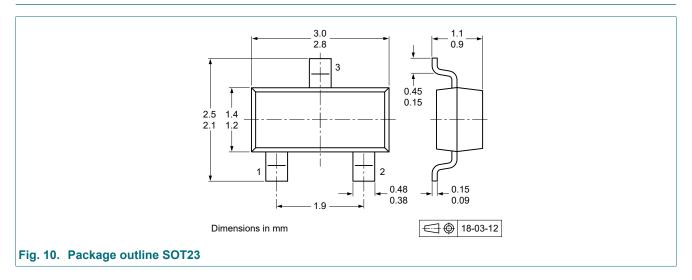
Table 8. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions	Test conditions				
			I ₁	l ₂	l ₃	14		
PDTC143ZT	4.7	47	90 µA	140 µA	-55 µA	-105 µA		

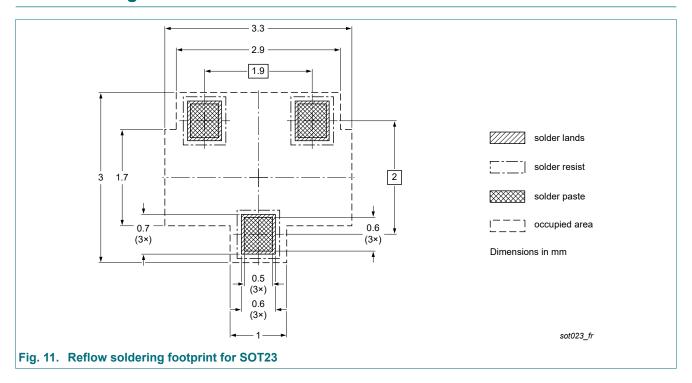
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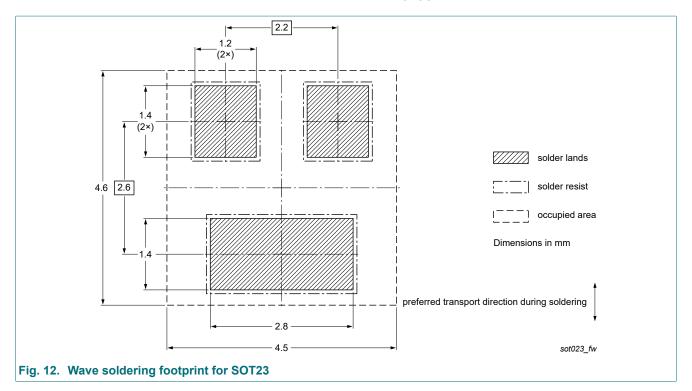
12. Package outline



13. Soldering



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14. Revision history

Table 9. Revision history

Table 9. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTC143ZT v.09	20230714	Product data sheet	-	PDTC143Z_SER v.8
Modification:	•	sheet reduced to single type		
l	 The format of Nexperia. 		edesigned to co	mply with the identity guidelines
	•	nave been adapted to the ne	w company nam	e where appropriate.
	 Packing info 	rmation removed.		
PDTC143Z_SER v.8	20111205	Product data sheet	-	PDTC143Z_SERIES v.7
PDTC143Z_SERIES v.7	20040816	Product data sheet	-	PDTC143Z_SERIES v.6
PDTC143Z_SERIES v.6	20040406	Product specification	-	PDTC143Z_SERIES v.5
PDTC143Z_SERIES v.5	20030910	Product specification	-	PDTC143Z_SERIES v.4
PDTC143Z_SERIES v.4	20030414	Product specification	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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