

XS3A1T5157

Low-ohmic single-pole double-throw analog switch

Rev. 1.1 — 1 August 2024

Product data sheet

1. General description

The XS3A1T5157 is a low-ohmic single-pole double-throw analog switch suitable for use as an analog or digital 2:1 multiplexer/demultiplexer. It has a digital select input (S), two independent inputs/outputs (Y1 and Y2) and a common input/output (Z).

Schmitt trigger action at the digital input makes the circuit tolerant to slower input rise and fall times. Low threshold digital input allows this device to be driven by 1.8 V logic levels in 3.3 V applications without significant increase in supply current I_{CC} . This makes it possible for the XS3A1T5157 to switch 4.3 V signals with a 1.8 V digital controller, eliminating the need for logic level translation. The XS3A1T5157 allows signals with amplitude up to V_{CC} to be transmitted from Z to Y1 or Y2, or from Y1 or Y2 to Z. Its low ON resistance (0.5 Ω) and flatness (0.13 Ω) ensures minimal attenuation and distortion of transmitted signals.

2. Features and benefits

- Wide supply voltage range from 1.4 V to 4.3 V
- Very low ON resistance (peak):
 - 1.6 Ω (typical) at $V_{CC} = 1.4$ V
 - 1.0 Ω (typical) at $V_{CC} = 1.65$ V
 - 0.55 Ω (typical) at $V_{CC} = 2.3$ V
 - 0.50 Ω (typical) at $V_{CC} = 2.7$ V
 - 0.50 Ω (typical) at $V_{CC} = 4.3$ V
- Break-before-make switching
- High noise immunity
- CMOS low-power consumption
- Latch-up performance exceeds 100 mA per JESD78 Class II Level A
- Low-switching threshold levels
- Control input accepts voltages above supply voltage
- Very low supply current, even when input is below V_{CC}
- High current handling capability (350 mA continuous current under 3.3 V supply)
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 class 3B exceeds 8000 V
 - CDM ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
 - IEC61000-4-2 contact discharge exceeds 8000 V for switch ports
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

3. Applications

- Mobile phone
- Tablet / Notebook
- Wearables

4. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
XS3A1T5157GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886
XS3A1T5157GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202

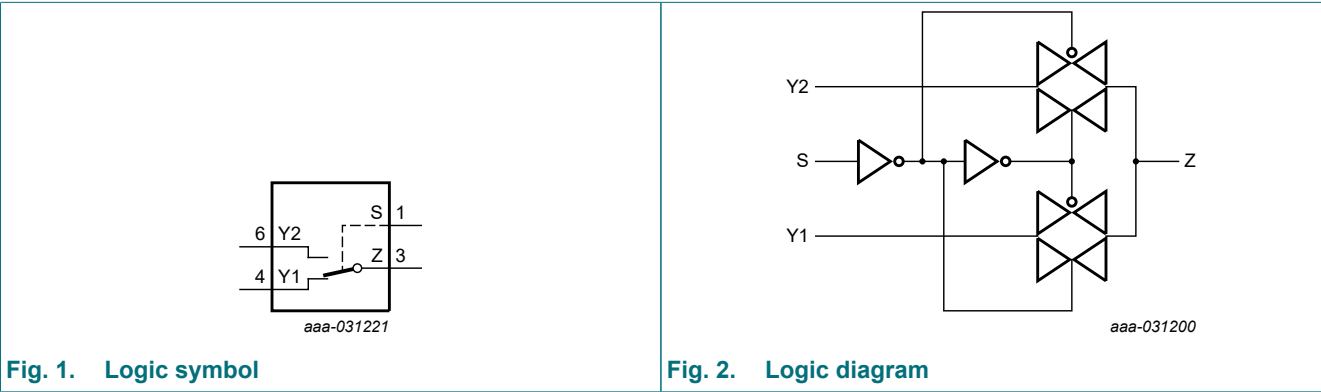
5. Marking

Table 2. Marking codes

Type number	Marking code[1]
XS3A1T5157GM	aS
XS3A1T5157GS	aS

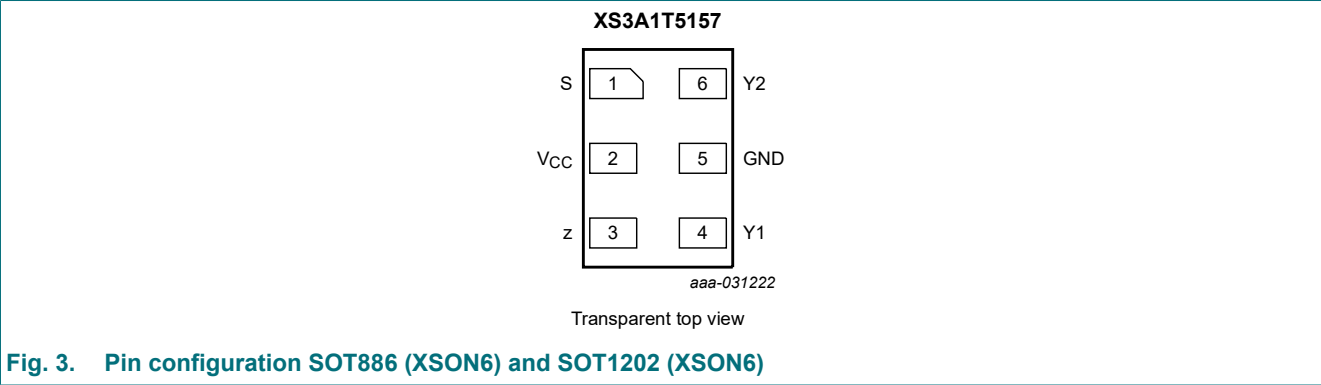
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

6. Functional diagram



7. Pinning information

7.1. Pinning



7.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
S	1	select input
V _{CC}	2	supply voltage
Z	3	common input or output
Y1	4	independent input or output
GND	5	ground (0 V)
Y2	6	independent input or output

8. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

Input S	Channel on
L	Y1
H	Y2

9. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
V _I	input voltage	select input S [1]	-0.5	+4.6	V
V _{SW}	switch voltage	[2]	-0.5	V _{CC} + 0.5	V
I _{IK}	input clamping current	V _I < -0.5 V	-50	-	mA
I _{SK}	switch clamping current	V _I < -0.5 V or V _I > V _{CC} + 0.5 V	-	±50	mA
I _{SW}	switch current	V _{SW} > -0.5 V or V _{SW} < V _{CC} + 0.5 V; source or sink current	-	±350	mA
		V _{SW} > -0.5 V or V _{SW} < V _{CC} + 0.5 V; pulsed at 1 ms duration, < 10 % duty cycle; peak current	-	±500	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C [3]	-	250	mW

- [1] The minimum input voltage rating may be exceeded if the input current rating is observed.
- [2] The minimum and maximum switch voltage ratings may be exceeded if the switch clamping current rating is observed but may not exceed 4.6 V.
- [3] For SOT886 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.
For SOT1202 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

10. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		1.4	4.3	V
V _I	input voltage	select input S	0	4.3	V
V _{SW}	switch voltage	[1]	0	V _{CC}	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 1.4 V to 4.3 V [2]	-	200	ns/V

- [1] To avoid sinking GND current from terminal Z when switch current flows in terminal Yn, the voltage drop across the bidirectional switch must not exceed 0.4 V. If the switch current flows into terminal Z, no GND current will flow from terminal Yn. In this case, there is no limit for the voltage drop across the switch.
- [2] Applies to control signal levels.

11. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground 0 V).

Symbol	Parameter	Conditions	T _{amb} = 25 °C			T _{amb} = -40 °C to +85 °C		T _{amb} = -40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
V _{IH}	HIGH-level input voltage	V _{CC} = 1.4 V to 1.6 V	0.9	-	-	0.9	-	0.9	-	V
		V _{CC} = 1.65 V to 1.95 V	0.9	-	-	0.9	-	0.9	-	V
		V _{CC} = 2.3 V to 2.7 V	1.1	-	-	1.1	-	1.1	-	V
		V _{CC} = 2.7 V to 3.6 V	1.3	-	-	1.3	-	1.3	-	V
		V _{CC} = 3.6 V to 4.3 V	1.4	-	-	1.4	-	1.4	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 1.4 V to 1.6 V	-	-	0.3	-	0.3	-	0.3	V
		V _{CC} = 1.65 V to 1.95 V	-	-	0.4	-	0.4	-	0.3	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.4	-	0.4	-	0.4	V
		V _{CC} = 2.7 V to 3.6 V	-	-	0.5	-	0.5	-	0.5	V
		V _{CC} = 3.6 V to 4.3 V	-	-	0.6	-	0.6	-	0.6	V
I _I	input leakage current	select input S; V _I = GND to 4.3 V; V _{CC} = 1.4 V to 4.3 V	-	-	-	-	±0.5	-	±1	µA
I _{S(OFF)}	OFF-state leakage current	Y1 and Y2 port; see Fig. 4								
		V _{CC} = 1.4 V to 3.6 V	-	-	±5	-	±50	-	±500	nA
		V _{CC} = 3.6 V to 4.3 V	-	-	±10	-	±50	-	±500	nA
I _{S(ON)}	ON-state leakage current	Z port; see Fig. 5								
		V _{CC} = 1.4 V to 3.6 V	-	-	±5	-	±50	-	±500	nA
		V _{CC} = 3.6 V to 4.3 V	-	-	±10	-	±50	-	±500	nA
I _{CC}	supply current	V _I = V _{CC} or GND; V _{SW} = GND or V _{CC}								
		V _{CC} = 3.6 V	-	-	100	-	690	-	6000	nA
		V _{CC} = 4.3 V	-	-	150	-	800	-	7000	nA
ΔI _{CC}	additional supply current	V _{SW} = GND or V _{CC}								
		V _I = 2.6 V; V _{CC} = 4.3 V	-	2.0	4.0	-	7	-	7	µA
		V _I = 2.6 V; V _{CC} = 3.6 V	-	0.35	0.7	-	1	-	1	µA
		V _I = 1.8 V; V _{CC} = 4.3 V	-	7.0	10.0	-	15	-	15	µA
		V _I = 1.8 V; V _{CC} = 3.6 V	-	2.5	4.0	-	5	-	5	µA
		V _I = 1.8 V; V _{CC} = 2.5 V	-	50	200	-	300	-	500	nA
C _I	input capacitance		-	1.0	-	-	-	-	-	pF
C _{S(OFF)}	OFF-state capacitance		-	35	-	-	-	-	-	pF
C _{S(ON)}	ON-state capacitance		-	130	-	-	-	-	-	pF

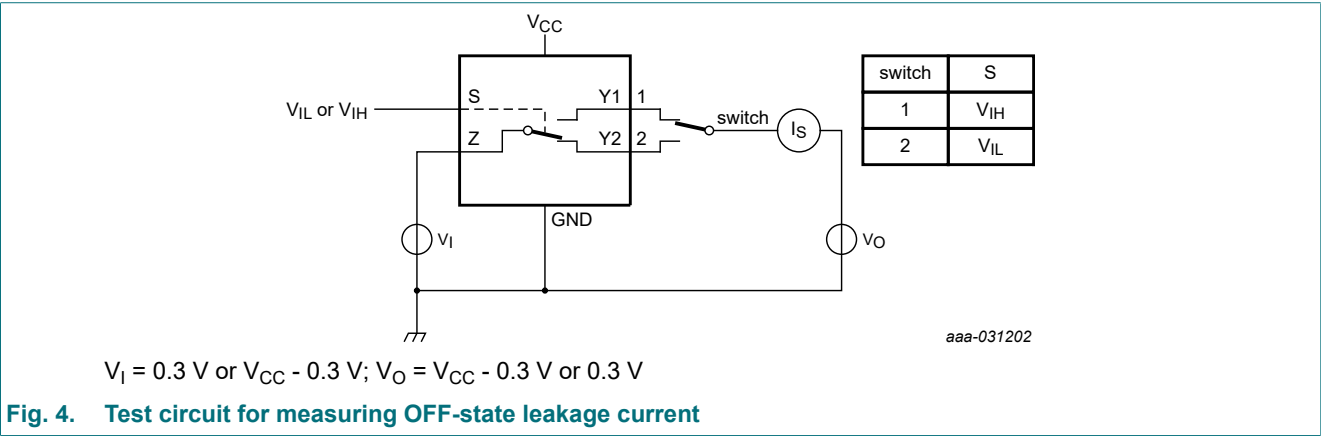
Table 8. ON resistance

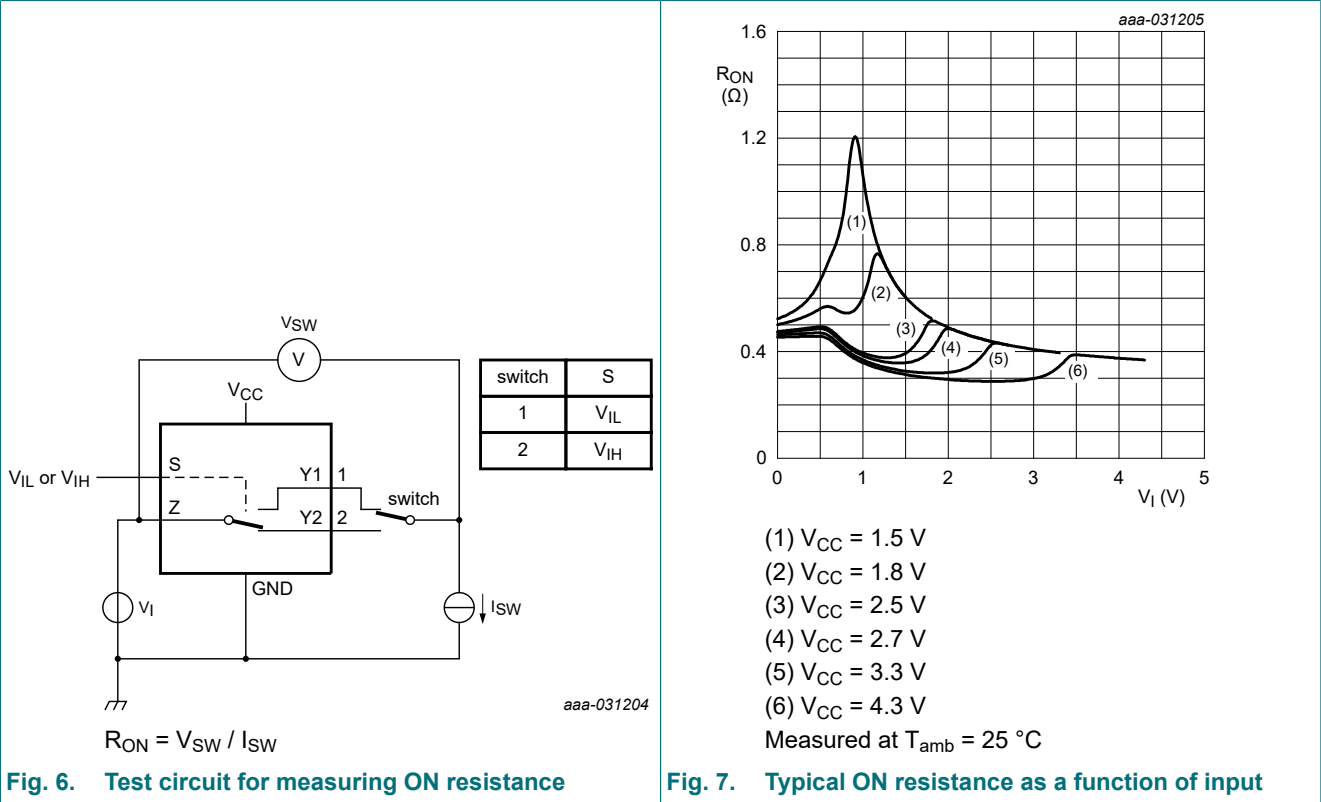
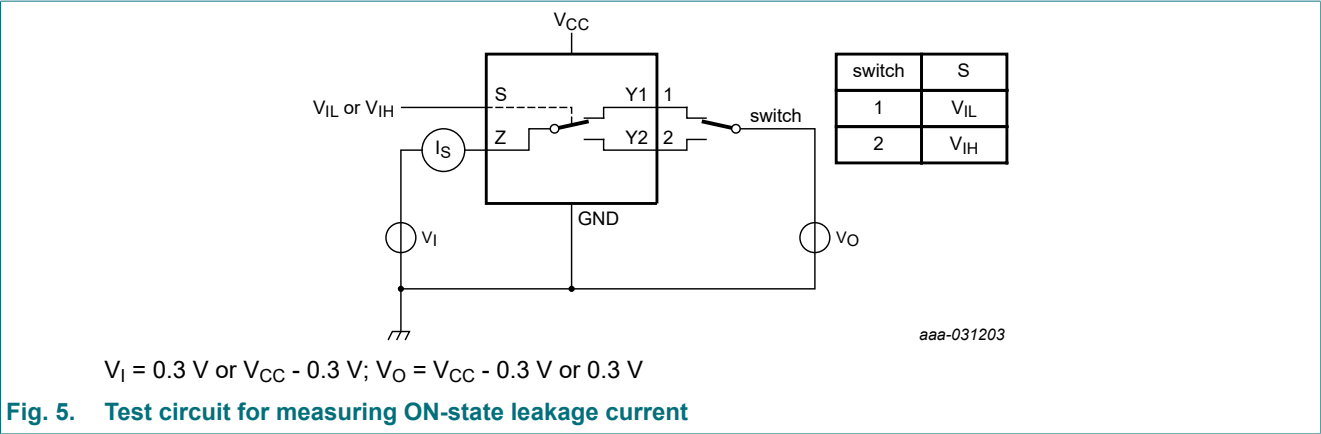
At recommended operating conditions; voltages are referenced to GND (ground = 0 V); for graphs see Fig. 7 to Fig. 13.

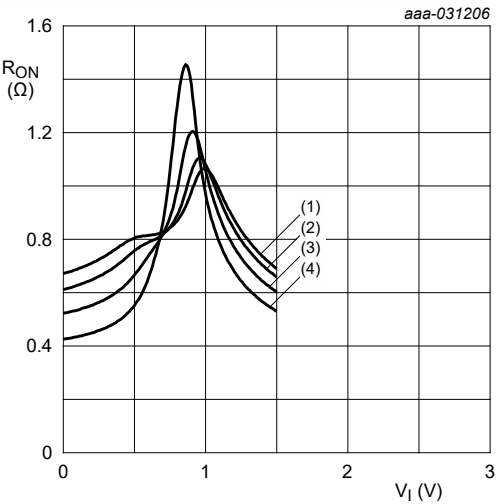
Symbol	Parameter	Conditions	T _{amb} = -40 °C to +85 °C			T _{amb} = -40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	
R _{ON(peak)}	ON resistance (peak)	V _I = GND to V _{CC} ; I _{SW} = 100 mA; see Fig. 6						
		V _{CC} = 1.4 V	-	1.6	3.7	-	4.1	Ω
		V _{CC} = 1.65 V	-	1.0	1.6	-	1.7	Ω
		V _{CC} = 2.3 V	-	0.55	0.8	-	0.9	Ω
		V _{CC} = 2.7 V	-	0.5	0.75	-	0.9	Ω
		V _{CC} = 4.3 V	-	0.5	0.75	-	0.9	Ω
ΔR _{ON}	ON resistance mismatch between channels	V _I = GND to V _{CC} ; I _{SW} = 100 mA [2]						
		V _{CC} = 1.4 V	-	0.04	0.3	-	0.3	Ω
		V _{CC} = 1.65 V	-	0.04	0.2	-	0.3	Ω
		V _{CC} = 2.3 V	-	0.02	0.08	-	0.1	Ω
		V _{CC} = 2.7 V	-	0.02	0.075	-	0.1	Ω
		V _{CC} = 4.3 V	-	0.02	0.075	-	0.1	Ω
R _{ON(flat)}	ON resistance (flatness)	V _I = GND to V _{CC} ; I _{SW} = 100 mA [3]						
		V _{CC} = 1.4 V	-	1.0	3.3	-	3.6	Ω
		V _{CC} = 1.65 V	-	0.5	1.2	-	1.3	Ω
		V _{CC} = 2.3 V	-	0.15	0.3	-	0.35	Ω
		V _{CC} = 2.7 V	-	0.13	0.3	-	0.35	Ω
		V _{CC} = 4.3 V	-	0.2	0.4	-	0.45	Ω

- [1] Typical values are measured at T_{amb} = 25 °C.
- [2] Measured at identical V_{CC}, temperature and input voltage.
- [3] Flatness is defined as the difference between the maximum and minimum value of ON resistance measured at identical V_{CC} and temperature.

11.1. Test circuits and graphs

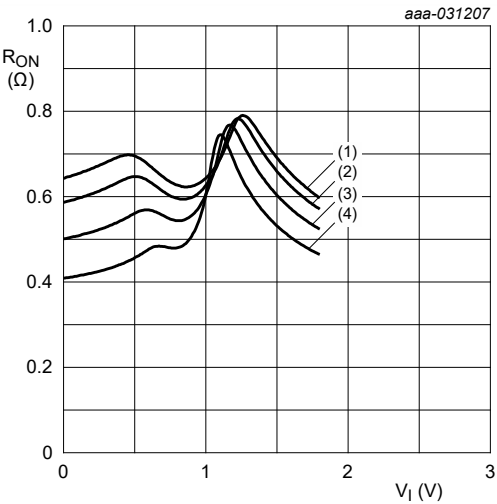






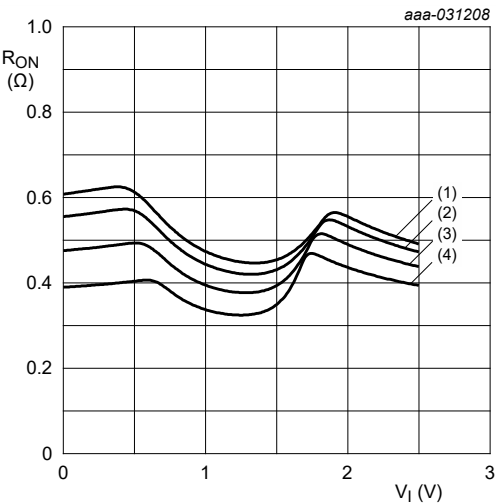
- (1) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Fig. 8. ON resistance as a function of input voltage;
 $V_{CC} = 1.5$ V



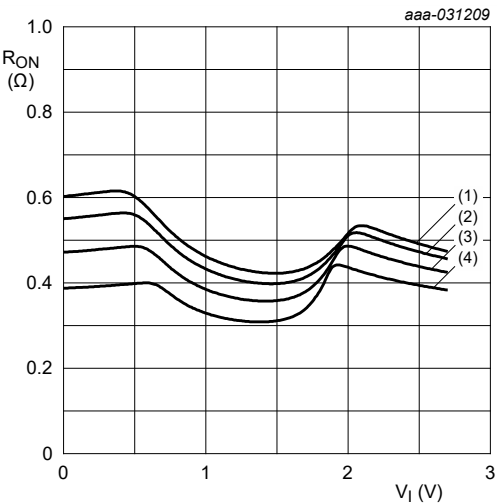
- (1) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Fig. 9. ON resistance as a function of input voltage;
 $V_{CC} = 1.8$ V



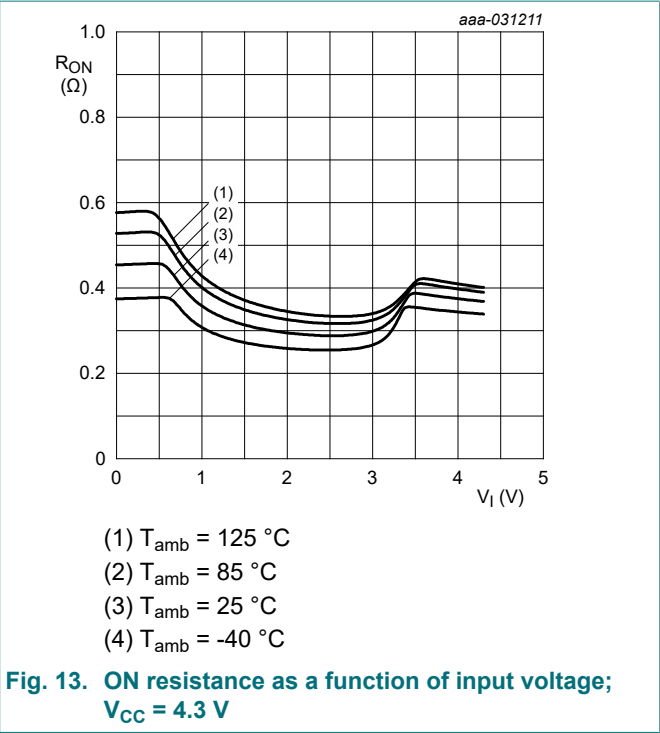
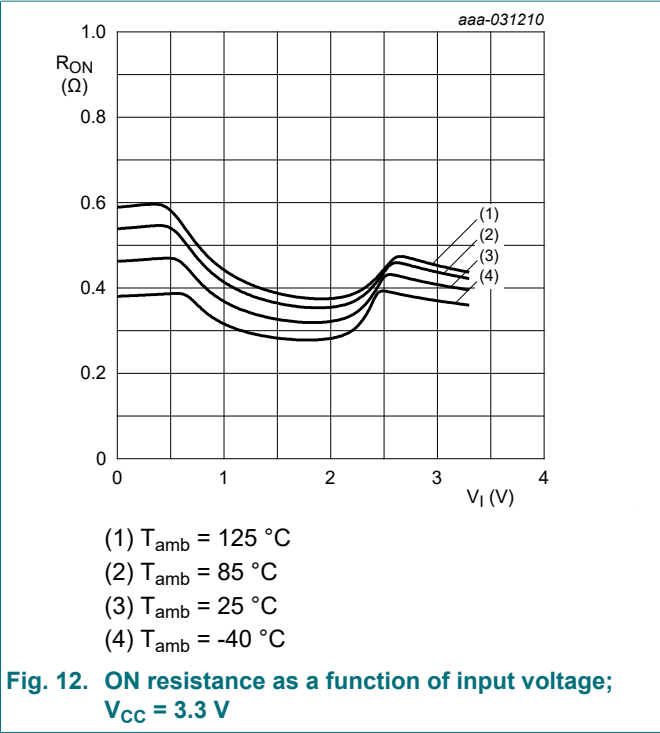
- (1) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Fig. 10. ON resistance as a function of input voltage;
 $V_{CC} = 2.5$ V



- (1) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Fig. 11. ON resistance as a function of input voltage;
 $V_{CC} = 2.7$ V



12. Dynamic characteristics

Table 9. Dynamic characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 16.

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
t_{en}	enable time	S to Z or Yn; see Fig. 14								
		$V_{CC} = 1.4\text{ V to }1.6\text{ V}$	-	50	100	-	120	-	120	ns
		$V_{CC} = 1.65\text{ V to }1.95\text{ V}$	-	36	70	-	80	-	90	ns
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	-	24	45	-	50	-	55	ns
		$V_{CC} = 2.7\text{ V to }3.6\text{ V}$	-	22	40	-	45	-	50	ns
		$V_{CC} = 3.6\text{ V to }4.3\text{ V}$	-	22	40	-	45	-	50	ns
t_{dis}	disable time	S to Z or Yn; see Fig. 14								
		$V_{CC} = 1.4\text{ V to }1.6\text{ V}$	-	32	80	-	80	-	90	ns
		$V_{CC} = 1.65\text{ V to }1.95\text{ V}$	-	20	55	-	60	-	65	ns
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	-	12	25	-	30	-	35	ns
		$V_{CC} = 2.7\text{ V to }3.6\text{ V}$	-	10	20	-	25	-	30	ns
		$V_{CC} = 3.6\text{ V to }4.3\text{ V}$	-	10	20	-	25	-	30	ns
t_{b-m}	break-before-make time	see Fig. 15 [2]								
		$V_{CC} = 1.4\text{ V to }1.6\text{ V}$	-	19	-	9	-	9	-	ns
		$V_{CC} = 1.65\text{ V to }1.95\text{ V}$	-	17	-	7	-	7	-	ns
		$V_{CC} = 2.3\text{ V to }2.7\text{ V}$	-	13	-	4	-	4	-	ns
		$V_{CC} = 2.7\text{ V to }3.6\text{ V}$	-	10	-	3	-	3	-	ns
		$V_{CC} = 3.6\text{ V to }4.3\text{ V}$	-	10	-	2	-	2	-	ns

[1] Typical values are measured at $V_{CC} = 1.5\text{ V}, 1.8\text{ V}, 2.5\text{ V}, 3.3\text{ V}$ and 4.3 V respectively.

[2] Break-before-make guaranteed by design.

12.1. Waveform and test circuits

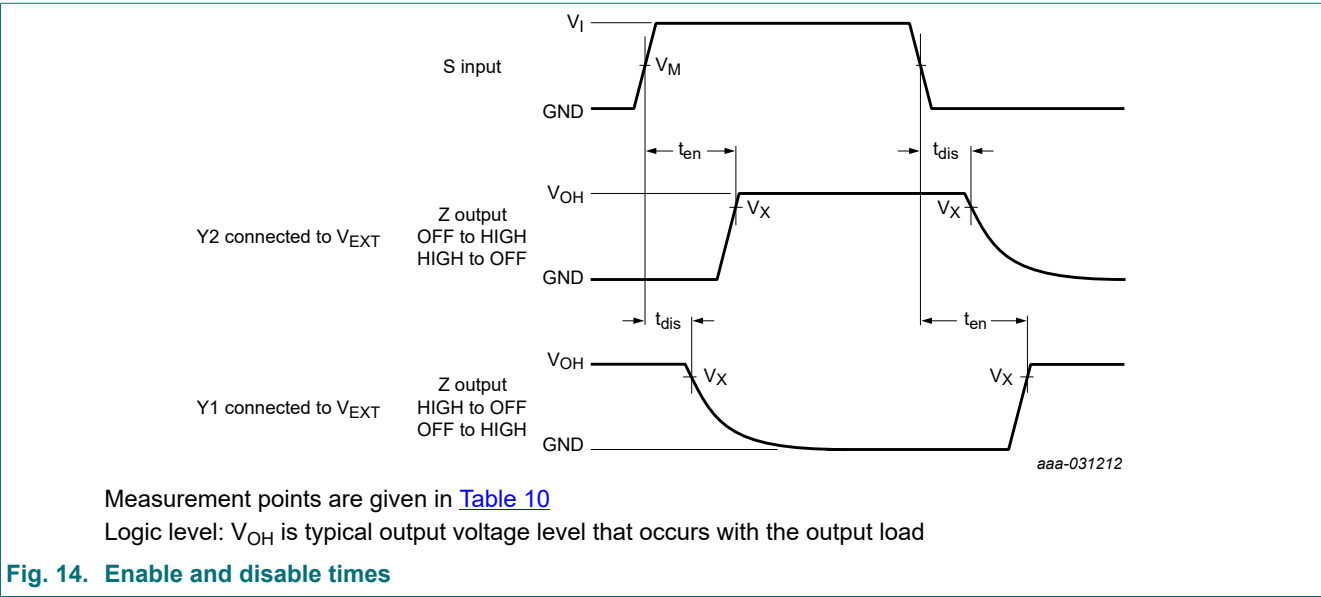


Table 10. Measurement points

Supply voltage	Input	Output
V_{CC}	V_M	V_X
1.4 V to 4.3 V	$0.5V_{CC}$	$0.9V_{OH}$

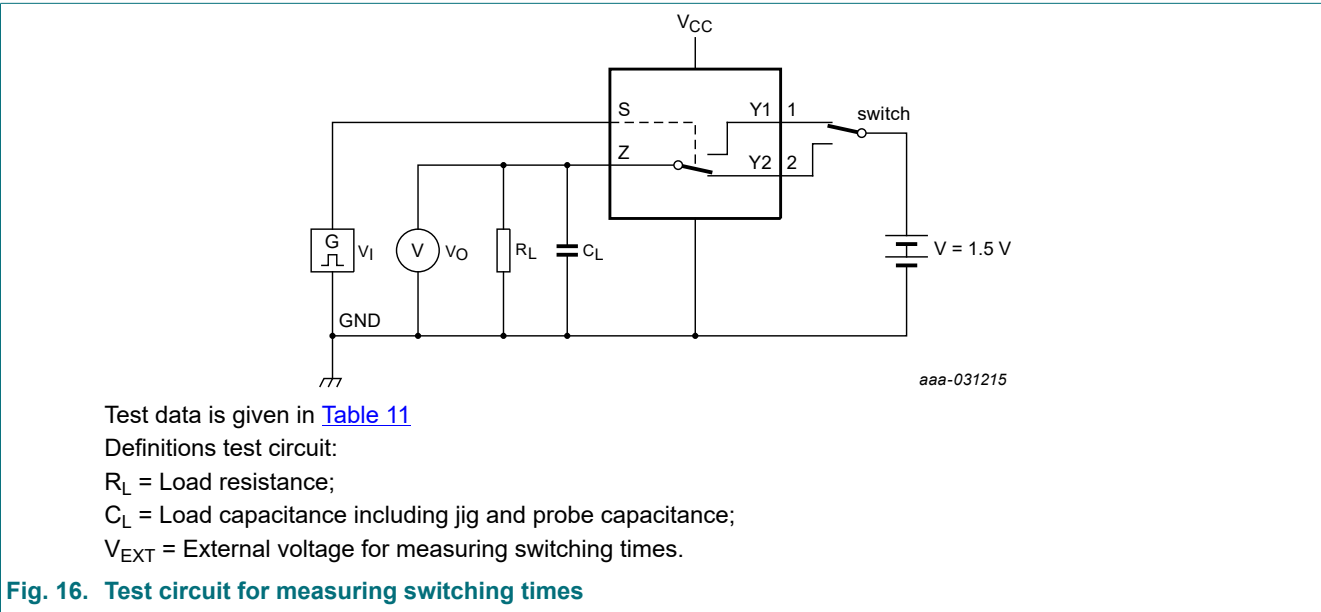
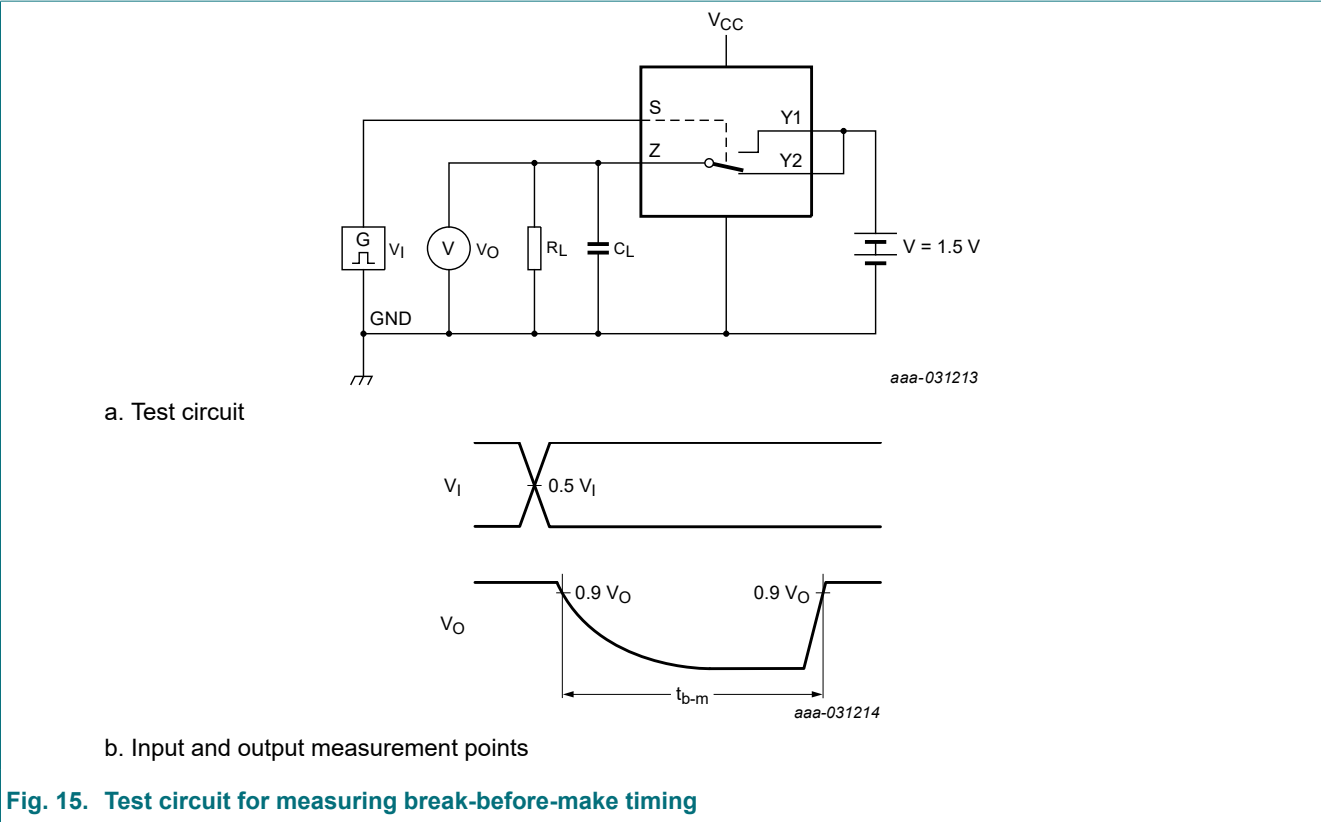


Table 11. Test data

Supply voltage	Input		Load	
V_{CC}	V_I	t_r, t_f	C_L	R_L
1.4 V to 4.3 V	V_{CC}	$\leq 2.5\text{ ns}$	35 pF	50 Ω

12.2. Additional dynamic characteristics

Table 12. Additional dynamic characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V); $V_I = \text{GND}$ or V_{CC} (unless otherwise specified); $t_r = t_f \leq 2.5 \text{ ns}$; $T_{amb} = 25^\circ\text{C}$.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
THD	total harmonic distortion	$f_i = 20 \text{ Hz to } 20 \text{ kHz}$; $R_L = 32 \text{ } \Omega$; see Fig. 17 [1]				
		$V_{CC} = 1.4 \text{ V}$; $V_I = 1 \text{ V (p-p)}$	-	0.15	-	%
		$V_{CC} = 1.65 \text{ V}$; $V_I = 1.2 \text{ V (p-p)}$	-	0.10	-	%
		$V_{CC} = 2.3 \text{ V}$; $V_I = 1.5 \text{ V (p-p)}$	-	0.04	-	%
		$V_{CC} = 2.7 \text{ V}$; $V_I = 2 \text{ V (p-p)}$	-	0.03	-	%
		$V_{CC} = 4.3 \text{ V}$; $V_I = 2 \text{ V (p-p)}$	-	0.01	-	%
$f_{(-3\text{dB})}$	-3 dB frequency response	$R_L = 50 \text{ } \Omega$; see Fig. 18 [1]				
		$V_{CC} = 1.4 \text{ V to } 4.3 \text{ V}$	-	40	-	MHz
α_{iso}	isolation (OFF-state)	$f_i = 100 \text{ kHz}$; $R_L = 50 \text{ } \Omega$; see Fig. 19 [1]				
		$V_{CC} = 1.4 \text{ V to } 4.3 \text{ V}$	-	-90	-	dB
V_{ct}	crosstalk voltage	between digital inputs and switch; $f_i = 1 \text{ MHz}$; $C_L = 50 \text{ pF}$; $R_L = 50 \text{ } \Omega$; see Fig. 20 [1]				
		$V_{CC} = 1.4 \text{ V to } 3.6 \text{ V}$	-	0.4	-	V
		$V_{CC} = 3.6 \text{ V to } 4.3 \text{ V}$	-	0.6	-	V
Q_{inj}	charge injection	$f_i = 1 \text{ MHz}$; $C_L = 0.1 \text{ nF}$; $R_L = 1 \text{ M}\Omega$; $V_{\text{gen}} = 0 \text{ V}$; $R_{\text{gen}} = 0 \text{ } \Omega$; see Fig. 21 [1]				
		$V_{CC} = 1.5 \text{ V}$	-	3	-	pC
		$V_{CC} = 1.8 \text{ V}$	-	4	-	pC
		$V_{CC} = 2.5 \text{ V}$	-	6	-	pC
		$V_{CC} = 3.3 \text{ V}$	-	9	-	pC
		$V_{CC} = 4.3 \text{ V}$	-	15	-	pC

[1] f_i is biased at $0.5V_{CC}$.

12.3. Test circuits

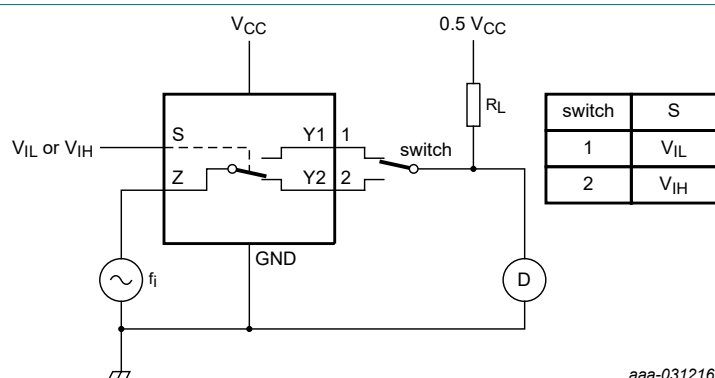
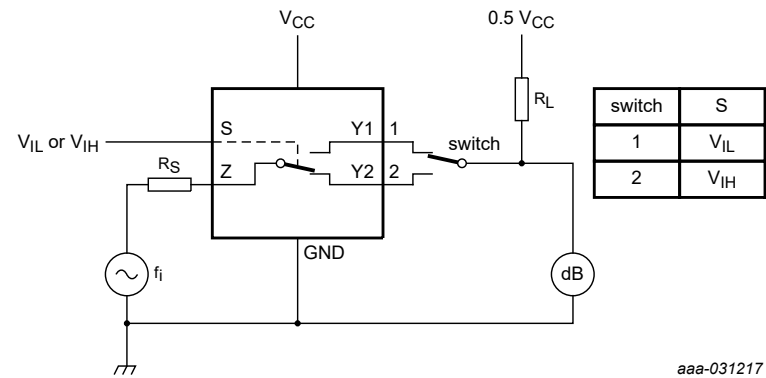
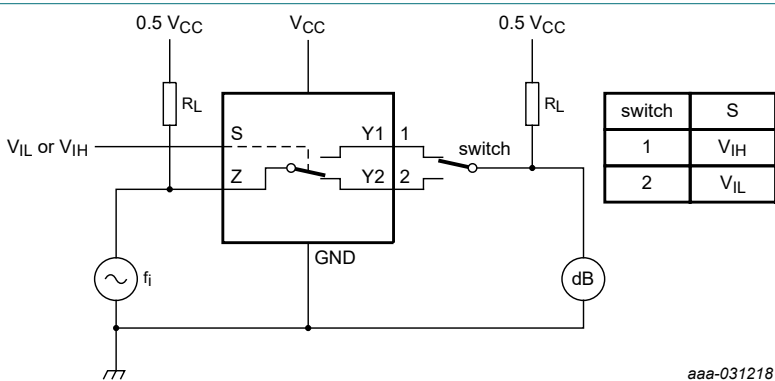


Fig. 17. Test circuit for measuring total harmonic distortion



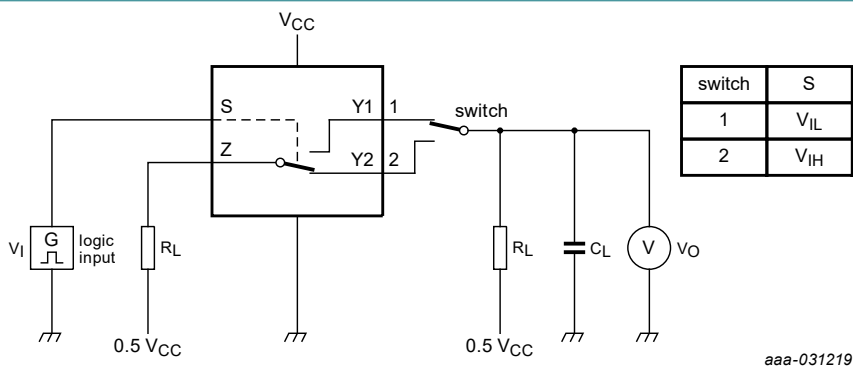
Adjust f_i voltage to obtain 0 dBm level at output. Increase f_i frequency until dB meter reads -3 dB.
 $R_S = R_L = 50 \, \Omega$ (standard 50 Ω system).

Fig. 18. Test circuit for measuring the frequency response when channel is in ON-state

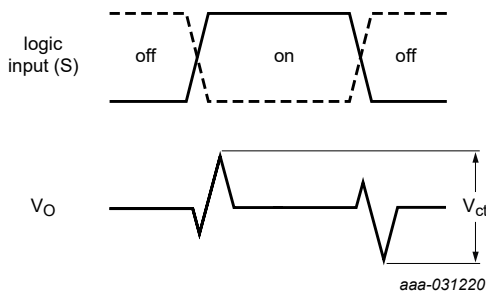


Adjust f_i voltage to obtain 0 dBm level at input.

Fig. 19. Test circuit for measuring isolation (OFF-state)

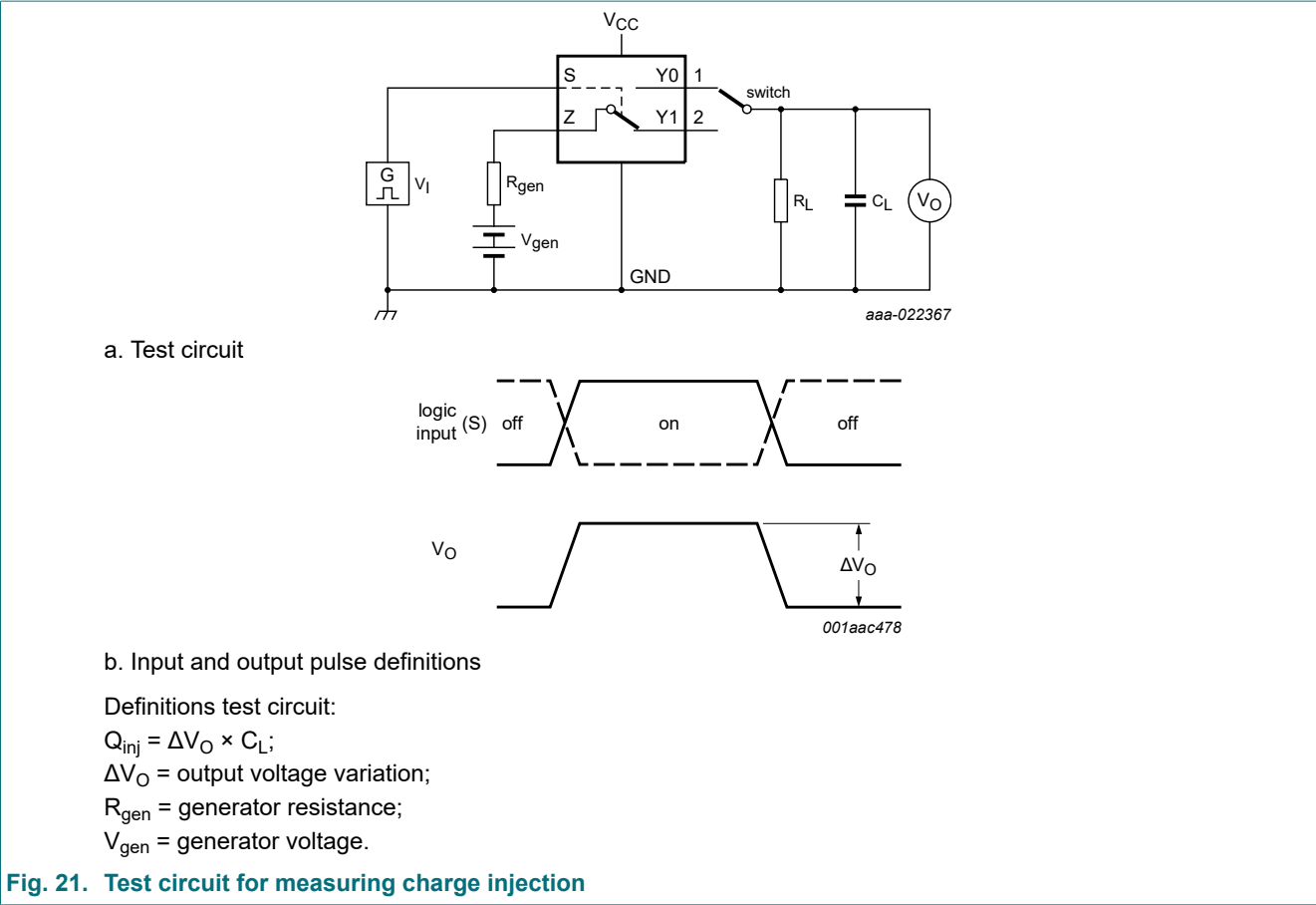


a. Test circuit



b. Input and output pulse definitions

Fig. 20. Test circuit for measuring crosstalk voltage between digital input and switch



13. Package outline

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

SOT886

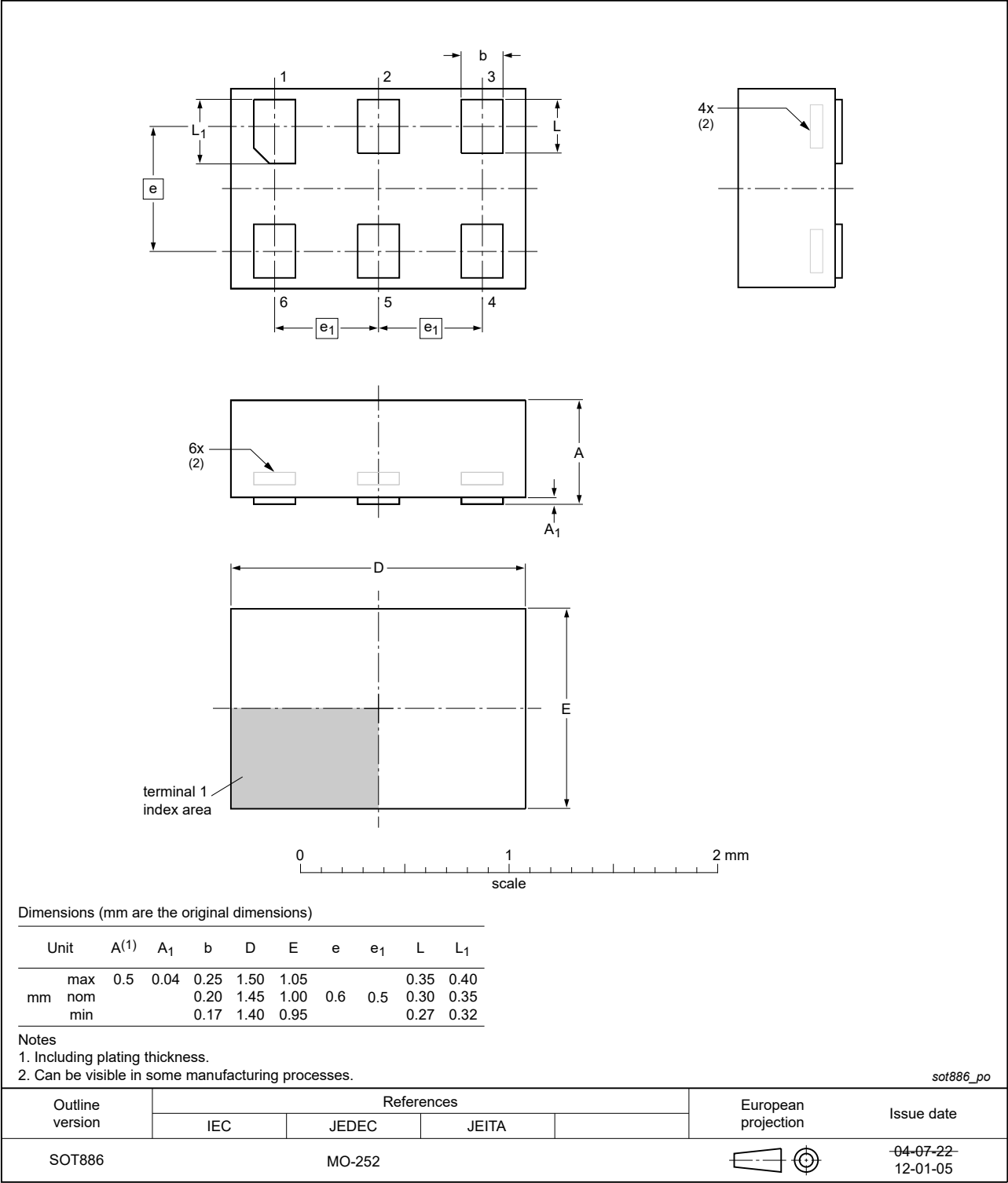
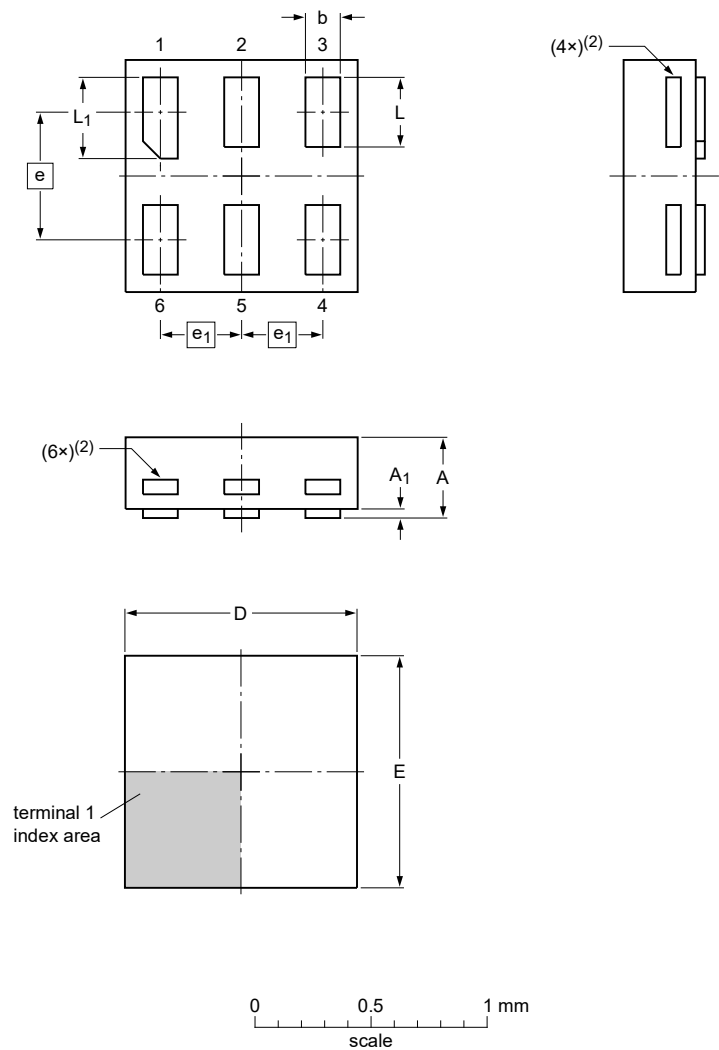


Fig. 22. Package outline SOT886 (XSON6)

XSON6: extremely thin small outline package; no leads;
6 terminals; body 1.0 x 1.0 x 0.35 mm

SOT1202



Dimensions

Unit	A ⁽¹⁾	A ₁	b	D	E	e	e ₁	L	L ₁
mm	max	0.35	0.04	0.20	1.05	1.05		0.35	0.40
	nom			0.15	1.00	1.00	0.55	0.30	0.35
	min			0.12	0.95	0.95		0.27	0.32

Note

- 1. Including plating thickness.
- 2. Visible depending upon used manufacturing technology.

sot1202_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT1202						10-04-02 10-04-06

Fig. 23. Package outline SOT1202 (XSON6)

14. Abbreviations

Table 13. Abbreviations

Acronym	Description
ANSI	American National Standards Institute
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
ESD	ElectroStatic Discharge
ESDA	ElectroStatic Discharge Association
HBM	Human Body Model
JEDEC	Joint Electron Device Engineering Council

15. Revision history

Table 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
XS3A1T5157 v.1.1	20240801	Product data sheet	-	XS3A1T5157 v.1
XS3A1T5157 v.1	20200317	Product data sheet	-	-

16. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Contents

1. General description..... 1

2. Features and benefits..... 1

3. Applications..... 2

4. Ordering information.....2

5. Marking.....2

6. Functional diagram.....2

7. Pinning information.....3

7.1. Pinning.....3

7.2. Pin description.....3

8. Functional description..... 3

9. Limiting values..... 4

10. Recommended operating conditions.....4

11. Static characteristics.....5

11.1. Test circuits and graphs.....6

12. Dynamic characteristics..... 9

12.1. Waveform and test circuits..... 10

12.2. Additional dynamic characteristics..... 12

12.3. Test circuits..... 12

13. Package outline..... 15

14. Abbreviations..... 17

15. Revision history..... 17

16. Legal information.....18

© Nexperia B.V. 2024. All rights reserved

For more information, please visit: <http://www.nexperia.com>

For sales office addresses, please send an email to: salesaddresses@nexperia.com

Date of release: 1 August 2024

单击下面可查看定价，库存，交付和生命周期等信息

[>>Nexperia\(安世\)](#)