

Ultra low capacitance unidirectional double ESD protection diode 2 May 2024

Product data sheet

1. General description

Ultra low capacitance unidirectional double ElectroStatic Discharge (ESD) protection diode in a DFN1006B-3 (SOT883B) leadless ultra small Surface-Mounted Device (SMD) plastic package, designed to protect up to two signal lines from the damage caused by ESD and other transients.

2. Features and benefits

- Ultra low diode capacitance: $C_d = 0.50 \text{ pF}$
- Ultra low package height of only 0.37 mm
- ESD protection up to 10 kV; IEC 61000-4-2
- I_{PPM} = 1.5 A; IEC 61000-4-5 (surge)

3. Applications

- High-speed data lines
- Portable electronics
- Communication systems
- Computers and peripherals

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C	[1]	-	-	5	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[1]	-	0.5	0.65	pF

[1] Measured from pin 1 or 2 to 3.



5. Pinning information

Table 2.	. Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)		
3	A	common anode	1 2 Transparent top view DFN1006B-3 (SOT883B)	K1 CA K2 brb051

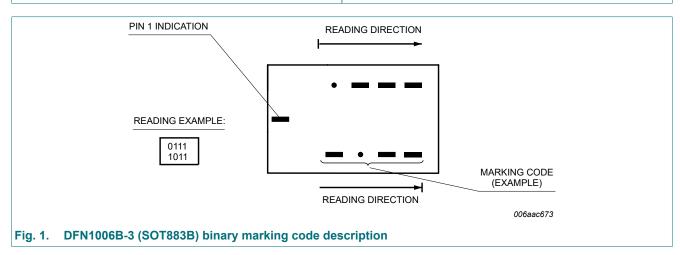
6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD5V0X2UMB		plastic, leadless ultra small plastic package; 3 solder lands; 0.35 mm pitch; 1.0 mm x 0.6 mm x 0.37 mm body	SOT883B

7. Marking

Table 4. Marking codes					
Marking code					
0100					



8. Limiting values

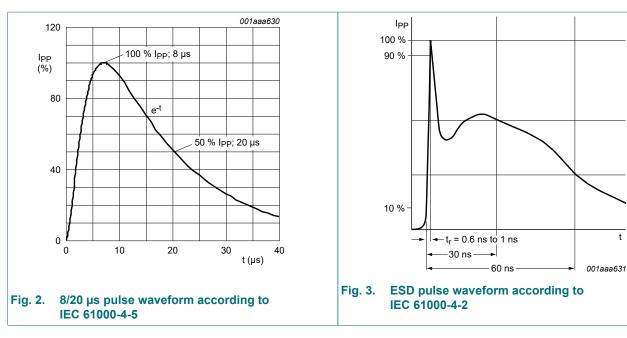
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1] [2]	-	1.5	А
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-55	150	°C
ESD maximu	um ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2; contact discharge	[3] [2]	-	10	kV
	voltage	IEC 61000-4-2; air discharge	[3] [2]	-	15	kV
		machine model	[2]	-	400	V
		MIL-STD-883; human body model (HBM)		-	10	kV

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

- Measured from pin 1 or 2 to 3. [2]
- Device stressed with ten non-repetitive ESD pulses. [3]



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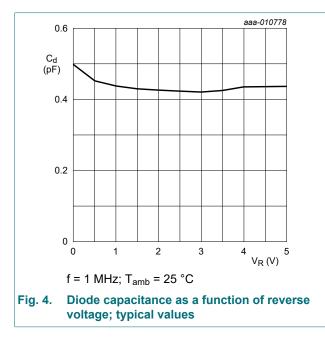
9. Characteristics

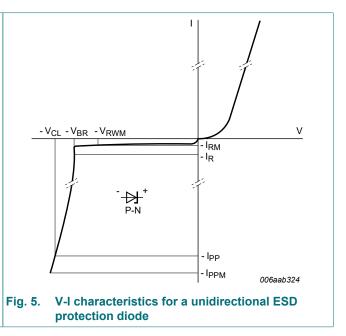
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C	[1]	-	-	5	V
V _{BR}	breakdown voltage	I _R = 10 mA; T _{amb} = 25 °C	[1]	7.5	8.8	10	V
I _{RM}	reverse leakage current	V _{RWM} = 5 V; T _{amb} = 25 °C	[1]	-	1	10	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[1]	-	0.5	0.65	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; t _p = 8/20 μs; T _{amb} = 25 °C	[2] [1]	-	-	13	V
		I _{PPM} = 1.5 A; t _p = 8/20 μs; T _{amb} = 25 °C	[2] [1]	-	-	14	V
R _{dyn}	dynamic resistance	I _R = 10 A; t _p = 100 ns; T _{amb} = 25 °C	[3] [1]	-	0.9	-	Ω

Measured from pin 1 or 2 to 3. [1]

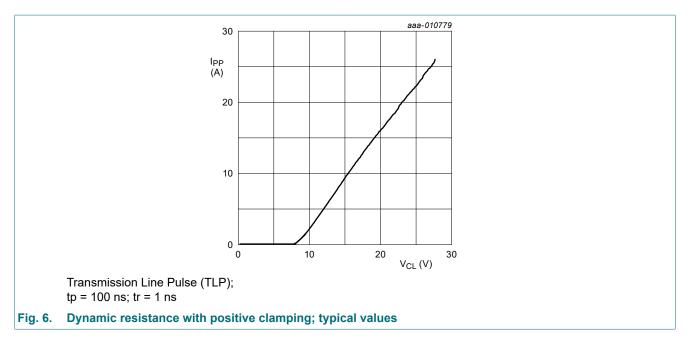
Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

[2] [3] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008.

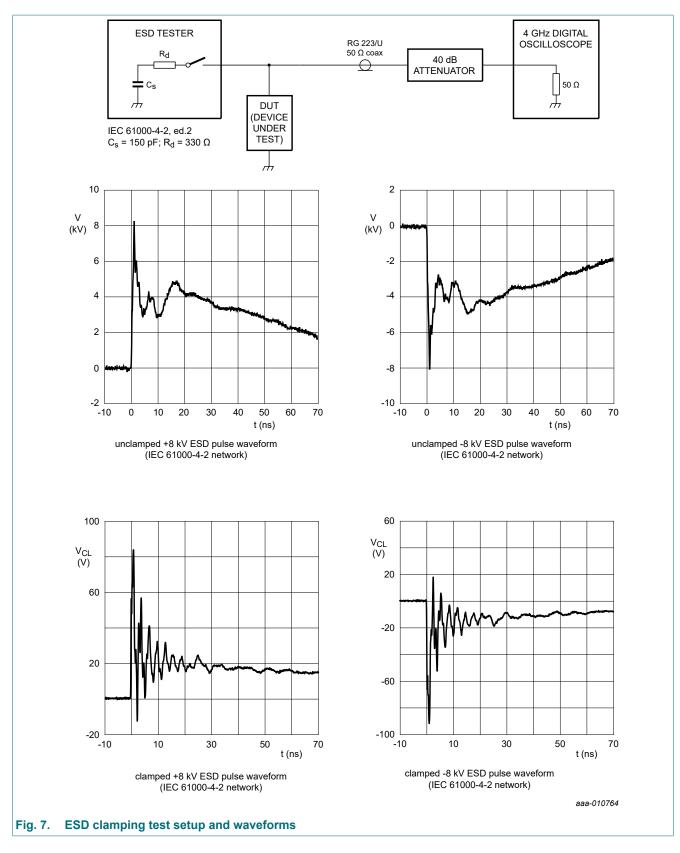




Ultra low capacitance unidirectional double ESD protection diode



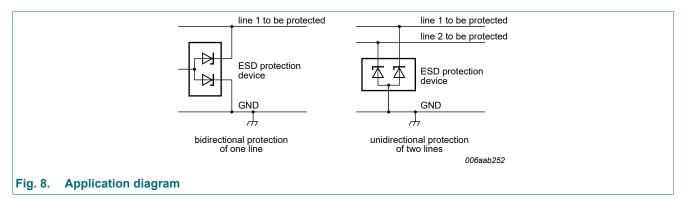
Ultra low capacitance unidirectional double ESD protection diode



10. Application information

The device is designed for the protection of one bidirectional data or signal line from the damage caused by ESD. The device may be used on lines where the signal polarities are both, positive and negative with respect to ground.

The device may also be used for the protection of two unidirectional data or signal lines, which have positive signal polarities with respect to ground.

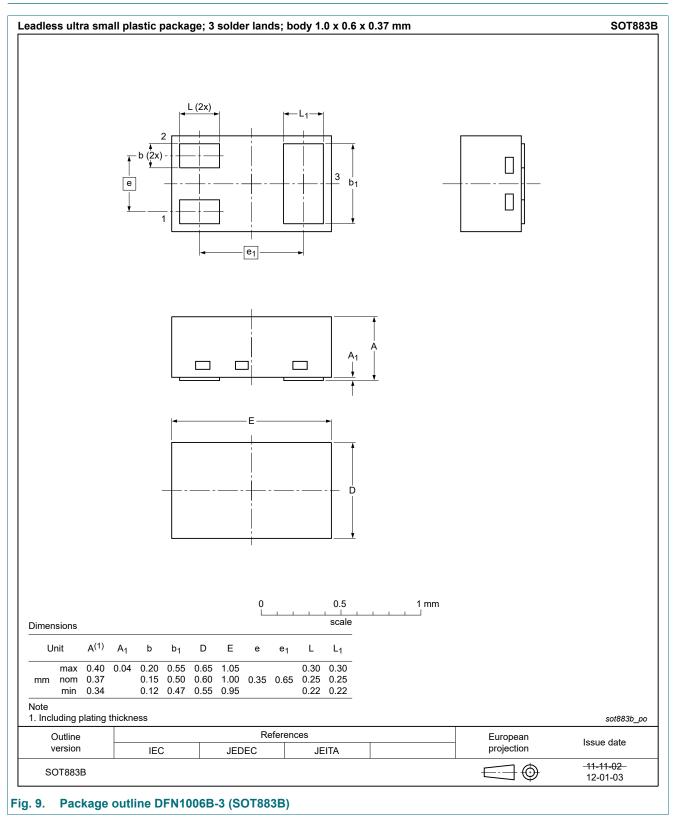


Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

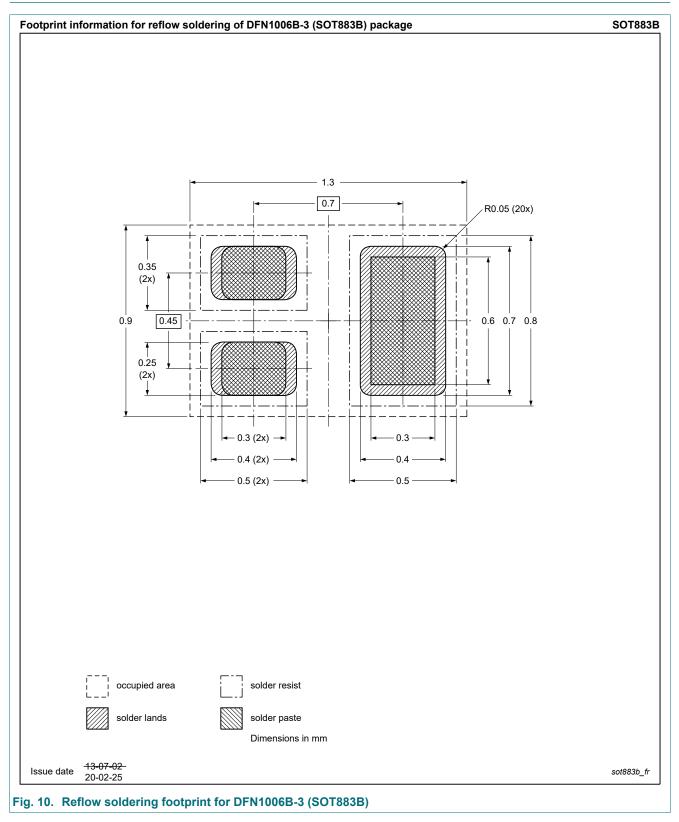
- 1. Place the device as close to the input terminal or connector as possible.
- 2. The path length between the device and the protected line should be minimized.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Ground planes should be used whenever possible. For multilayer PCBs, use ground vias.

11. Package outline



Ultra low capacitance unidirectional double ESD protection diode

12. Soldering



13. Revision history

Table 7. Revision histo	ory				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PESD5V0X2UMB v.2	20240502	Product data sheet	-	PESD5V0X2UMB v.1	
Modifications:	 Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s). 				
PESD5V0X2UMB v.1	20140203	Product data sheet	-	-	

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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