

MMBZ12VAL

Low capacitance unidirectional double ESD protection diode

12 April 2023 Product data sheet

1. General description

Unidirectional double ElectroStatic Discharge (ESD) protection diode in a common anode configuration, encapsulated in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package. The device is designed for ESD and transient overvoltage protection of up to two signal lines.

2. Features and benefits

- Unidirectional ESD protection of two lines
- · Bidirectional ESD protection of one line
- Low diode capacitance: C_d ≤ 140 pF
- Rated peak pulse power: P_{PPM} = 40 W
- Ultra low leakage current: I_{RM} = 5 nA
- ESD protection up to 30 kV (contact discharge)
- IEC 61000-4-2; level 4 (ESD)
- IEC 61643-321

3. Applications

- · Computers and peripherals
- Audio and video equipment
- · Cellular handsets and accessories
- Portable electronics

4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|------------------|--------------------------|---|--|-----|-----|-----|------|
| V _{RWM} | reverse standoff voltage | T _{amb} = 25 °C | | - | - | 8.5 | V |
| C _d | diode capacitance | f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C | | - | 110 | 140 | pF |



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5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------------|--------------------|------------------|
| 1 | K1 | cathode (diode 1) | 3 | 3 |
| 2 | K2 | cathode (diode 2) | | |
| 3 | A | common anode | SOT23 | 1 2 006aaa154 |

6. Ordering information

Table 3. Ordering information

| Type number | | | |
|-------------|------|--|---------|
| | Name | Description | Version |
| MMBZ12VAL | | plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body | SOT23 |

7. Marking

Table 4. Marking codes

| Type number | Marking code[1] |
|-------------|-----------------|
| MMBZ12VAL | %H1 |

[1] % = placeholder for manufacturing site code

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8. Limiting values

Table 5. Limiting values

In accordance with the Aboluste Maximum Rating System (IEC 60134)

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|------------------|---------------------------------|--|---------|-----|------|------|
| P _{PPM} | rated peak pulse power | t _p = 10/1000 μs | [1] [2] | - | 40 | W |
| I _{PPM} | rated peak pulse current | | [1] [2] | - | 2.35 | Α |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [3] | - | 265 | mW |
| | | | [4] | - | 360 | mW |
| T _j | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -55 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |
| ESD maxim | um ratings | | | | | |
| V _{ESD} | electrostatic discharge voltage | IEC 61000-4-2 (contact discharge); T _{amb} = 25 °C | [5] [2] | - | 30 | kV |
| | | IEC 61000-4-2 (air discharge); T _{amb} = 25 °C | | - | 15 | kV |
| | | MIL-STD-883; human body model (HBM); T _{amb} = 25 °C | | - | 8 | kV |
| | | machine model; T _{amb} = 25 °C | [2] | - | 2 | kV |

- [1] In accordance with IEC 61643-321 (10/1000 µs current waveform).
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [5] Device stressed with ten non-repetitive ESD pulses.

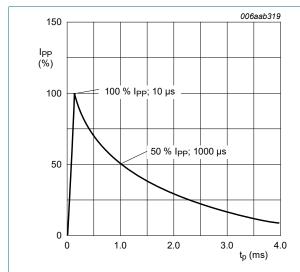


Fig. 1. 10/1000 µs pulse waveform according to IEC 61643-321

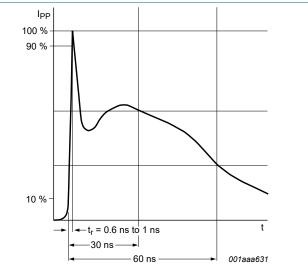


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

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9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|--|--|------------|-----|-----|-----|-----|------|
| R _{th(j-a)} thermal resistance from junction to ambient | in free air | [1] | - | - | 460 | K/W | |
| | | [2] | - | - | 340 | K/W | |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point | | [3] | - | - | 50 | K/W |

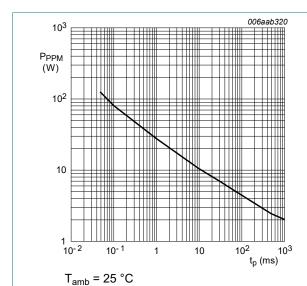
- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [3] Measured from pin 1 or 2 to pin 3.

10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|------------------|--------------------------|---|---------|------|-----|------|------|
| V _F | forward voltage | I _F = 10 mA; T _{amb} = 25 °C | | - | - | 0.9 | V |
| V _{RWM} | reverse standoff voltage | T _{amb} = 25 °C | | - | - | 8.5 | V |
| V_{BR} | breakdown voltage | I _R = 1 mA; T _{amb} = 25 °C | | 11.4 | 12 | 12.6 | V |
| I _{RM} | reverse leakage current | V _{RWM} = 8.5 V; T _{amb} = 25 °C | | - | 0.1 | 5 | nA |
| C _d | diode capacitance | f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C | | - | 110 | 140 | pF |
| V_{CL} | clamping voltage | I _{PPM} = 2.35 A; T _{amb} = 25 °C | [1] [2] | - | - | 17 | V |

- [1] In accordance with IEC 61643-321 (10/1000 µs current waveform).
- [2] Measured from pin 1 or 2 to pin 3.



unidirectional and bidirectional

Fig. 3. Rated peak pulse power as a function of exponential pulse duration (rectangular waveform); typical values

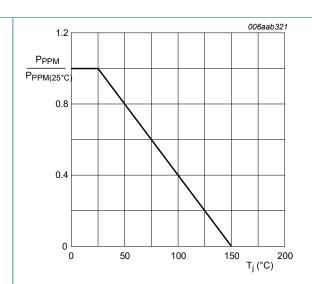
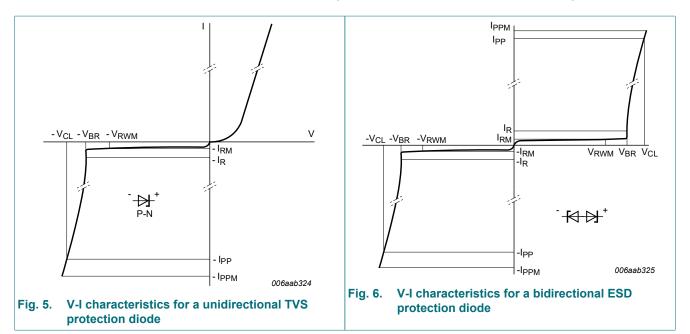


Fig. 4. Relative variation of rated peak pulse power as a function of junction temperature; typical values

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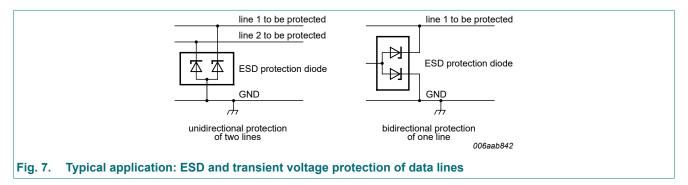


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11. Application information

The device is designed for the protection of up to two unidirectional data lines from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarities are either positive or negative with respect to ground.



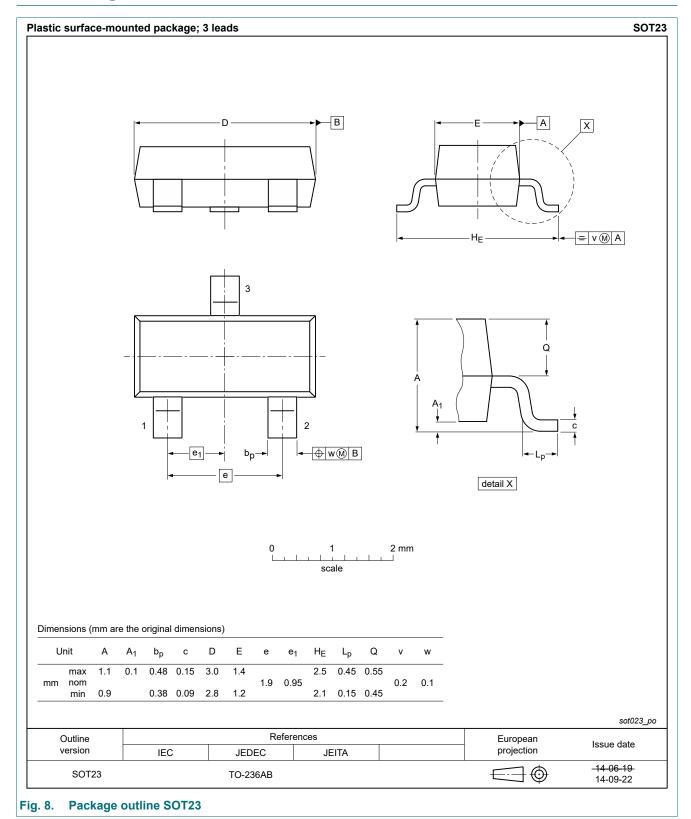
Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

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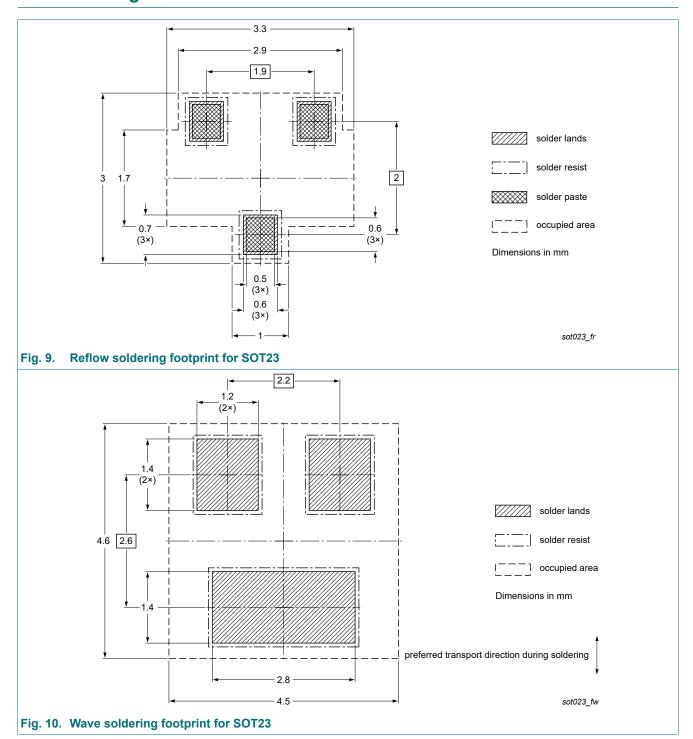
12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

| Tubic o. Itevision mate | 'i y | | | | | | |
|-------------------------|---|--------------------|---------------|----------------|--|--|--|
| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes | | | |
| MMBZ12VAL v.3 | 20230412 | Product data sheet | - | MMBZXAL_SER_2 | | | |
| Modifications: | Family data sheet reduced to single type data sheet. Product changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s). | | | | | | |
| MMBZXAL_SER_2 | 20091210 | Product data sheet | - | MMBZXVAL_SER_1 | | | |
| MMBZXVAL_SER_1 | 20080901 | Product data sheet | - | - | | | |

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15. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

- Please consult the most recently issued document before initiating or completing a design.
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