

Product data sheet

1. General description

Bidirectional ElectroStatic Discharge (ESD) protection diode in a DFN0603-2 leadless ultra small Surface-Mounted Device (SMD) package. The device is designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

- · Bidirectional protection of one line
- Reverse standoff voltage: V_{RWM} = 5V
- Surge robustness: I_{PPM} = 7 A (8/20 μs)
- Ultra low clamping voltage V_{CL} = 7.9 V typ at I_{PPM} = 7 A

3. Applications

- Portable electronics
- Computers and peripherals
- Audio and video equipment
- Communication systems

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-5	-	5	V
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1]	-	-	7	A
V _{CL}	clamping voltage	I_{PPM} = 7 A; t _p = 8/20 µs; T _{amb} = 25 °C	[1]	-	7.9	11.5	V

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.



5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode 1		
2	K2	cathode 2	1 2	К1 🕅 К2
			Transparent top view DFN0603-2 (SOD972-S1)	sym045

6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PESD5V0L1BLF	DFN0603-2	plastic, ultra small and leadless full encapsulated package;2 terminals; 0.37 mm pitch; 0.62 mm x 0.32 mm x 0.30 mm body	SOD972-S1		

7. Marking

Table 4. Marking codes	
Type number	Marking code
PESD5V0L1BLF	D

8. Limiting values

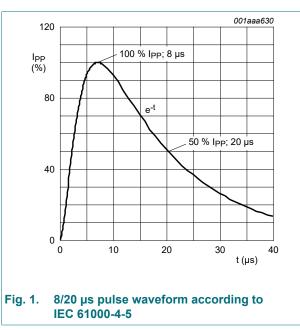
Table 5. Limiting values

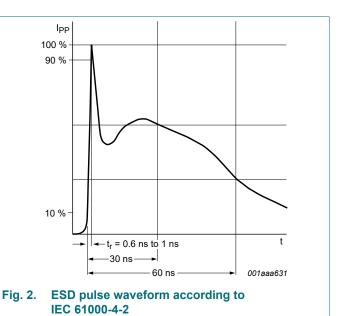
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1]	-	7	А
Tj	junction temperature			-	125	°C
T _{amb}	ambient temperature			-55	125	°C
T _{stg}	storage temperature			-55	150	°C
ESD maxim	um ratings	·	•			
V _{ESD}	electrostatic discharge	IEC 61000-4-2; contact discharge	[2]	-	30	kV
	voltage	IEC 61000-4-2; air discharge	[2]	-	30	kV

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

[2] Device stressed with ten non-repetitive ESD pulses.



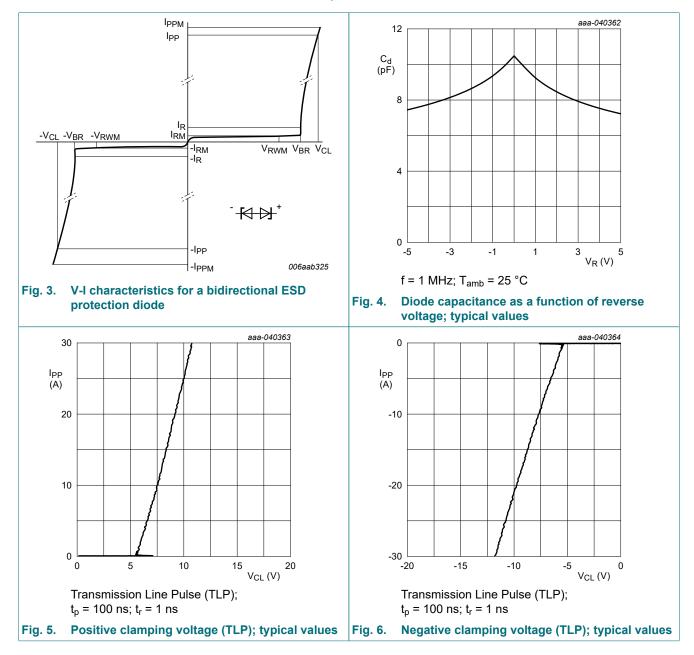


9. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-5	-	5	V
V _{BR}	breakdown voltage	I _R = 1 mA; T _{amb} = 25 °C		5.6	6.5	8.4	V
I _{RM}	reverse leakage current	V _{RWM} = 5 V; T _{amb} = 25 °C		-	-	100	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	-	15	pF
V _{CL}	clamping voltage	I _{PPM} = 7 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1]	-	7.9	11.5	V
		I _{PPM} = 16 A; t _p = TLP; T _{amb} = 25 °C	[2]	-	9	-	V

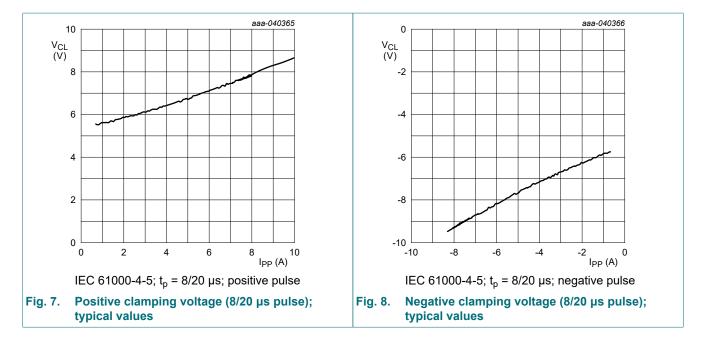
[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

[2] Non-repetitive current pulse, Transmission Line Pulse (TLP) tp = 100 ns; square pulse; ANSI / ESD STM5.5.1-2008.



PESD5V0L1BLF

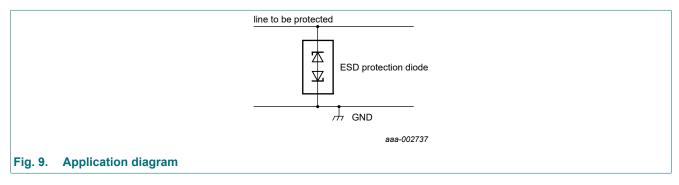
ESD protection device



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10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.



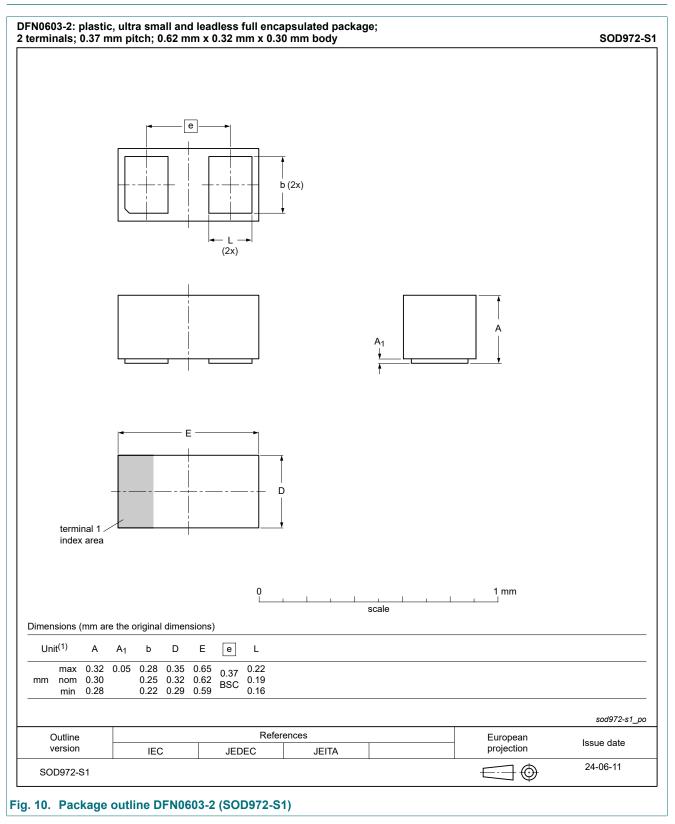
Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- **3.** Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

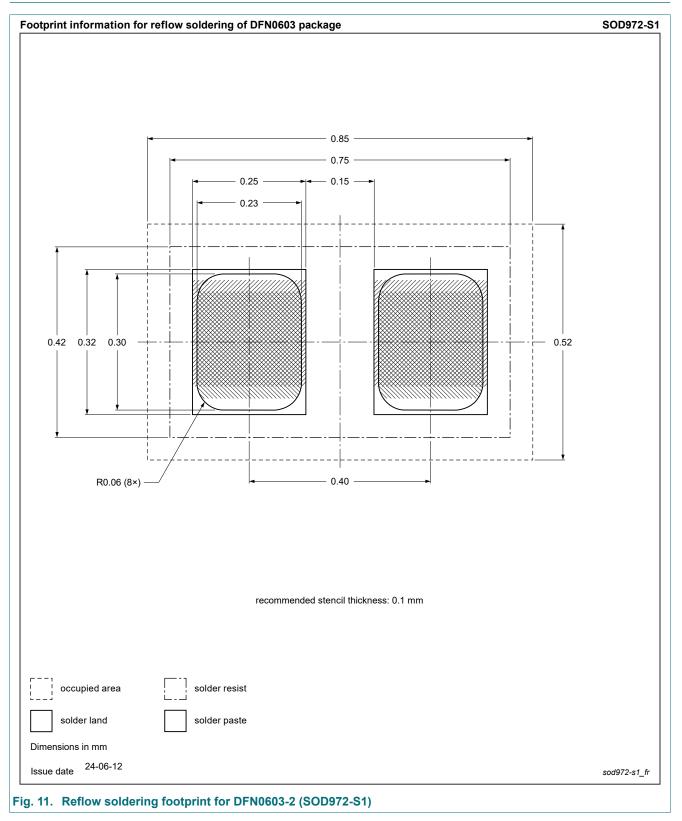
ESD protection device

11. Package outline



ESD protection device

12. Soldering



13. Revision history

Table 7. Revision history					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PESD5V0L1BLF v.1	20240909	Product data sheet	-	-	

PESD5V0L1BLF

ESD protection device

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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