

## 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless, ultra small DFN1006-3 (SOT883) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

### 2. Features and benefits

- Logic-level compatible
- · Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection

### 3. Applications

- Relaydriver
- High-speed line driver
- Low-side load switch
- Switching circuits

## 4. Quick reference data

#### Table 1. Quick reference data

Tubio II duion	Telefence data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	60	V
V <sub>GS</sub>	gate-source voltage			-20	-	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	-	0.27	А
Static charac	teristics					÷	
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 0.19 A; T <sub>j</sub> = 25 °C		-	2.8	4.2	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.



# 5. Pinning information

Table 2.	<b>Pinning info</b>	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	S	source		
3	D	drain	2 Transparent top view DFN1006-3 (SOT883)	G G S 017aaa255

# 6. Ordering information

### Table 3. Ordering information

Type number			
	Name	Description	Version
NX138AKM		plastic, leadless ultra small package; 3 terminals; 0.35 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	SOT883

# 7. Marking

Table 4. Marking codes	
Type number	Marking code
NX138AKM	6A

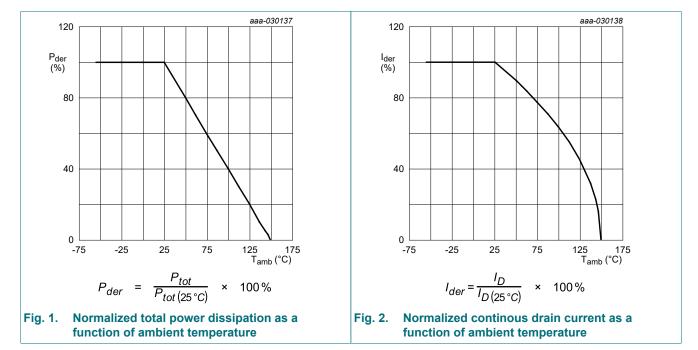
### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

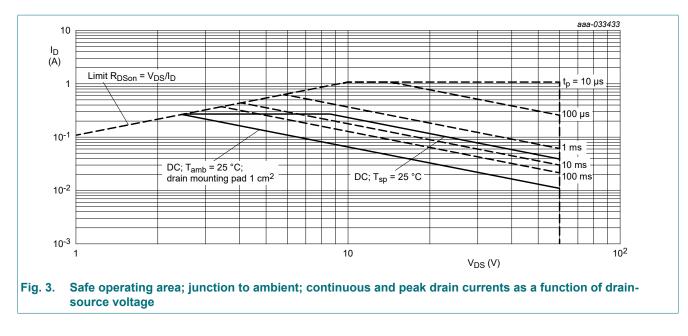
Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	60	V
V <sub>GS</sub>	gate-source voltage			-20	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	0.27	А
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 100 °C	[1]	-	0.17	А
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	1.08	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	340	mW
			[1]	-	648	mW
		T <sub>sp</sub> = 25 °C		-	2.3	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drai	n diode					
Is	source current	T <sub>amb</sub> = 25 °C	[1]	-	0.27	А

Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.
Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



**Product data sheet** 

### 60 V, N-channel Trench MOSFET

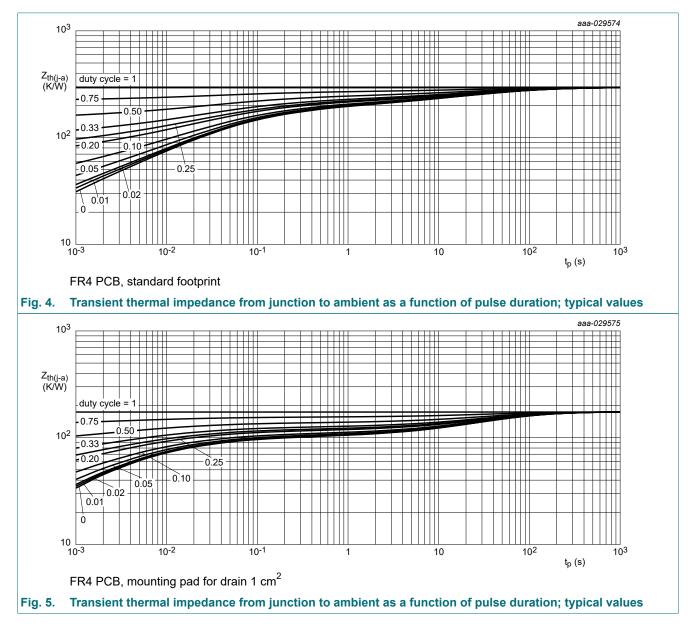


# 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub> thermal resistance from	in free air	[1]	-	317	365	K/W	
	junction to ambient		[2]	-	168	193	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	47	54	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.

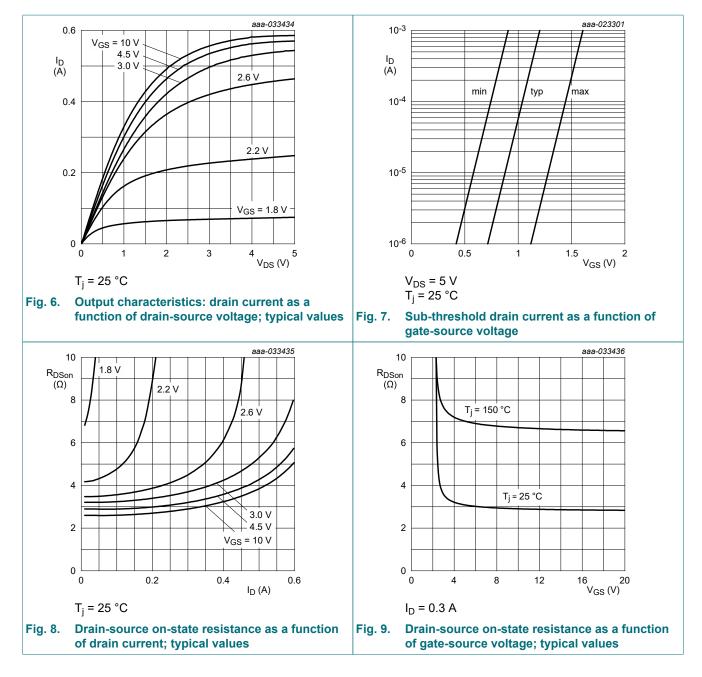


# **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	60	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	$I_D = 250 \ \mu A; V_{DS} = V_{GS}; T_j = 25 \ ^{\circ}C$	0.8	1.1	1.5	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 60 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	2	μA
		V <sub>GS</sub> = -20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-2	μA
		V <sub>GS</sub> = 10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	0.5	μA
		V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-0.5	μA
		V <sub>GS</sub> = 5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	100	nA
		V <sub>GS</sub> = -5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-100	nA
R <sub>DSon</sub> drain-source on-s resistance	drain-source on-state	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 0.19 A; T <sub>j</sub> = 25 °C	-	2.8	4.2	Ω
	resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 0.19 A; T <sub>j</sub> = 150 °C	-	6.2	9.2	Ω
		V <sub>GS</sub> = 5 V; I <sub>D</sub> = 0.17 A; T <sub>j</sub> = 25 °C	-	3	4.5	Ω
		V <sub>GS</sub> = 2.5 V; I <sub>D</sub> = 0.13 A; T <sub>j</sub> = 25 °C	-	4	8	Ω
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 5 V; I <sub>D</sub> = 0.19 A; T <sub>j</sub> = 25 °C	-	0.4	-	S
Dynamic ch	aracteristics	1				
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = 30 V; I <sub>D</sub> = 0.2 A; V <sub>GS</sub> = 10 V;	-	0.4	0.6	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.03	-	nC
Q <sub>GD</sub>	gate-drain charge	1	-	0.08	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 30 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	15	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	2	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	1.3	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 30 V; $I_{D}$ = 0.2 A; $V_{GS}$ = 10 V;	-	1	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	1	-	ns
t <sub>d(off)</sub>	turn-off delay time	1	-	3	-	ns
t <sub>f</sub>	fall time	1	-	8	-	ns
Source-drai	in diode					
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 0.26 A; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	-	0.8	1.2	V

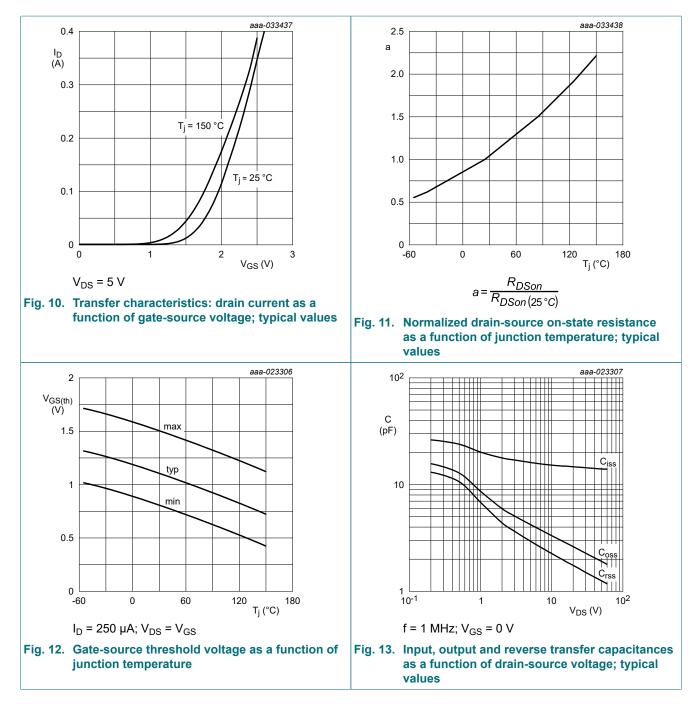
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#### 60 V, N-channel Trench MOSFET



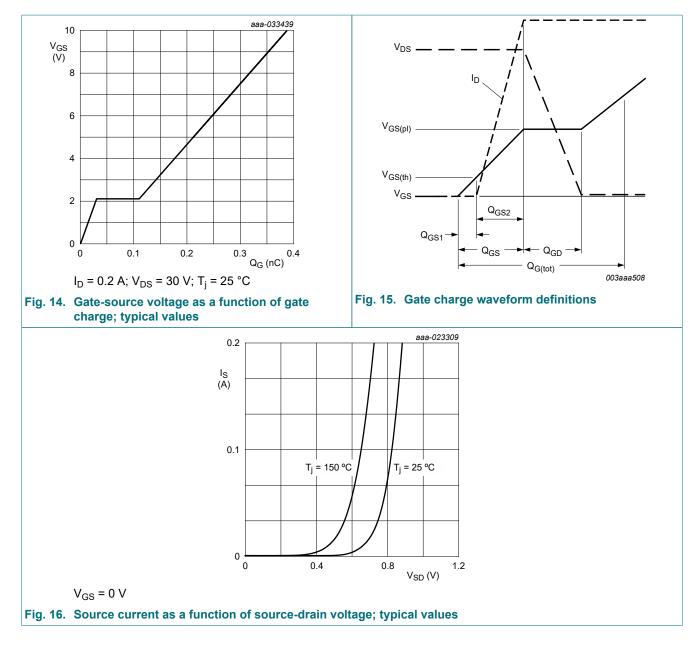
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#### 60 V, N-channel Trench MOSFET

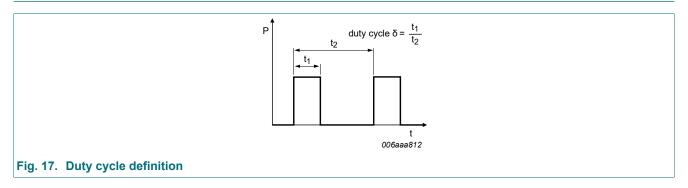


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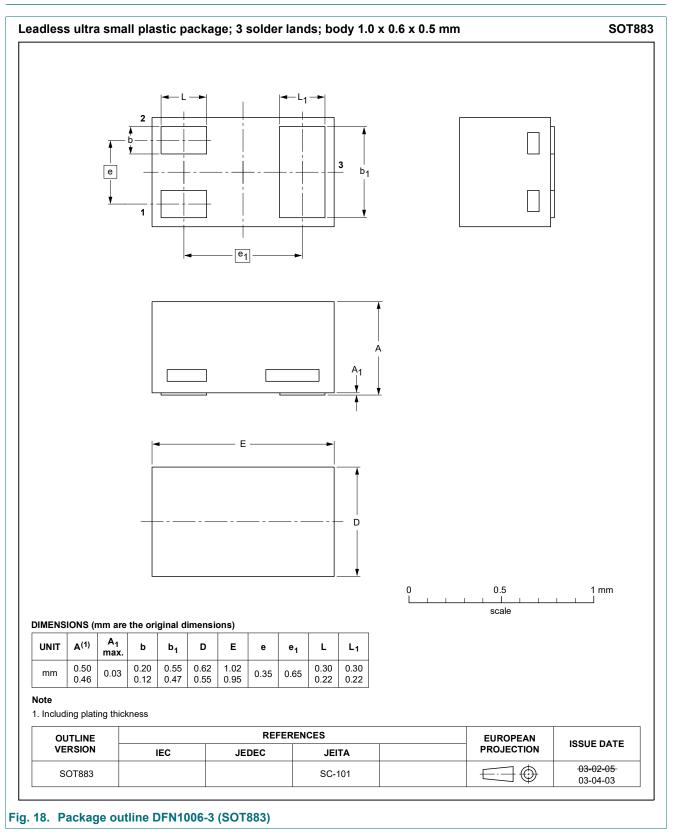
#### 60 V, N-channel Trench MOSFET



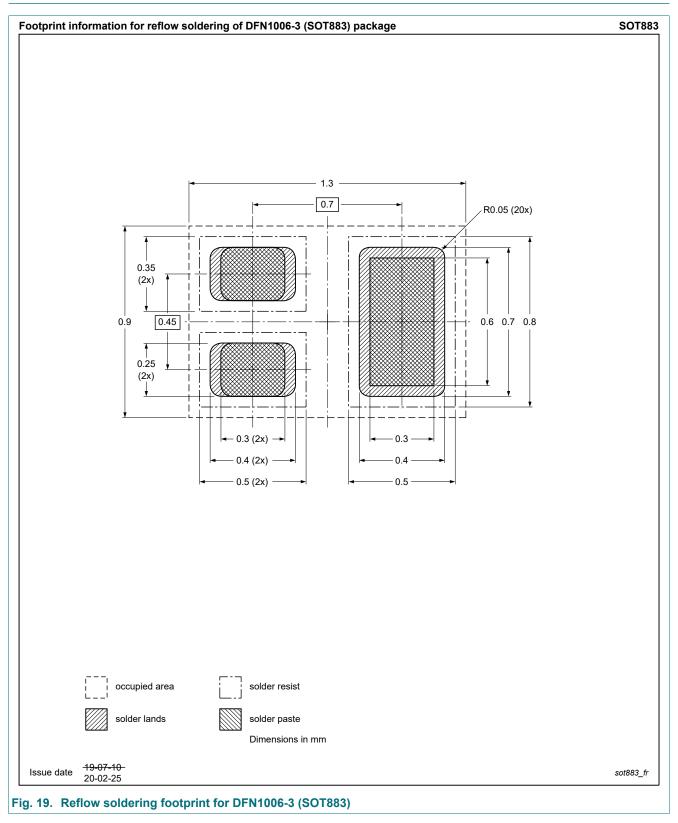
## 11. Test information



## 12. Package outline



## 13. Soldering



# 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
NX138AKM v.1	20210512	Product data sheet	-	-		

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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12 May 2021

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