

BSR43-Q

80 V, 1 A NPN medium power transistor

9 December 2021

Product data sheet

1. General description

NPN general-purpose transistor in a medium power SOT89 (SC-62) Surface-Mounted Device (SMD) plastic package. PNP complement: BSR33-Q.

2. Features and benefits

- High current (max. 1 A)
- Low voltage (max. 80 V)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Linear voltage regulators
- Low-side switches
- Battery-driven devices
- Power management
- MOSFET drivers
- Amplifiers

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	80	V
I _C	collector current		-	-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	2	А
h _{FE}	DC current gain	V_{CE} = 5 V; I _C = 100 μA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _{amb} = 25 °C	30	-	-	
		V_{CE} = 5 V; I _C = 100 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _{amb} = 25 °C	100	-	300	
		V_{CE} = 5 V; I _C = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _{amb} = 25 °C	50	-	-	

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		С
2	С	collector		
3	В	base		B
			SOT89	sym042

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
BSR43-Q	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89			

7. Marking

Table 4. Marking codes	
Type number	Marking code
BSR43-Q	AR4

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	90	V
V _{CEO}	collector-emitter voltage	open base		-	80	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	2	А
I _{BM}	peak base current			-	0.2	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.35	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	93	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	13	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

10. Characteristics

Ì	Table	7.	Charac	teristics
1				

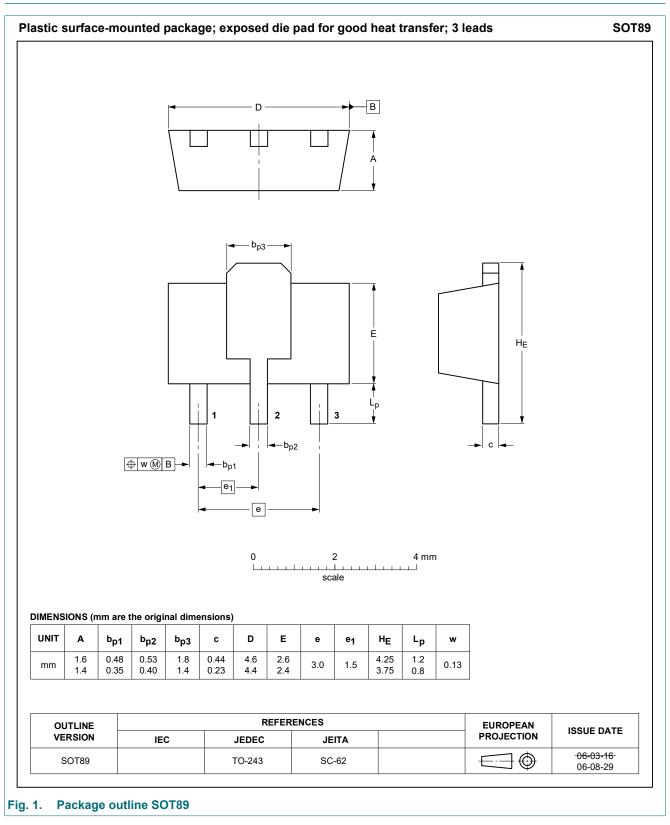
Symbol	Parameter	Conditions	Mir	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 60 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
	current (emitter open)	V _{CB} = 60 V; I _E = 0 A; T _j = 150 °C	-	-	50	μA
I _{EBO}	emitter-base cut-off current (collector open)			-	100	nA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 100 μA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _{amb} = 25 °C	30	-	-	
		V _{CE} = 5 V; I _C = 100 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _{amb} = 25 °C	100	-	300	
		V _{CE} = 5 V; I _C = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _{amb} = 25 °C	50	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = 150 mA; I_B = 15 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.01; T_{amb} = 25 °C	-	-	250	mV
		I_C = 500 mA; I_B = 50 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.01; T_{amb} = 25 °C	-	-	500	mV
V _{BEsat}	base-emitter saturation voltage	I_C = 150 mA; I_B = 15 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.01; T_{amb} = 25 °C	-	-	1	V
		I_{C} = 500 mA; I_{B} = 50 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.01; T_{amb} = 25 °C	-	-	1.2	V
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	12	pF
C _e	emitter capacitance	V _{EB} = 0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	90	pF
f _T	transition frequency	V _{CE} = 10 V; I _C = 50 mA; f = 100 MHz; T _{amb} = 25 °C	100	-	-	MHz
Switching t	imes (between 10% and 90	% levels)				
t _{on}	turn-on time	I _C = 100 mA; I _{Bon} = 5 mA; I _{Boff} = -5 mA;	-	-	250	ns
t _{off}	turn-off time	T _{amb} = 25 °C	-	-	1	μs

11. Test information

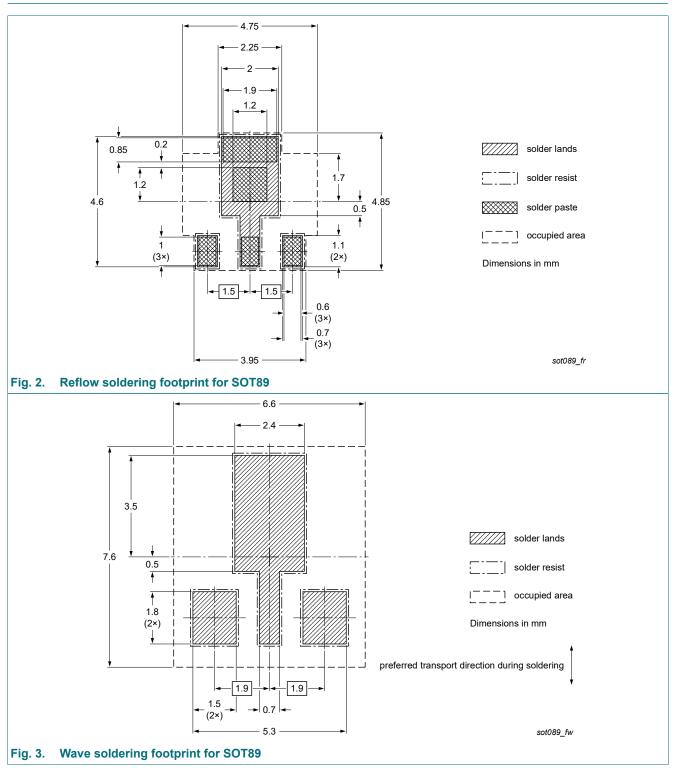
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



Product data sheet

14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
BSR43-Q v.1	20211209	Product data sheet	-	-		

Product data sheet

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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Product data sheet

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