Product data sheet

1. General description

NPN low V_{CEsat} transistor in a SOT89 (SC-62/TO-243) small and flat lead Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS304PX

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- · High efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- AEC-Q101 qualified

3. Applications

- High-voltage DC-to-DC conversion
- · High-voltage MOSFET gate driving
- High-voltage motor control
- · High-voltage power switches (e.g. motors, fans)
- Automotive applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	60	V
I _C	collector current		-	-	4.7	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	9.4	Α
R _{CEsat}	collector-emitter saturation resistance	I_C = 4 A; I_B = 200 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	37	53	mΩ



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter		С
2	С	collector		
3	В	base	3 2 1	B — (
			SOT89	sym123

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PBSS304NX	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89		

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS304NX	%5E

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

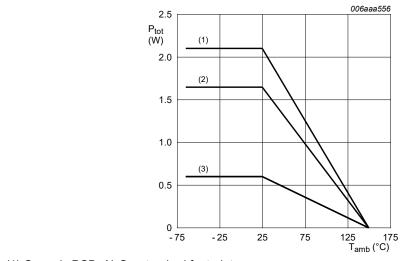
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	60	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
Ic	collector current			-	4.7	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	9.4	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.6	W
			[2]	-	1.65	W
			[3]	-	2.1	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

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- (1) Ceramic PCB, Al₂O₃, standard footprint
- (2) FR4 PCB, mounting pad for collector 6 cm²
- (3) FR4 PCB, standard footprint

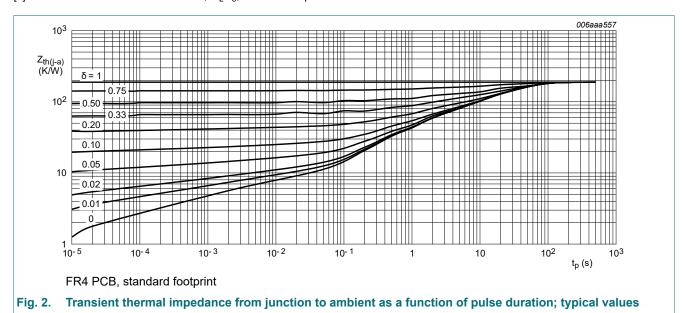
Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance from	nt	[1]	-	-	208	K/W
	junction to ambient		[2]	-	-	76	K/W
			[3]	-	-	60	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	20	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



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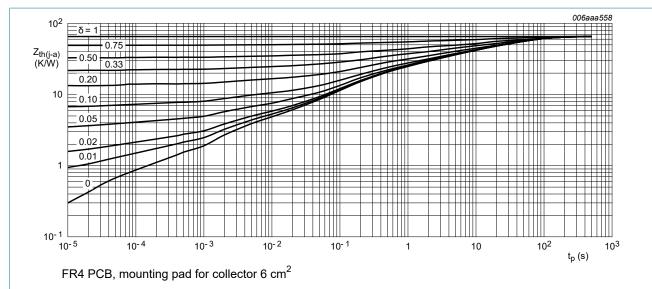


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

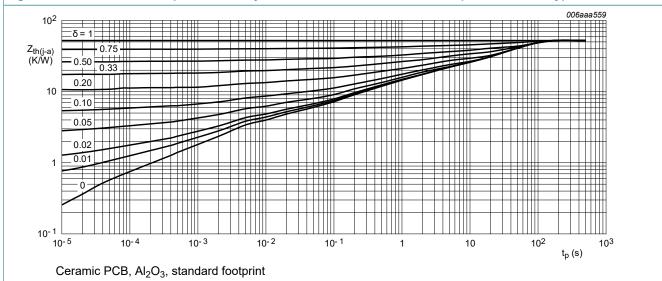


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Tuble 1. Characteriotics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 60 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
		V _{CB} = 60 V; I _E = 0 A; T _j = 150 °C		-	-	50	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		-	-	100	nA

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
h _{FE}	DC current gain	V_{CE} = 2 V; I_{C} = 0.5 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	300	520	-	
		V_{CE} = 2 V; I_{C} = 1 A; pulsed; $t_{p} \le 300 \ \mu s$; $\delta \le 0.02$; T_{amb} = 25 °C	300	500	-	
		V_{CE} = 2 V; I_{C} = 2 A; pulsed; $t_{p} \le 300 \ \mu s$; δ ≤ 0.02; T_{amb} = 25 °C	250	470	-	
		V_{CE} = 2 V; I_{C} = 4 A; pulsed; $t_{p} \le 300 \ \mu s$; δ ≤ 0.02; T_{amb} = 25 °C	150	250	-	
		V_{CE} = 2 V; I_{C} = 6 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	75	115	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = 0.5 A; I_B = 50 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	25	35	mV
		I_C = 1 A; I_B = 50 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	50	70	mV
		I_C = 1 A; I_B = 10 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	85	120	mV
		I_C = 2 A; I_B = 40 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	105	150	mV
		I_C = 4 A; I_B = 200 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	145	210	mV
		I_C = 4 A; I_B = 400 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	140	200	mV
		I_C = 4 A; I_B = 80 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	190	290	mV
		I_C = 4.7 A; I_B = 235 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	170	245	mV
GEGGI	collector-emitter saturation resistance	I_C = 4 A; I_B = 200 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	37	53	mΩ
		I_C = 4 A; I_B = 80 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	48	73	mΩ
V _{BEsat}	base-emitter saturation voltage	I_C = 1 A; I_B = 100 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	0.82	0.9	V
		I_C = 4 A; I_B = 400 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	0.94	1.05	V
V_{BEon}	base-emitter turn-on voltage	V_{CE} = 2 V; I_{C} = 2 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	0.75	0.85	V
t _d	delay time	$V_{CC} = 12.5 \text{ V}; I_C = 3 \text{ A}; I_{Bon} = 0.15 \text{ A};$	-	15	-	ns
t _r	rise time	I _{Boff} = -0.15 A; T _{amb} = 25 °C	-	95	-	ns
t _{on}	turn-on time		-	110	-	ns
·s	storage time		-	360	-	ns
t _f	fall time		-	195	-	ns
t _{off}	turn-off time		-	555	-	ns
fт	transition frequency	V_{CE} = 10 V; I_{C} = 100 mA; f = 100 MHz; T_{amb} = 25 °C	-	130	-	MHz
C _c	collector capacitance	V_{CB} = 10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	48	70	pF

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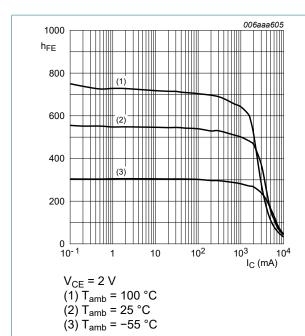


Fig. 5. DC current gain as a function of collector current; typical values

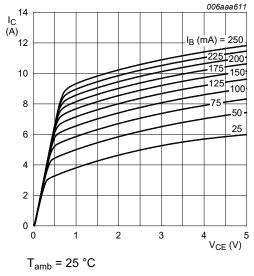


Fig. 6. Collector current as a function of collectoremitter voltage; typical values

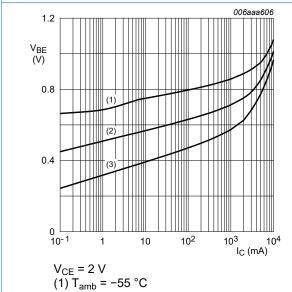
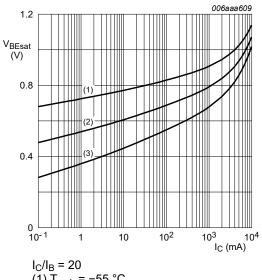


Fig. 7. Base-emitter voltage as a function of collector current; typical values

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = 100 \, ^{\circ}C$



 $I_{C}/I_{B} = 20$ (1) $T_{amb} = -55 \,^{\circ}\text{C}$ (2) $T_{amb} = 25 \,^{\circ}\text{C}$ (3) $T_{amb} = 100 \,^{\circ}\text{C}$

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

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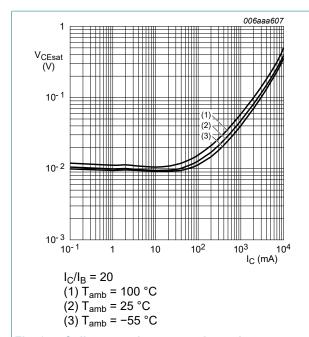


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

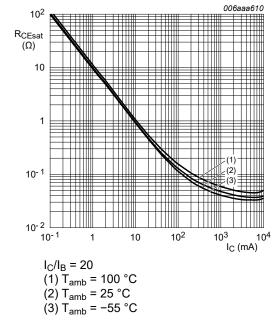


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

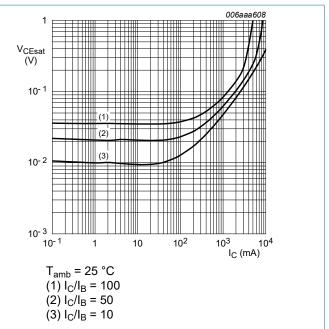


Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values

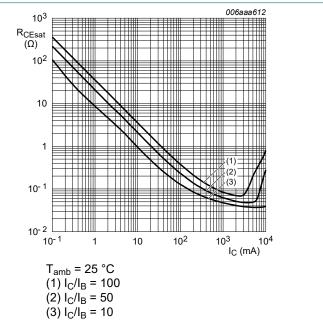
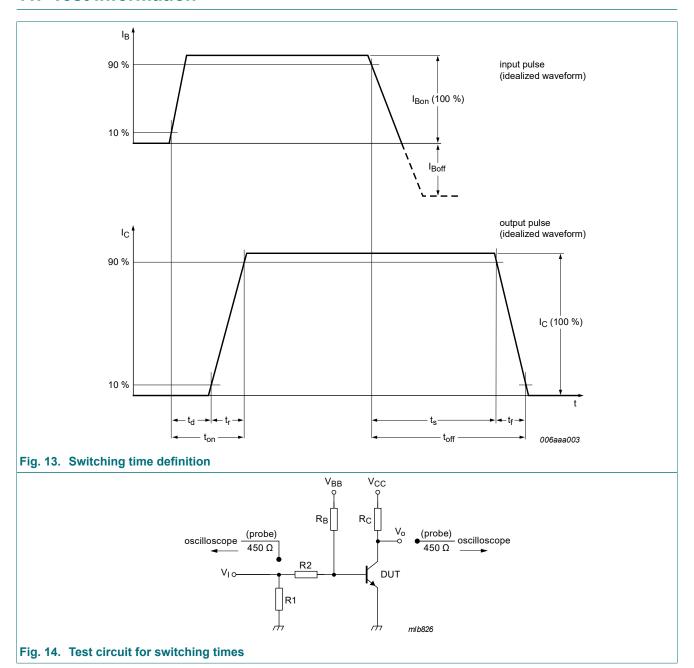


Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values

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11. Test information



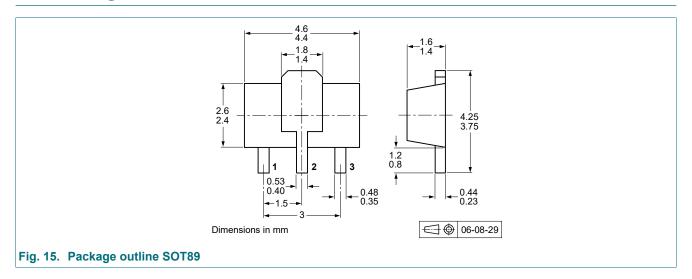
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

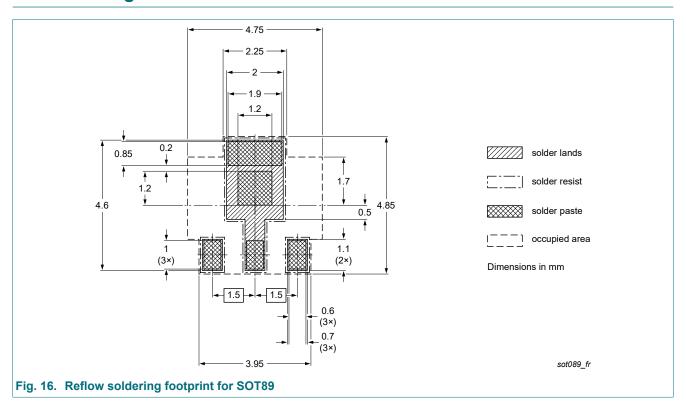
Product data sheet

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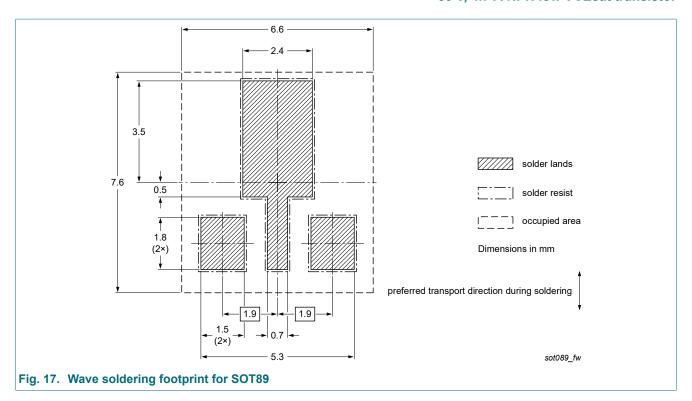
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS304NX v.3	20231109	Product data sheet	-	PBSS304NX_2
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Section "Packing information" removed. 			
PBSS304NX_2	20091120	Product data sheet	-	PBSS304NX_1
PBSS304NX_1	20060816	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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